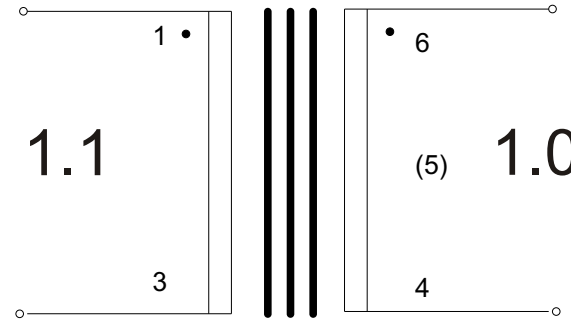
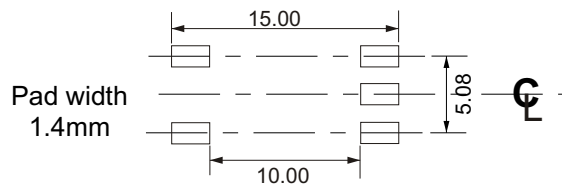
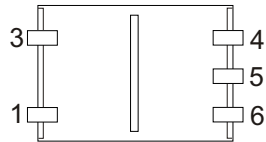
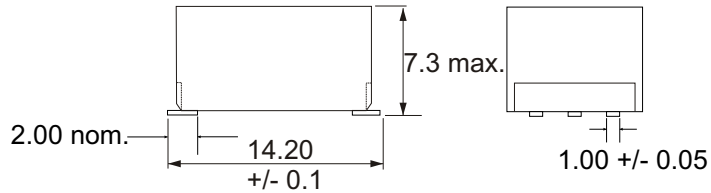


YYWW = date code
 YY = year of manufacture
 WW = week of manufacture

Part number



Electrical schematic



Suggested PCB layout

Electrical specification:

Primary DC resistance: 272 ohms +/- 15%
 Secondary DC resistance: 247.4 ohms +/- 15%
 Inductance (L) @270mVrms, 100Hz, Pins 1 - 3: 9.6H nom.
 Pins 6 - 4: 8.3H nom.

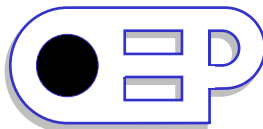
Turns ratio 1.1:1 (@ 6kHz, 0.1Vrms), pins 1 - 3 & 6 - 4: +/- 1%

Hi-pot, primary to secondary: 5kVDC min., 1mA for 1 minute

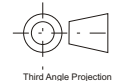
Operating temperature range*: -25°C to +85°C
 Storage temperature range: -40°C to +125°C

* Note: below -10°C the inductance reduces, for example at -25°C, L= 3H nominal.

Do not pass DC current through windings.



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DESCRIPTION	ISSUE	DATE	DRAWN	CHECKED	DRAWING NUMBER
Specification for Q0806E 	1	20/10/04	CS		Q0806E
	4	30/03/06	CS		
	5	15/07/08	CS		
	6	31/10/12	CS		

Scale: nts

All dimensions in mm unless stated otherwise