

Simple Packaged Crystal Oscillator (SPXO)

Highs Frequency
Ceramic Package
LVPECL Out

Pb-free : RoHs compliance



SP-7050LP2/LP3

Typical applications

- SONET,SDH,Gbits,Ethernet,IEEE1394,Fibre Channel.

Features

- 40 to 266 MHz.
- LVPECL / 50Ω Load.
- Package : Metal lid + Ceramic package.
- 7.5×5.0×1.65mm

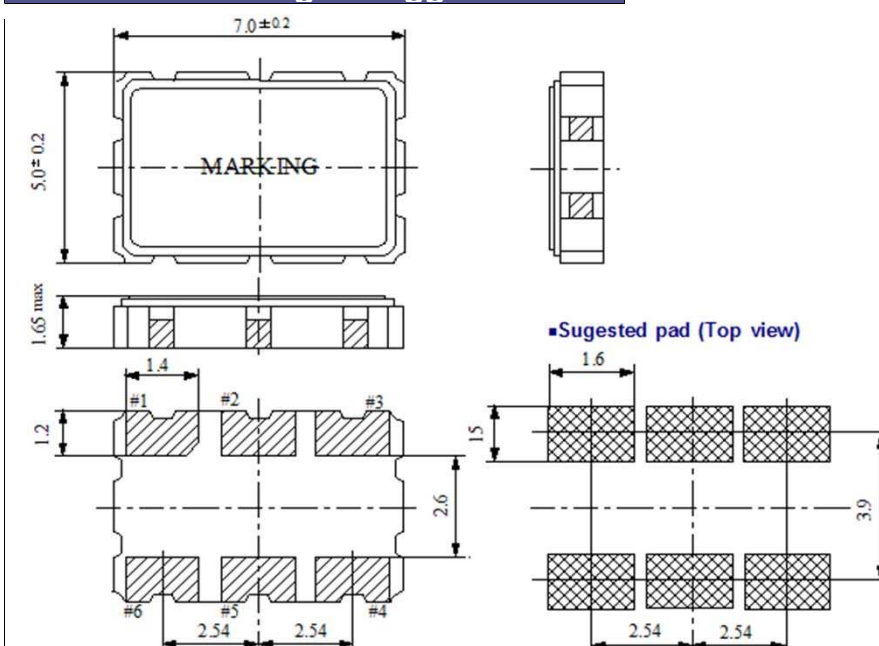
Specifications

Model		SP-7050LP2	SP-7050LP3
Available Frequency Range		40.000 to 266.000 MHz	
Frequency Stability		±30ppm(-10 to +70°C) ,±50 ppm(-30 to+75°C),±100 ppm(-40 to +85°C)	
Operating Temperature		-40 to +85° C	
Storage Temperature		-40 to +85° C	
Power Supply Voltage		2.5V±5%	3.3V±5%
Power Supply Current Consumption		90 mA max.	
		LVPECL	
Output	Level	50 Ω (terminus to Vcc-2V)	
	Load	Vol : Vcc-1.620V max., Voh : Vcc-1.025V min.	
	Output Voltage	1ns max. at output amplitude 20% to 80%	
	Rise & Fall Time	40 to 60% at output amplitude 50% 45 to 55% at output amplitude 50%	
	Duty Cycle	RMS 1σ 5ps max. (3ps typ.)	
Tri state Function		RMS 1ps max. (0.3ps typ) / 12KHz to 20MHz offset	

Environmental Characteristics

Description	Specification
Fall Shock	Test : 3 time free drop onto concrete board from 150cm high Result: ±5.0 ppm max.
Vibration Shock	Test : Freq. 5-500Hz, Amplitude 1.5mm, Test time 4cycles/XYZ axis Result: ±5.0 ppm max.
Solderability	Test : Immersed into solder bath at 235±° C for 3 sec. Result: More than 95%
Humidity	Test : 90 to 95%RH at 60±3° C±500 hours Result: ±5.0 ppm max.
Reflow Heat	Test : 2 times of IR reflow soldering in accordance with reflow temerature profil as per the document No.:TR-0531 Result: ±5.0 ppm max.

Outline Drawing & Suggested Pad



TERMINAL	CONNECTION
#1	CE
#2	NC
#3	GND
#4	OUTPUT
#5	OUTN
#6	Vcc