



# High-/Full-Speed USB 2.0 Switches with High ESD

**MAX4906EF**

## General Description

The MAX4906EF are electrostatic discharge (ESD)-protected analog switches that combine low on-capacitance ( $C_{ON}$ ) and low on-resistance ( $R_{ON}$ ) necessary for high-performance switching applications. The  $COM_{-}$  inputs are protected against  $\pm 15kV$  ESD without latchup or damage. The device is designed for USB 2.0 high-speed applications at 480Mbps. The switches also handle all the requirements for USB low- and full-speed signaling.

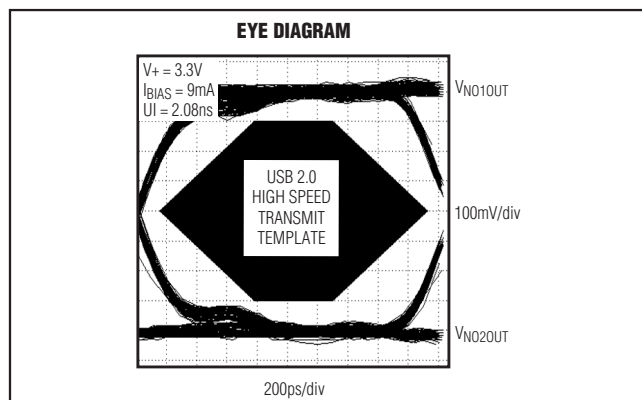
The MAX4906EF features two single-pole/double-throw (SPDT) switches. The device is fully specified to operate from a single +2.7V to +3.6V power supply and is protected against a +5.5V short to all analog inputs ( $COM_{-}$ ,  $NC_{-}$ ,  $NO_{-}$ ). This feature makes the MAX4906EF fully compliant with the USB 2.0 specification of +5.5V fault protection. The device features a low threshold voltage and a +1.4V  $V_{IH}$ , permitting them to be used with low-voltage logic. The device features a  $\overline{QP}$  input that when driven high, turns the charge pump off and sets the device in standby mode. When the device is in standby mode, the quiescent supply current is reduced to 3 $\mu A$  (max) and the switches remain operable.

The MAX4906EF is available in a space-saving, 2mm x 2mm  $\mu DFN$  package and operates over a -40°C to +85°C temperature range.

## Applications

USB Switching	Relay Replacements
Cell Phones	Ethernet Switching
PDA's	Video Switching
Digital Still Cameras	Bus Switches
GPS	T3/E3 Switches for
Notebook Computers	Redundancy Protection

## Typical Operating Characteristics



## Features

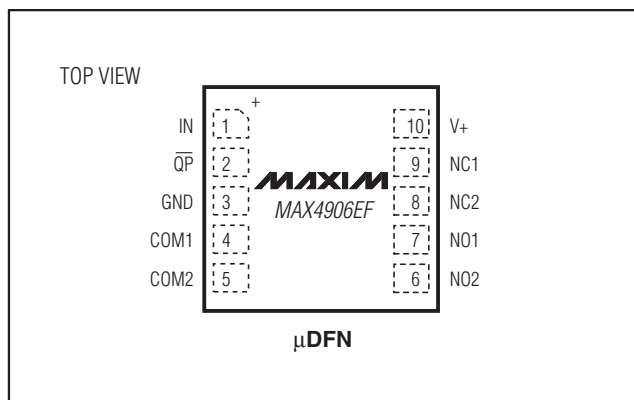
- ◆  $\pm 15kV$  (Human Body Model) ESD Protection, on  $COM_{-}$
- ◆ Fully Specified for a Single +2.7V to +3.6V Power-Supply Voltage
- ◆ Low 4 $\Omega$  (typ), 7 $\Omega$  (max) On-Resistance ( $R_{ON}$ )
- ◆ -3dB Bandwidth: 500MHz (typ)
- ◆ Low Bit-to-Bit Skew  $\leq 20ps$
- ◆ Charge-Pump Noise = 90 $\mu V$  (typ)
- ◆ Charge-Pump Enable
- ◆ No Need for Logic-Level Shifters for 1.4V or Above
- ◆  $COM_{-}$  Analog Inputs Fault-Protected Against Shorts to USB Supply Rail Up to +5.5V
- ◆ Low Supply Current 3 $\mu A$  (max) in Standby
- ◆ Space-Saving 10-Pin, 2mm x 2mm  $\mu DFN$  Package

## Ordering Information

PART	TEMP RANGE	PIN-PACKAGE	TOP MARK
MAX4906EFELB+T	-40°C to +85°C	10 $\mu DFN$	AAJ

+ Denotes a lead(Pb)-free/RoHS-compliant package.  
T = Tape and reel.

## Pin Configuration



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## ABSOLUTE MAXIMUM RATINGS

(All voltages referenced to GND.)

V+	.....-0.3V to +4V
IN, $\overline{QP}$ (Note 1)	.....-0.3V to +4V
COM_, NO_, NC_	.....-0.3V to +5.5V
Continuous Current (COM_ to NO_/NC_)	.....±120mA
Peak Current, (COM_ to NO_/NC_) (pulsed at 1ms 10% duty cycle)	.....±240mA

Continuous Power Dissipation ( $T_A = +70^\circ\text{C}$ )

10-Pin $\mu\text{DFN}$ (derate 5.0mW/ $^\circ\text{C}$ above $+70^\circ\text{C}$ )	.....403mW
Operating Temperature Range	..... $-40^\circ\text{C}$ to $+85^\circ\text{C}$
Junction Temperature	..... $+150^\circ\text{C}$
Storage Temperature Range	..... $-65^\circ\text{C}$ to $+150^\circ\text{C}$
Lead Temperature (soldering, 10s)	..... $+300^\circ\text{C}$

**Note 1:** Signals on IN,  $\overline{QP}$  exceeding GND are clamped by internal diodes. Limit forward-diode current to maximum current rating.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## ELECTRICAL CHARACTERISTICS

( $V_+ = +2.7\text{V}$  to  $+3.6\text{V}$ ,  $T_A = T_{\text{MIN}}$  to  $T_{\text{MAX}}$ , charge-pump enabled, unless otherwise noted. Typical values are at  $V_+ = 3.3\text{V}$ ,  $T_A = +25^\circ\text{C}$ .) (Note 2)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
ANALOG SWITCH							
Analog Signal Range	$V_{\text{COM\_}}, V_{\text{NO\_}}, V_{\text{NC\_}}$	$\overline{\text{QP}}$ = GND or V+ (Note 3)		0		3.6	V
Fault-Protection Trip Threshold (Note 4)	$V_{\text{FP}}$			3.62	3.9	4.20	V
On-Resistance, Charge-Pump Enabled	$R_{\text{ON}}$	$V_+ = 2.7\text{V}$ , $I_{\text{COM\_}} = -10\text{mA}$ , $V_{\text{COM\_}} = 0\text{V}, 1.5\text{V}$ , $\overline{\text{QP}}$ = GND	$T_A = +25^\circ\text{C}$		3.8	5	$\Omega$
			$T_A = T_{\text{MIN}}$ to $T_{\text{MAX}}$			6	
		$V_+ = 2.7\text{V}$ , $I_{\text{COM\_}} = -10\text{mA}$ , $V_{\text{COM\_}} = 2.7\text{V}$ , $\overline{\text{QP}}$ = GND	$T_A = +25^\circ\text{C}$		4	7	$\Omega$
			$T_A = T_{\text{MIN}}$ to $T_{\text{MAX}}$			8	
On-Resistance, Charge-Pump Disabled	$R_{\text{ON}}$	$V_+ = 3.0\text{V}$ , $I_{\text{COM\_}} = -10\text{mA}$ , $V_{\text{COM\_}} = 0\text{V}, 1.5\text{V}$ , $\overline{\text{QP}}$ = V+	$T_A = +25^\circ\text{C}$		5	12	$\Omega$
			$T_A = T_{\text{MIN}}$ to $T_{\text{MAX}}$			13	
		$V_+ = 2.7\text{V}$ , $I_{\text{COM\_}} = -10\text{mA}$ , $V_{\text{COM\_}} = 0\text{V}, 1.5\text{V}$ , $\overline{\text{QP}}$ = V+	$T_A = +25^\circ\text{C}$		8	15	
			$T_A = T_{\text{MIN}}$ to $T_{\text{MAX}}$			17	
On-Resistance Match Between Channels	$\Delta R_{\text{ON}}$	$V_+ = 2.7\text{V}$ , $I_{\text{COM\_}} = -10\text{mA}$ , $V_{\text{COM\_}} = 0\text{V}, 1.5\text{V}, 2.7\text{V}$ (Note 5)	$T_A = +25^\circ\text{C}$		0.5	0.8	$\Omega$
			$T_A = T_{\text{MIN}}$ to $T_{\text{MAX}}$			1.0	
On-Resistance Flatness	$R_{\text{FLAT(ON)}}$	$V_+ = 2.7\text{V}$ , $I_{\text{COM\_}} = -10\text{mA}$ , $V_{\text{COM\_}} = 0\text{V}, 1.5\text{V}$ (Note 6)			0.5		$\Omega$

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## ELECTRICAL CHARACTERISTICS (continued)

(V+ = +2.7V to +3.6V, T<sub>A</sub> = T<sub>MIN</sub> to T<sub>MAX</sub>, charge-pump enabled, unless otherwise noted. Typical values are at V+ = 3.3V, T<sub>A</sub> = +25°C.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Off-Leakage Current	I <sub>NC_</sub> , I <sub>NO_</sub> (OFF)	V+ = 3.6V, V <sub>COM_</sub> = 0.3V, 3.3V; V <sub>NO_</sub> or V <sub>NC_</sub> = 3.3V, 0.3V	-1		+1	μA
On-Leakage Current	I <sub>NC_</sub> , I <sub>NO_</sub> (ON)	V+ = 3.6V, V <sub>COM_</sub> = 0.3V, 3.3V; V <sub>NO_</sub> or V <sub>NC_</sub> = 0.3V, 3.3V, or unconnected	-1		+1	μA
<b>SWITCH AC PERFORMANCE</b>						
On-Channel -3dB Bandwidth	BW	R <sub>L</sub> = R <sub>S</sub> = 50Ω, signal = 0dBm, Figure 1		500		MHz
Off-Isolation	V <sub>ISO</sub>	f = 10MHz; V <sub>NO_</sub> , V <sub>NC_</sub> = 1VP-P; R <sub>L</sub> = R <sub>S</sub> = 50Ω, Figure 1		-60		dB
		f = 250MHz; V <sub>NO_</sub> , V <sub>NC_</sub> = 1VP-P; R <sub>L</sub> = R <sub>S</sub> = 50Ω, Figure 1		-32		
Crosstalk (Note 7)	V <sub>CT</sub>	f = 10MHz; V <sub>NO_</sub> , V <sub>NC_</sub> = 1VP-P; R <sub>L</sub> = R <sub>S</sub> = 50Ω, Figure 1		-59		dB
		f = 250MHz; V <sub>NO_</sub> , V <sub>NC_</sub> = 1VP-P; R <sub>L</sub> = R <sub>S</sub> = 50Ω, Figure 1		-31		
Charge-Pump Noise (Note 8)	V <sub>QP</sub>	Any input or output switch terminal = 50Ω		90		μV
<b>SWITCH DYNAMICS</b>						
NO_, NC_, COM_ Off-Capacitance (Note 9)	C <sub>(OFF)</sub>	f = 1MHz, Figure 2		9	10	pF
NO_, NC_, COM_ On-Capacitance (Note 9)	C <sub>(ON)</sub>	f = 1MHz, Figure 2		10	12	pF
Switch On-Capacitance Matching (Note 9)	C <sub>ONM</sub>	f = 1MHz		0.4		pF
Turn-On Time	t <sub>ON</sub>	V <sub>NO_</sub> , V <sub>NC_</sub> = 1.5V; R <sub>L</sub> = 300Ω, C <sub>L</sub> = 35pF, V <sub>IH</sub> = V+, V <sub>IL</sub> = 0V, QP = GND, Figure 3		1.4		ns
Turn-Off Time	t <sub>OFF</sub>	V <sub>NO_</sub> , V <sub>NC_</sub> = 1.5V; R <sub>L</sub> = 300Ω, C <sub>L</sub> = 35pF, V <sub>IH</sub> = V+, V <sub>IL</sub> = GND, QP = GND, Figure 3		35		ns
Propagation Delay	t <sub>PLH_</sub> , t <sub>PHL_</sub>	R <sub>L</sub> = R <sub>S</sub> = 50Ω, Figure 4		0.2		ns
Fault-Protection Response Time	t <sub>FP</sub>	V <sub>COM_</sub> = 0 to 5V step, R <sub>L</sub> = R <sub>S</sub> = 50Ω, C <sub>L</sub> = 10pF, Figure 5		1		μs
Fault-Protection Recovery Time	t <sub>FPR</sub>	V <sub>COM_</sub> = 5V to 3V step, R <sub>L</sub> = R <sub>S</sub> = 50Ω, C <sub>L</sub> = 10pF, Figure 5		1		μs
Output Skew Between Switches (Note 9)	t <sub>SK(o)</sub>	Skew between switch 1 and switch 2, R <sub>L</sub> = R <sub>S</sub> = 50Ω, Figure 4		20	100	ps
Output Skew Same Switch (Note 9)	t <sub>SK(p)</sub>	Skew between opposite transitions in same switch, R <sub>L</sub> = R <sub>S</sub> = 50Ω, Figure 4		5	100	ps

# High-/Full-Speed USB 2.0 Switches with High ESD

## ELECTRICAL CHARACTERISTICS (continued)

( $V_+ = +2.7V$  to  $+3.6V$ ,  $T_A = T_{MIN}$  to  $T_{MAX}$ , charge-pump enabled, unless otherwise noted. Typical values are at  $V_+ = 3.3V$ ,  $T_A = +25^\circ C$ .) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Total Harmonic Distortion Plus Noise	THD+N	$V_{COM\_} = 2V_{P-P}$ , $R_L = 600\Omega$ , $f = 20Hz$ to $20kHz$		0.01		%
Charge Injection	Q	$V_{GEN} = 1.5V$ , $R_{GEN} = 0\Omega$ , $C_L = 100pF$ , Figure 6		20		pC
<b>SWITCH LOGIC</b>						
Logic-Input Voltage Low	$V_{IL}$				0.4	V
Logic-Input Voltage High	$V_{IH}$		1.4			V
Input-Logic Hysteresis	$V_{HYST}$			100		mV
Input Leakage Current	$I_{IN}$	$V_+ = 3.6V$ , $V_{IN} = 0$ or $V_+$	-1		+1	$\mu A$
Operating Supply-Voltage Range	$V_+$		2.7		3.6	V
Quiescent Supply Current	$I_+$	$V_+ = 3.6V$ , $V_{IN} = 0$ or $V_+$ , $\overline{QP} = GND$		160	1000	$\mu A$
Quiescent Supply Current With Charge-Pump Disabled	$I_+$	$V_+ = 3.6V$ , $V_{IN} = 0$ or $V_+$ , $\overline{QP} = V_+$			3	$\mu A$
<b>ESD PROTECTION</b>						
COM <sub>-</sub>		Human Body Model		$\pm 15$		kV

**Note 2:** All units are 100% production tested at  $T_A = +25^\circ C$ . Limits over the operating temperature range are guaranteed by design and not production tested.

**Note 3:** The switch will turn off for voltages above ( $V_{FP}$ ); therefore, protecting downstream circuits in case of a fault condition.

**Note 4:** Fault-protection trip threshold limits are not production tested; guaranteed by design.

**Note 5:**  $\Delta R_{ON(MAX)} = |R_{ON(CH1)} - R_{ON(CH2)}|$

**Note 6:** Flatness is defined as the difference between the maximum and minimum value of on-resistance, as measured over specified analog signal ranges.

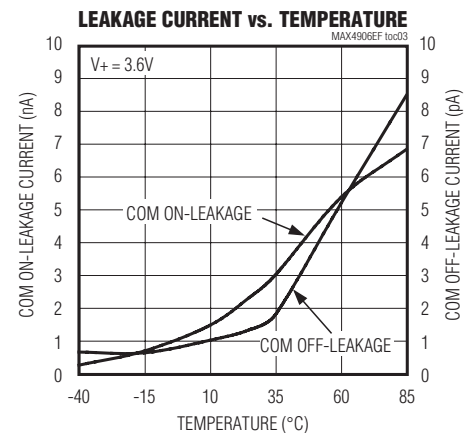
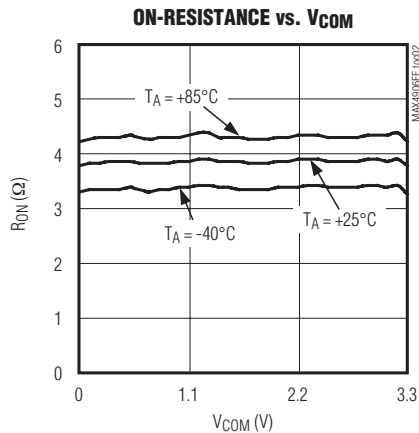
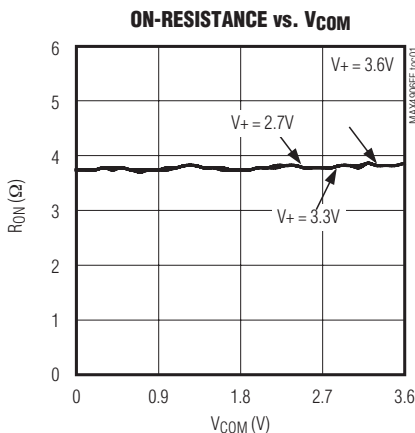
**Note 7:** Between any two switches.

**Note 8:** Noise specification is measured peak to peak.

**Note 9:** Switch off-capacitance, switch on-capacitance, output skew between switches, and output skew same-switch limits are not production tested; design guaranteed by correlation.

## Typical Operating Characteristics

( $V_+ = 3.3V$ ,  $T_A = +25^\circ C$ , unless otherwise noted.)

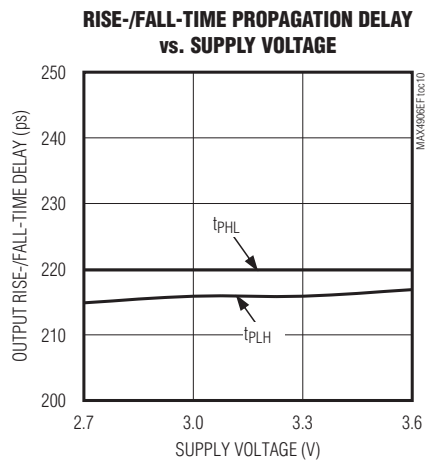
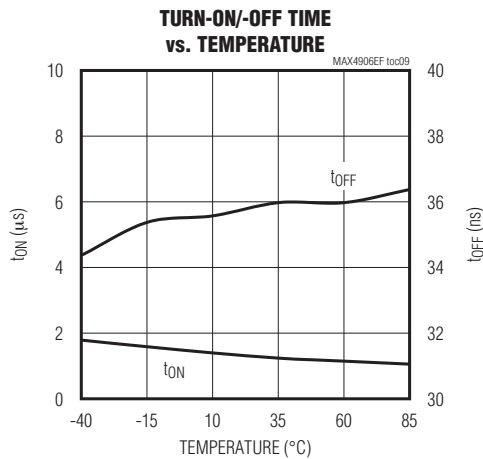
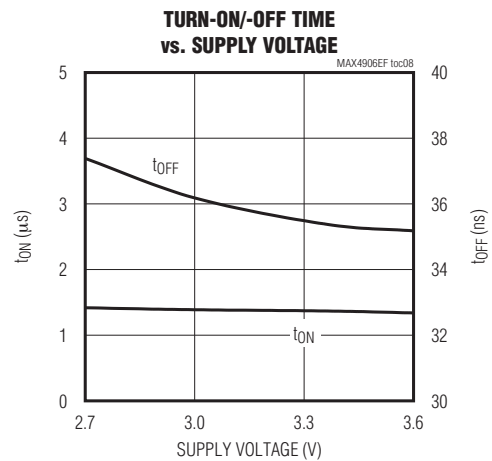
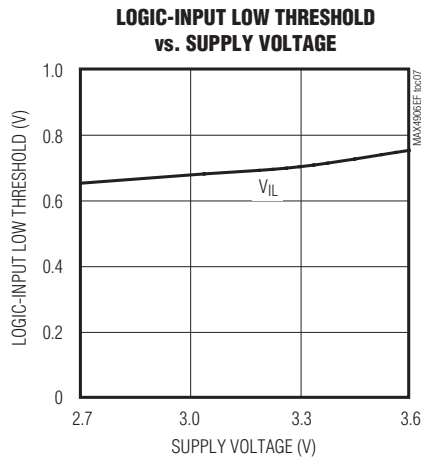
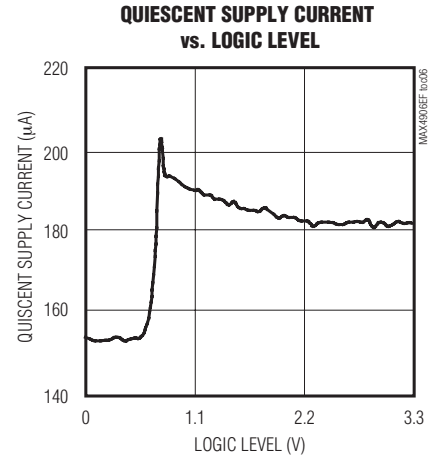
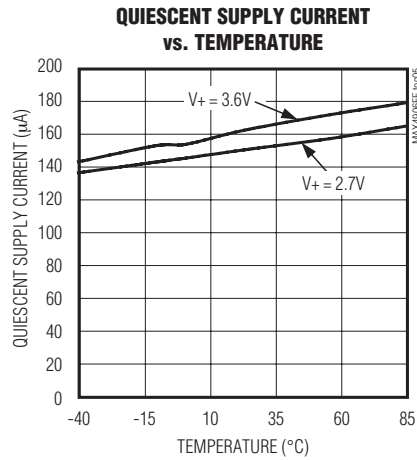
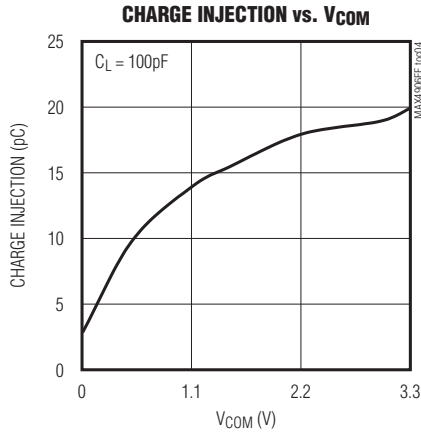


# High-/Full-Speed USB 2.0 Switches with High ESD

MAX4906EF

## Typical Operating Characteristics (continued)

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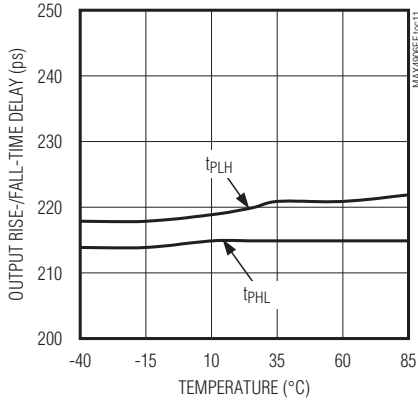


# High-/Full-Speed USB 2.0 Switches with High ESD

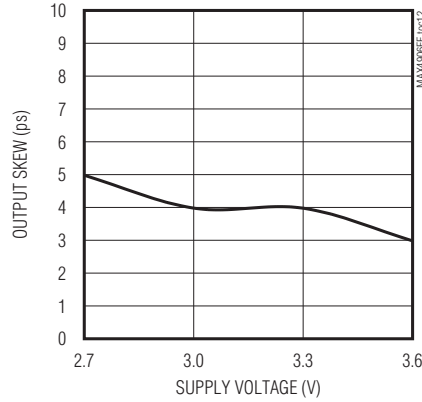
## Typical Operating Characteristics (continued)

(V+ = 3.3V, T<sub>A</sub> = +25°C, unless otherwise noted.)

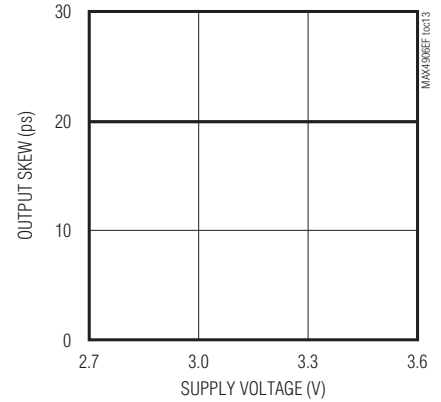
**RISE/FALL-TIME PROPAGATION DELAY vs. TEMPERATURE**



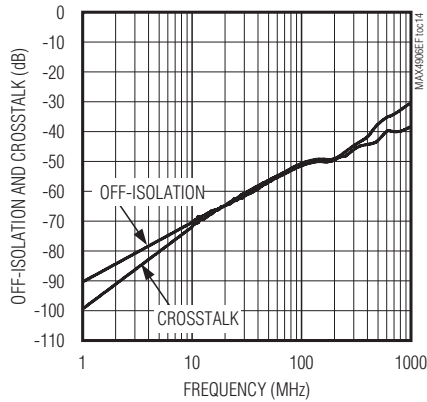
**SAME SWITCH OUTPUT SKEW vs. SUPPLY VOLTAGE**



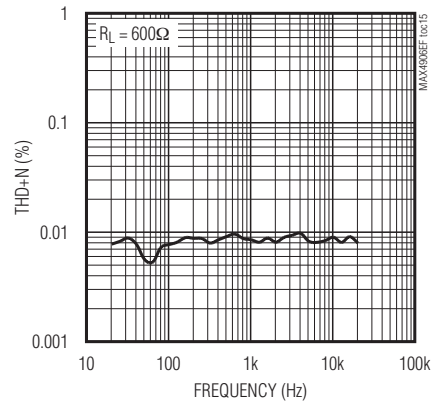
**OUTPUT SKEW BETWEEN SWITCHES vs. SUPPLY VOLTAGE**



**FREQUENCY RESPONSE**



**TOTAL HARMONIC DISTORTION PLUS NOISE vs. FREQUENCY**



# High-/Full-Speed USB 2.0 Switches with High ESD

MAX4906EF

## Pin Description

PIN	NAME	FUNCTION
1	IN	Digital Control Input. IN controls switch 1 and switch 2.
2	$\overline{QP}$	Charge-Pump Enable Input. Drive $\overline{QP}$ high to turn charge pump off. For normal operation, drive $\overline{QP}$ low.
3	GND	Ground
4	COM1	Analog Switch 1—Common Terminal
5	COM2	Analog Switch 2—Common Terminal
6	NO2	Analog Switch 2—Normally Open Terminal
7	NO1	Analog Switch 1—Normally Open Terminal
8	NC2	Analog Switch 2—Normally Closed Terminal
9	NC1	Analog Switch 1—Normally Closed Terminal
10	V+	Positive-Supply Voltage Input. Connect V+ to a +2.7V to +3.6V supply voltage. Bypass V+ to GND with a 0.1 $\mu$ F capacitor.

## Test Circuits/Timing Diagrams

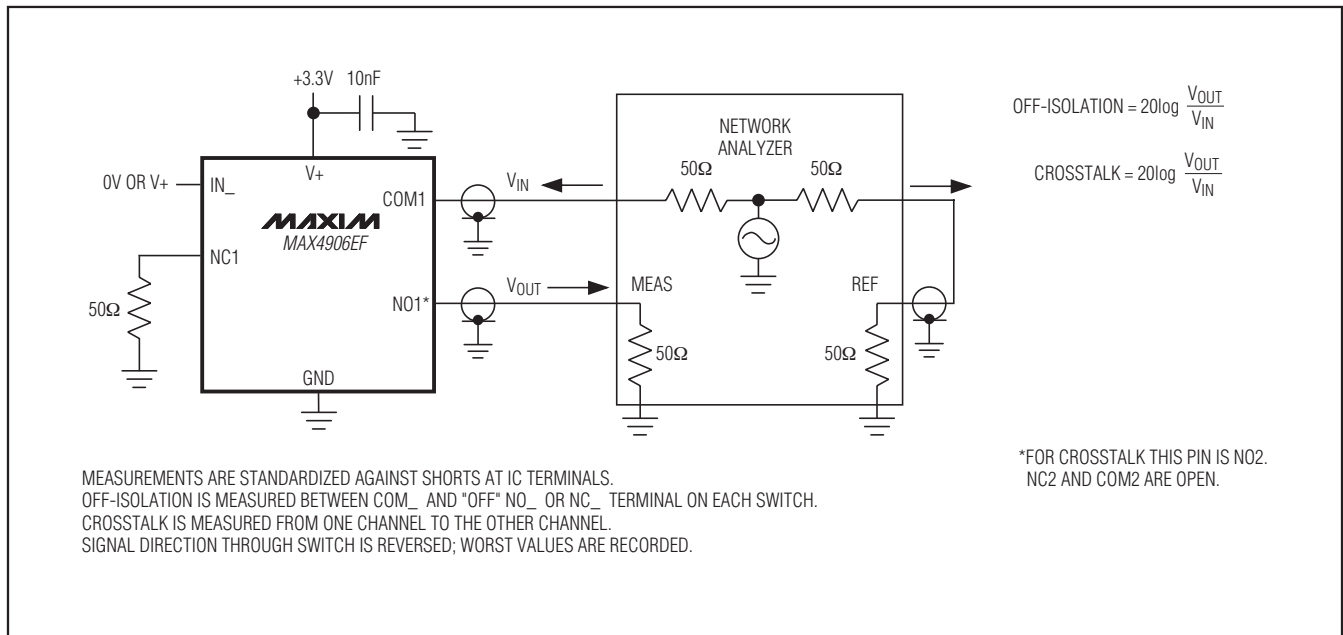


Figure 1. Off-Isolation and Crosstalk

# High-/Full-Speed USB 2.0 Switches with High ESD

## Test Circuits/Timing Diagrams (continued)

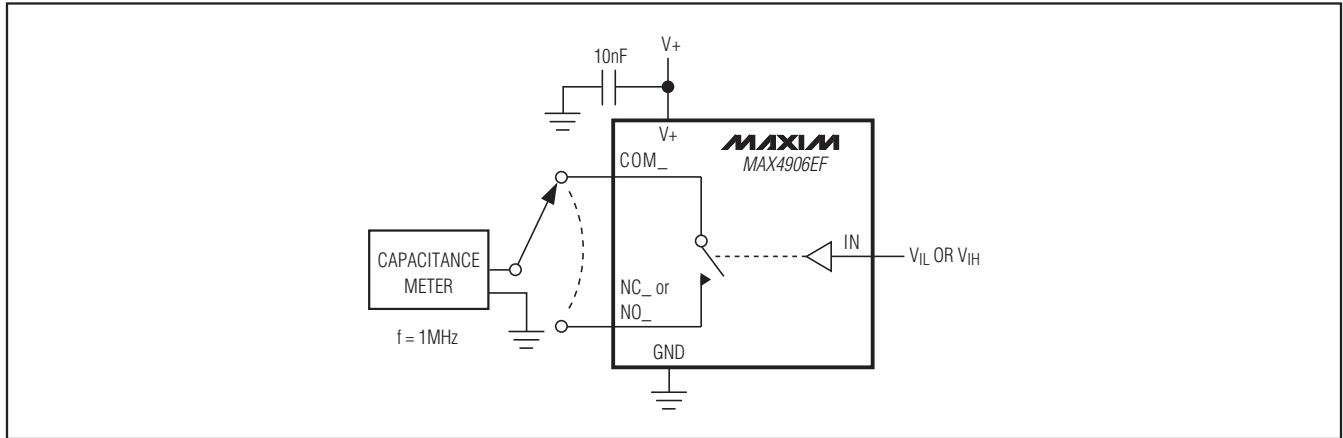


Figure 2. Channel Off-/On-Capacitance

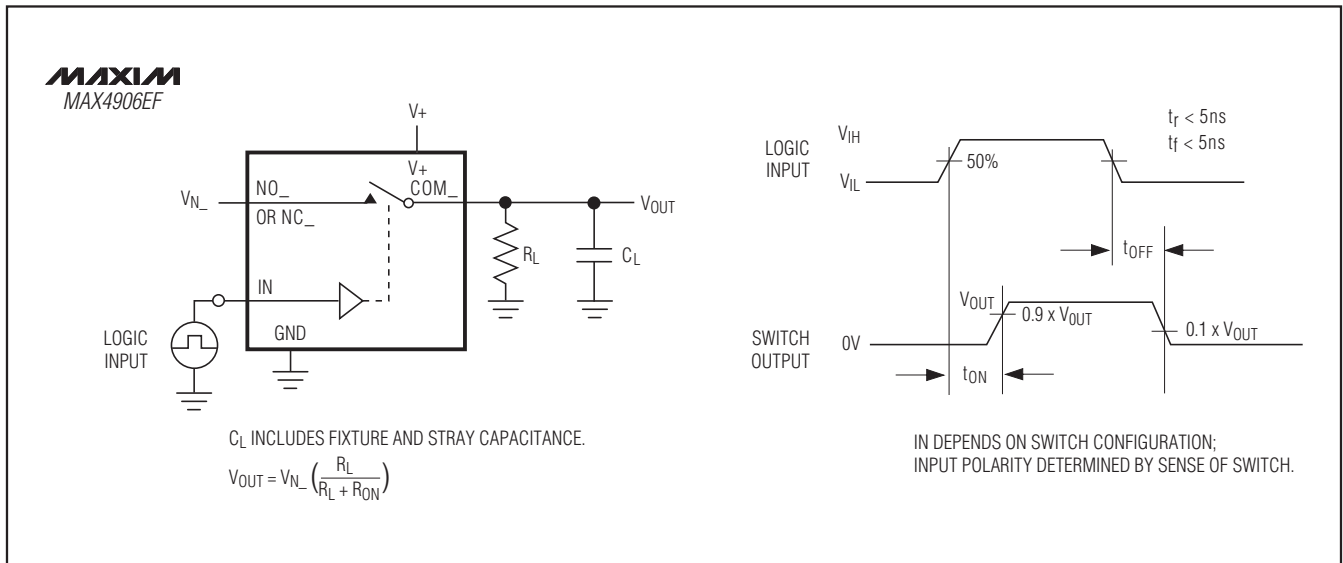


Figure 3. Switching Time



# High-/Full-Speed USB 2.0 Switches with High ESD

## Test Circuits/Timing Diagrams (continued)

MAX4906EF

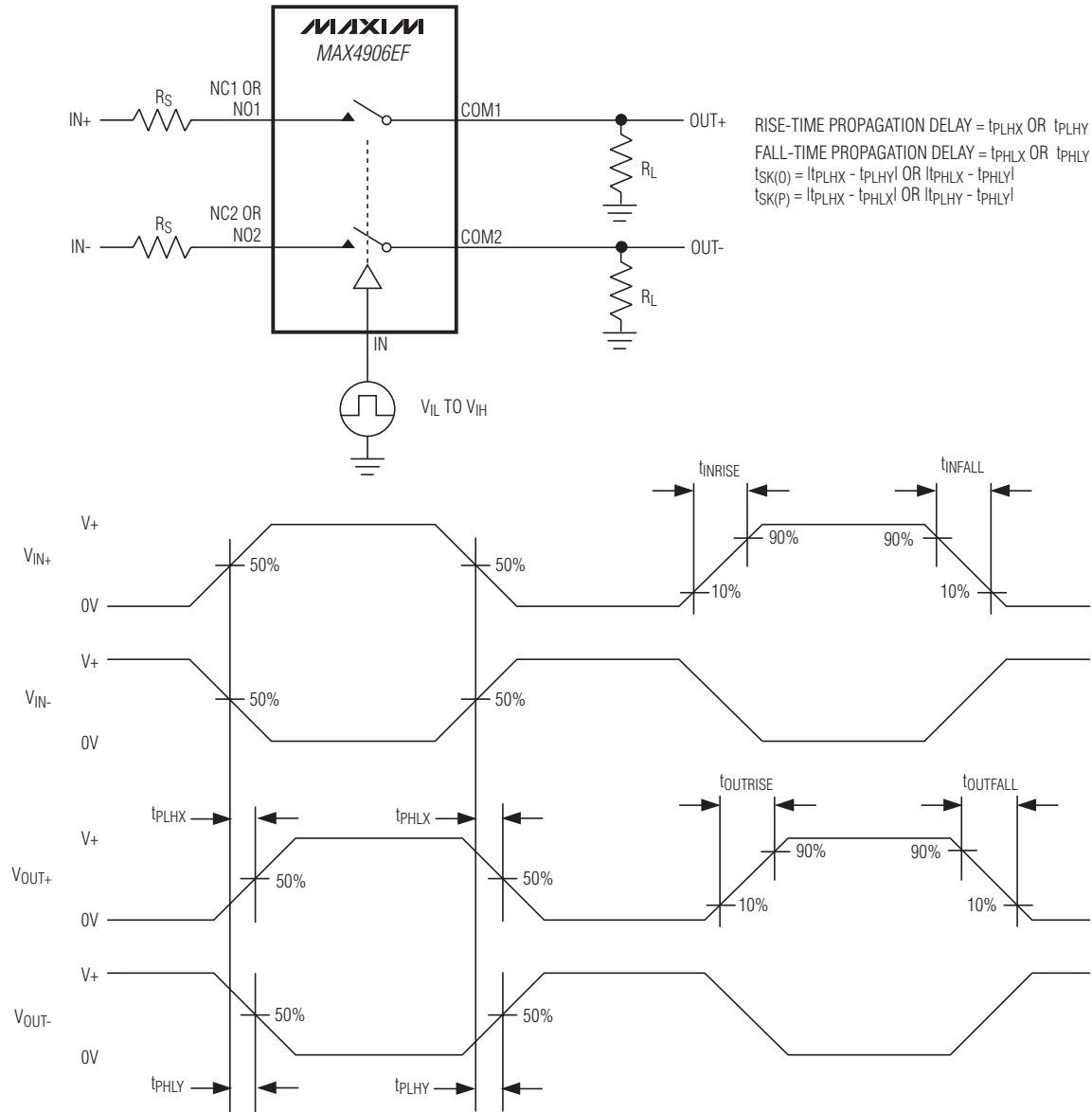


Figure 4. Output Signal Skew, Rise/Fall Time, Propagation Delay

# High-/Full-Speed USB 2.0 Switches with High ESD

## Test Circuits/Timing Diagrams (continued)

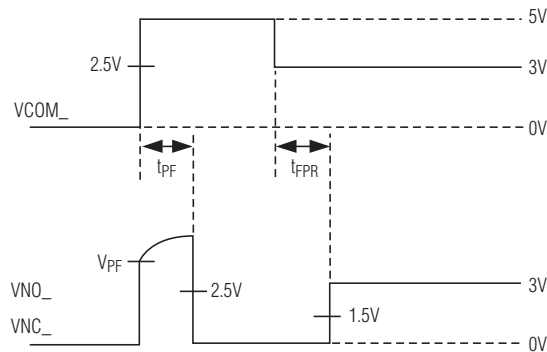


Figure 5. MAX4906EF Fault-Protection Response/Recovery Time

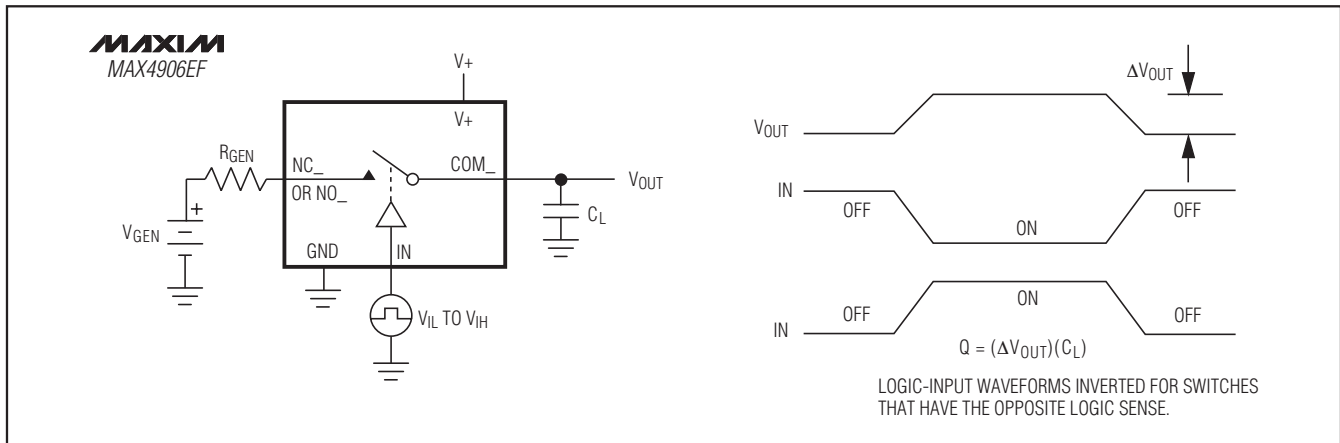


Figure 6. Charge Injection

## Detailed Description

The MAX4906EF are ESD-protected analog switches where the COM\_ inputs are further protected up to  $\pm 15\text{kV}$  ESD without latchup or damage. The device is targeted for USB 2.0 high-speed (480Mbps) switching applications. The device still meets USB low- and full-speed requirements and is suitable for 10/100 Ethernet switching. The MAX4906EF features two SPDT switches.

The MAX4906EF is fully specified to operate from a single +2.7V to +3.6V supply and is +5.5V fault protected.

When operating from a +2.7V to +3.6V supply, the low threshold of the device permits them to be used with logic levels as low as 1.4V. The MAX4906EF is based on a charge-pump-assisted n-channel architecture and thus operate at 170 $\mu\text{A}$  (max) quiescent current. The device features a standby mode to reduce the quiescent current to less than 3 $\mu\text{A}$  (max).

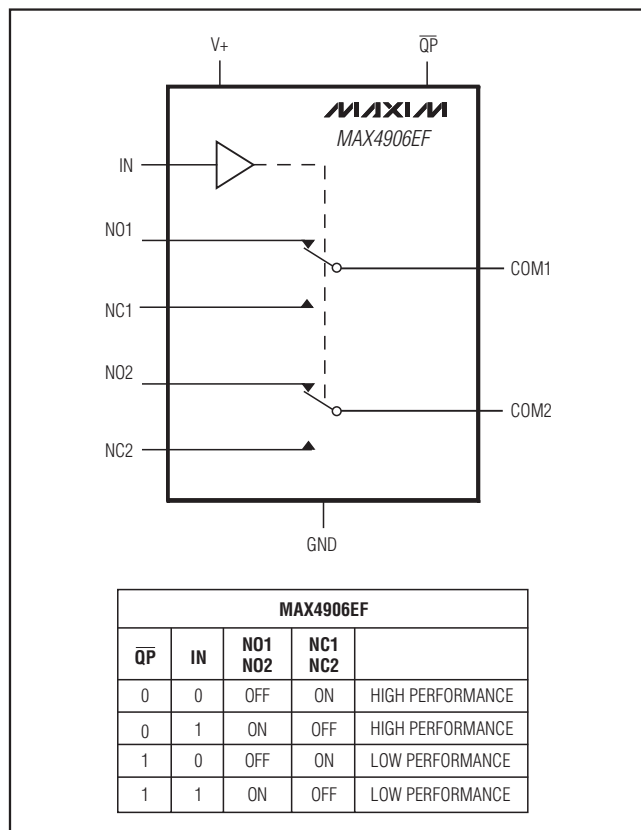
## Digital Control Input

The MAX4906EF provides a single-digit control logic input, IN. IN controls the position of the switches as shown in the *Functional Diagram/Truth Table*. Driving IN

# High-/Full-Speed USB 2.0 Switches with High ESD

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## Functional Diagram/Truth Table



rail-to-rail minimizes power consumption. With a +2.7V to +3.6V supply voltage range, the device is +1.4V logic compatible.

### Analog Signal Levels

The on-resistance of the MAX4906EF is very low and stable as the analog input signals are swept from ground to V+ (see the *Typical Operating Characteristics*). These switches are bidirectional, allowing NO\_, NC\_, and COM\_ to be configured as either inputs or outputs.

### Overvoltage Fault Protection

The MAX4906EF features +5.5V fault protection to all analog inputs. Fault protection prevents these switches from being damaged due to shorts to the USB bus voltage rail.

### Charge-Pump Enable

The MAX4906EF features a charge-pump enable mode that improves the performance and the dynamic range of the device. The device features a  $\overline{QP}$  input that when driven high, turns the charge pump off and sets the

device in standby mode. When the device is in standby mode, the quiescent supply current is reduced to 3 $\mu$ A (max) and the switches remain operable. When  $\overline{QP}$  is driven low, the charge pump is enabled and the switches enter an improved high-performance mode.

## Applications Information

### USB Switching

The MAX4906EF analog switch is fully compliant with the USB 2.0 specification. The low on-resistance and low on-capacitance of these switches make the device ideal for high-performance switching applications. The MAX4906EF is ideal for routing USB data lines (see Figure 7) and for applications that require switching between multiple USB hosts (see Figure 8). The MAX4906EF also features +5.5V fault protection to guard systems against shorts to the USB bus voltage that is recommended for all USB applications.

### Ethernet Switching

The wide bandwidth of the MAX4906EF meets the needs of 10/100 Ethernet switching. The device switch the signals from two interface transformers and connect the signals to a single 10/100 Base-T Ethernet PHY, simplifying docking station design and reducing manufacturing costs.

### $\pm 15$ kV ESD Protection

As with all Maxim devices, ESD-protection structures are incorporated on all pins to protect against electrostatic discharges encountered during handling and assembly. COM\_ are further protected against static electricity. Maxim's engineers have developed state-of-the-art structures to protect these pins against ESD up to  $\pm 15$ kV without damage. The ESD structures withstand high ESD in normal operation, and when the device is powered down. After an ESD event, the MAX4906EF continues to function without latchup, whereas competing products can latch and must be powered down to restore functionality.

ESD protection can be tested in various ways. The ESD protection of COM\_ are characterized for  $\pm 15$ kV (Human Body Model) using the MIL-STD-883.

### ESD Test Conditions

ESD performance depends on a variety of conditions. Contact Maxim for a reliability report that documents test setup, test methodology, and test results.

### Human Body Model

Figure 9a shows the Human Body Model and Figure 9b shows the current waveform it generates when discharged into a low impedance. This model consists of

# High-/Full-Speed USB 2.0 Switches with High ESD

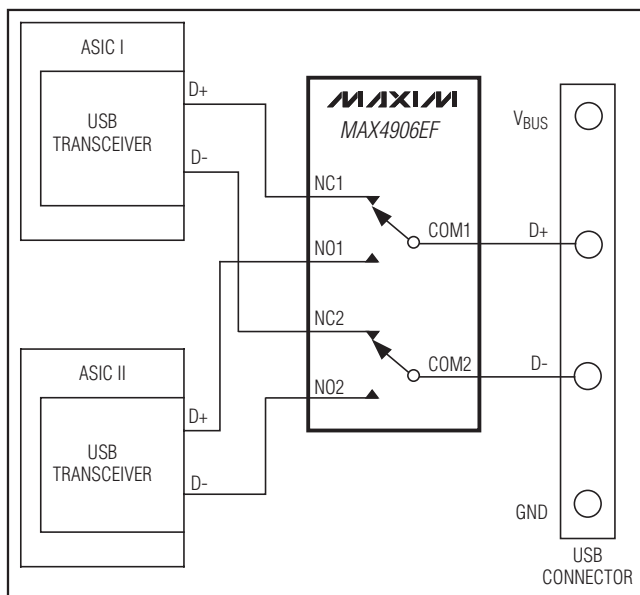


Figure 7. USB Data Routing

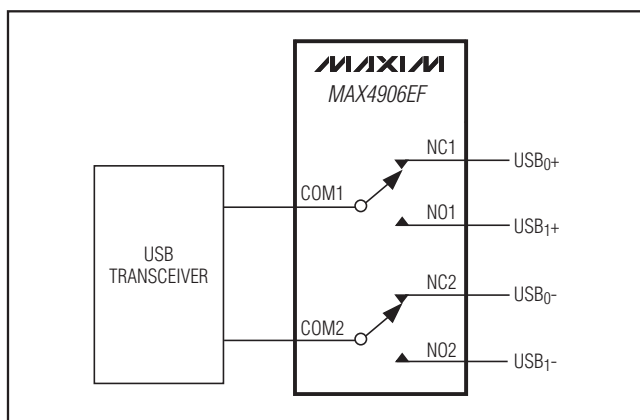


Figure 8. Switching Between Multiple USB Hosts

a 100pF capacitor charged to the ESD voltage of interest, which is then discharged into the test device through a 1.5kΩ resistor.

## Layout

High-speed switches require proper layout and design procedures for optimum performance. Keep design-controlled-impedance PC board traces as short as possible. Ensure that bypass capacitors are as close to the device as possible. Use large ground planes where possible.

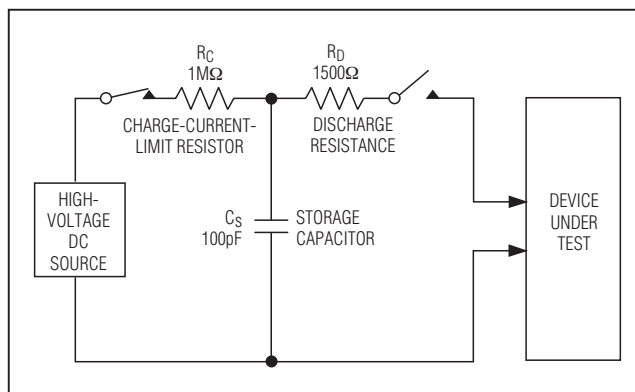


Figure 9a. Human Body ESD Test Model

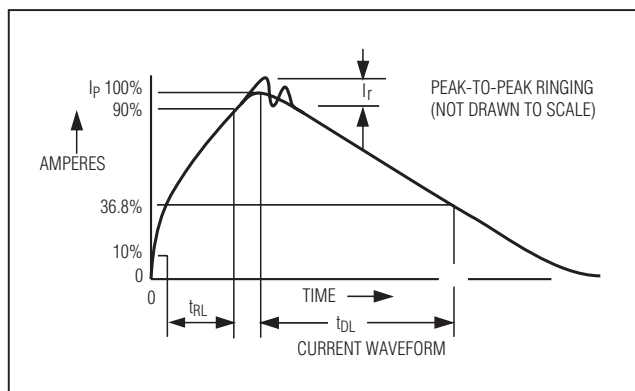


Figure 9b. Human Body Current Waveform

## Chip Information

PROCESS: BiCMOS

## Package Information

For the latest package outline information and land patterns, go to [www.maxim-ic.com/packages](http://www.maxim-ic.com/packages). Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE TYPE	PACKAGE CODE	DOCUMENT NO.
10 μDFN	L1022+1	<a href="#">21-0164</a>

# High-/Full-Speed USB 2.0 Switches with High ESD

## Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	8/06	Initial release.	—
1	11/07	Changed the <i>Electrical Characteristics</i> table.	2, 4
2	3/09	Changed the <i>Electrical Characteristics</i> table to show QP = GND.	2, 3, 4

MAX4906EF

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