

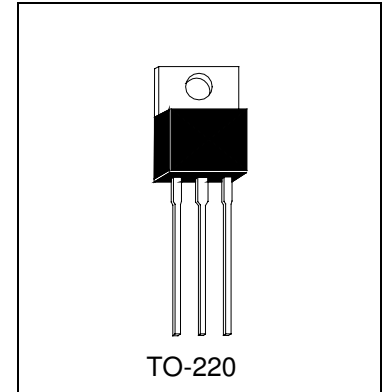


H2N6388

NPN EPITAXIAL PLANAR TRANSISTOR

Description

The H2N6388 is designed for general-purpose amplifier and switching applications.



Absolute Maximum Ratings (T_A=25°C)

- Maximum Temperatures
 - Storage Temperature -55 ~ +150 °C
 - Junction Temperature +150 °C Maximum
- Maximum Power Dissipation
 - Total Power Dissipation (T_C=25°C) 65 W
 - Total Power Dissipation (T_A=25°C) 2 W
- Maximum Voltages and Currents
 - BV_{CBO} Collector to Base Voltage 80 V
 - BV_{CEO} Collector to Emitter Voltage 80 V
 - BV_{EBO} Emitter to Base Voltage 5 V
 - I_C Collector Current 10 A

Electrical Characteristics (T_A=25°C)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
BV _{CBO}	80	-	-	V	I _C =1mA, I _E =0
I _{CBO}	-	-	100	uA	V _{CB} =160V, I _E =0
I _{EBO}	-	-	5	mA	V _{EB} =5V, I _C =0
I _{CEO}	-	-	1	mA	V _{CE} =80V, I _B =0
I _{CEV}	-	-	300	uA	V _{CE} =80V, V _{BE(off)} =1.5V
*V _{CE(sat)1}	-	-	2	V	I _C =5A, I _B =10mA
*V _{CE(sat)2}	-	-	3	V	I _C =10A, I _B =100mA
*V _{CE(sat)3}	-	1.5	-	V	I _C =5A, I _B =2.5mA
*V _{BE(sat)}	-	2	-	V	I _C =5A, I _B =5mA
V _{BE(on)1}	-	-	2.8	V	I _C =5A, V _{CE} =3V
V _{BE(on)2}	-	-	4.5	V	I _C =10A, V _{CE} =3V
*h _{FE1}	1	-	20	K	I _C =5A, V _{CE} =3V
*h _{FE2}	100	-	-		I _C =10A, V _{CE} =3V
V _{FEC}	-	3	-	V	I _C =5A
Cob	-	-	200	pF	V _{CB} =10V, I _E =0

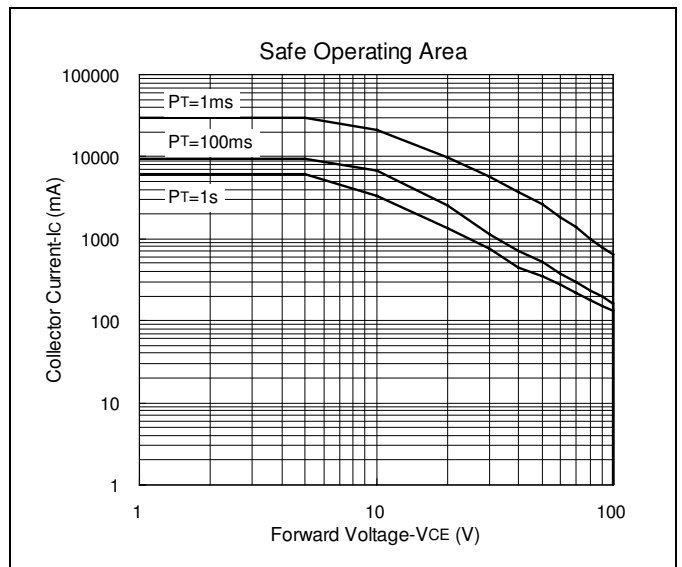
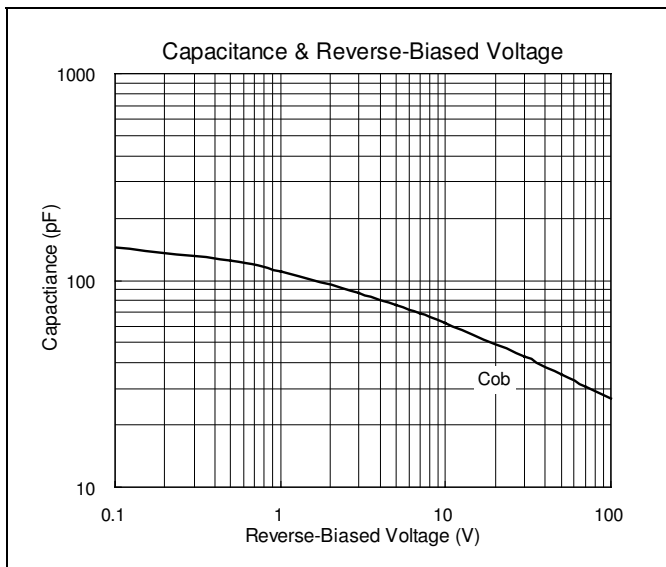
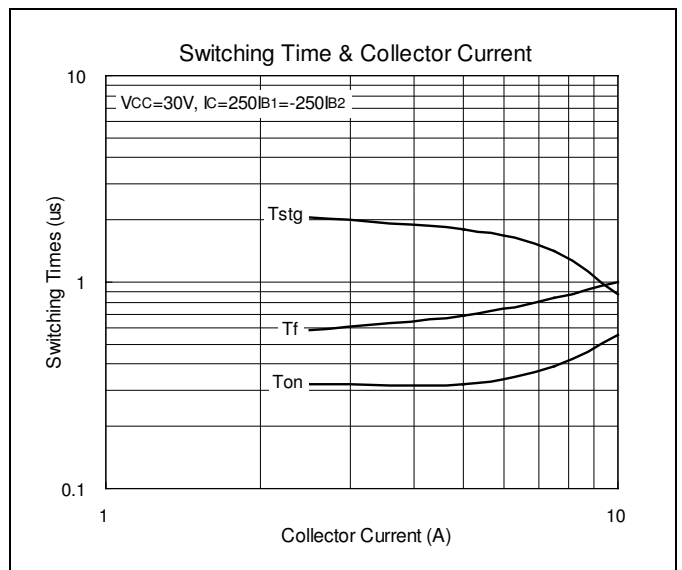
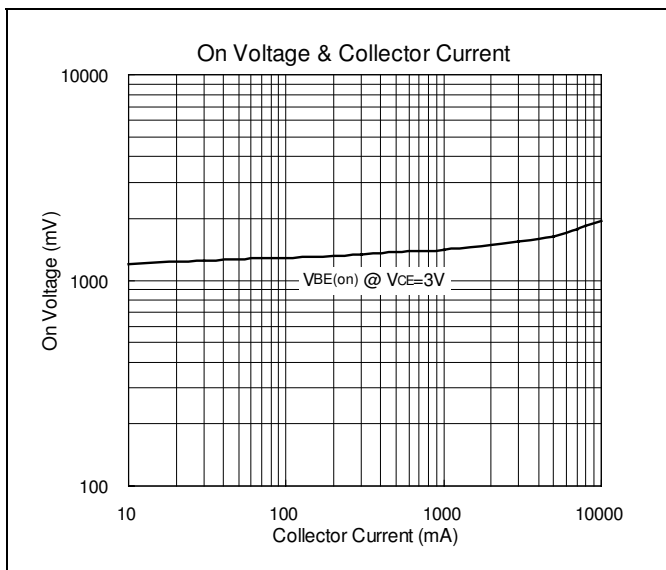
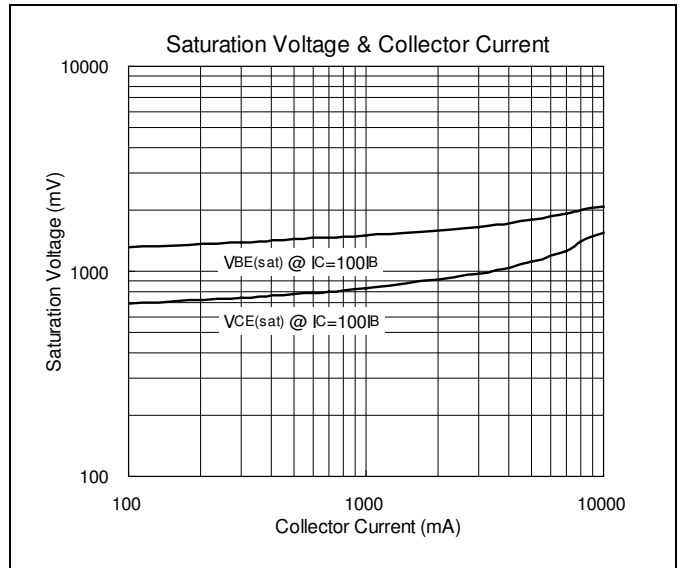
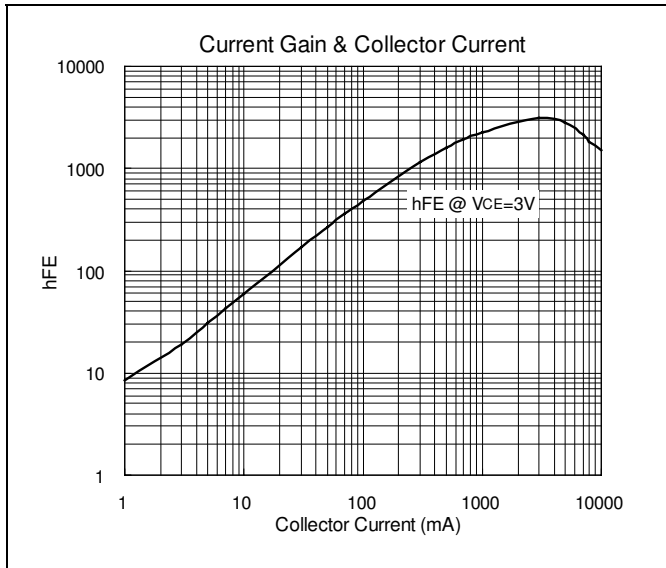
*Pulse Test: Pulse Width ≤380us, Duty Cycle≤2%

Classification

Rank	h _{FE1}	V _{CE(sat)1}	V _{CE(sat)3}	V _{BE(sat)}	V _{FEC}
KC	2-20K	<1.3V	<1.5V	<2.0V	<3.0V
Normal	1-20K	<2.0V	-	-	-



Characteristics Curve





TO-220AB Dimension

3-Lead TO-220AB
 Plastic Package
 HSMC Package Code: E

Marking:

Note: Green label is used for pb-free packing

Pin Style: 1.Base 2.Collector 3.Emitter

Material:

- Lead solder plating: Sn60/Pb40 (Normal), Sn/3.0Ag/0.5Cu or Pure-Tin (Pb-free)
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0

DIM	Min.	Max.
A	5.58	7.49
B	8.38	8.90
C	4.40	4.70
D	1.15	1.39
E	0.35	0.60
F	2.03	2.92
G	9.66	10.28
H	-	*16.25
I	-	*3.83
J	3.00	4.00
K	0.75	0.95
L	2.54	3.42
M	1.14	1.40
N	-	*2.54
O	12.70	14.27
P	14.48	15.87

*: Typical, Unit: mm

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Head Office And Factory:

- **Head Office** (Hi-Sincerity Microelectronics Corp.): 10F., No. 61, Sec. 2, Chung-Shan N. Rd. Taipei Taiwan R.O.C.
 Tel: 886-2-25212056 Fax: 886-2-25632712, 25368454
- **Factory 1:** No. 38, Kuang Fu S. Rd., Fu-Kou Hsin-Chu Industrial Park Hsin-Chu Taiwan. R.O.C
 Tel: 886-3-5983621~5 Fax: 886-3-5982931



Soldering Methods for HSMC's Products

1. Storage environment: Temperature=10°C~35°C Humidity=65%±15%
2. Reflow soldering of surface-mount devices



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (T _L to T _p)	<3°C/sec	<3°C/sec
Preheat		
- Temperature Min (T _{smin})	100°C	150°C
- Temperature Max (T _{smax})	150°C	200°C
- Time (min to max) (ts)	60~120 sec	60~180 sec
T _{smax} to T _L		
- Ramp-up Rate	<3°C/sec	<3°C/sec
Time maintained above:		
- Temperature (T _L)	183°C	217°C
- Time (t _L)	60~150 sec	60~150 sec
Peak Temperature (T _p)	240°C +0/-5°C	260°C +0/-5°C
Time within 5°C of actual Peak Temperature (t _p)	10~30 sec	20~40 sec
Ramp-down Rate	<6°C/sec	<6°C/sec
Time 25°C to Peak Temperature	<6 minutes	<8 minutes

3. Flow (wave) soldering (solder dipping)

Products	Peak temperature	Dipping time
Pb devices.	245°C ±5°C	5sec ±1sec
Pb-Free devices.	260°C +0/-5°C	5sec ±1sec