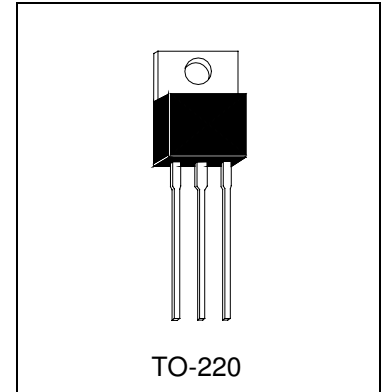




H2584

PNP EPITAXIAL PLANAR TRANSISTOR



Description

The H2584 is designed for use in low voltage and low dropout regulator applications.

Absolute Maximum Ratings

- Maximum Temperatures
 - Storage Temperature -55 ~ +150 °C
 - Junction Temperature 150 °C Maximum
- Maximum Power Dissipation
 - Total Power Dissipation ($T_C=25^{\circ}\text{C}$) 65 W
- Maximum Voltages and Currents ($T_A=25^{\circ}\text{C}$)
 - V_{CBO} Collector to Base Voltage -20 V
 - V_{CEO} Collector to Emitter Voltage -15 V
 - V_{EBO} Emitter to Base Voltage -5 V
 - I_C Collector Current -10 A

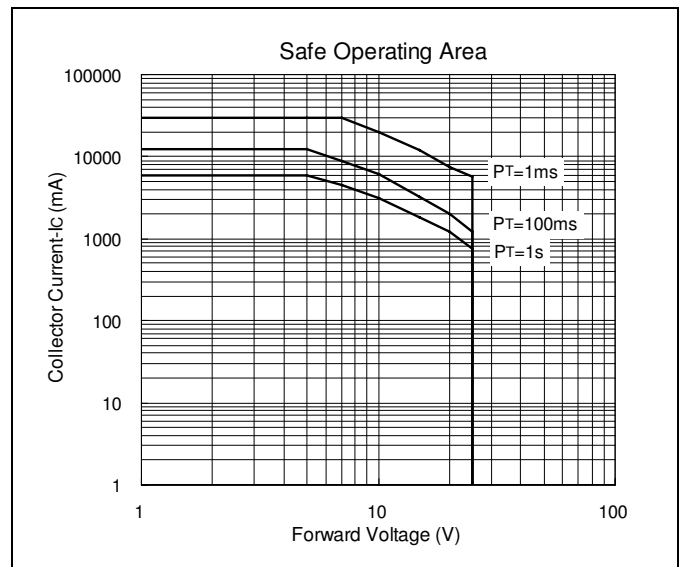
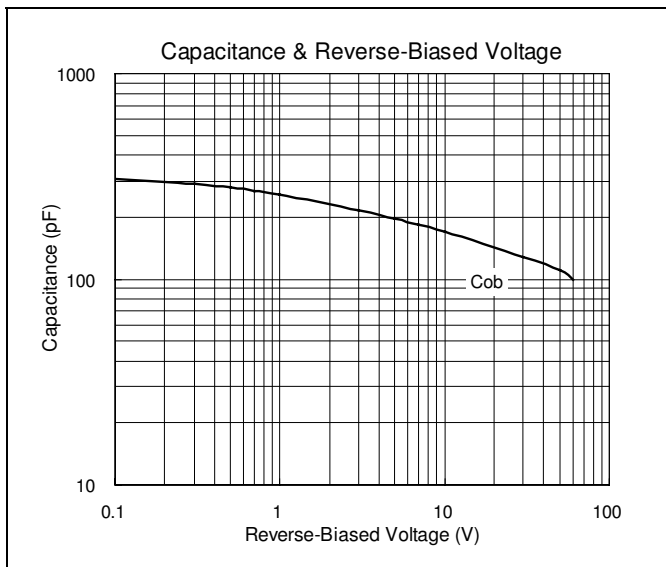
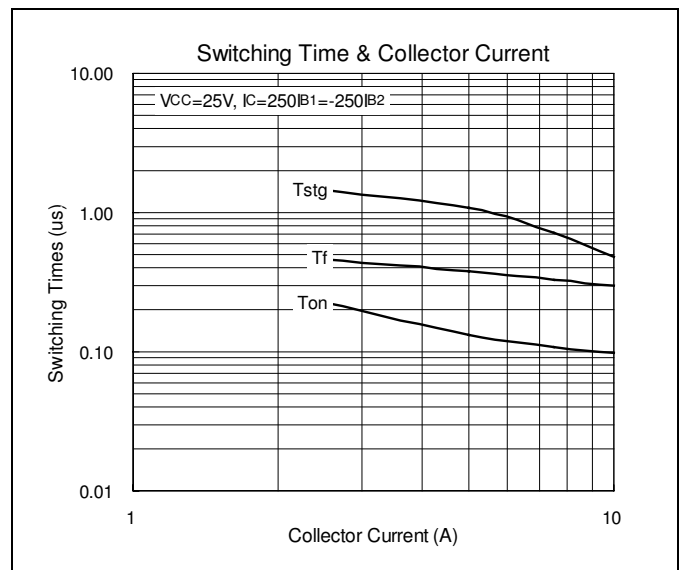
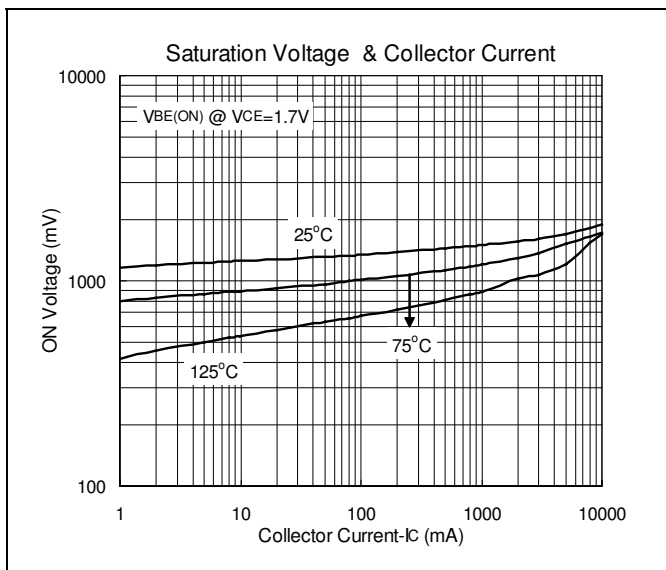
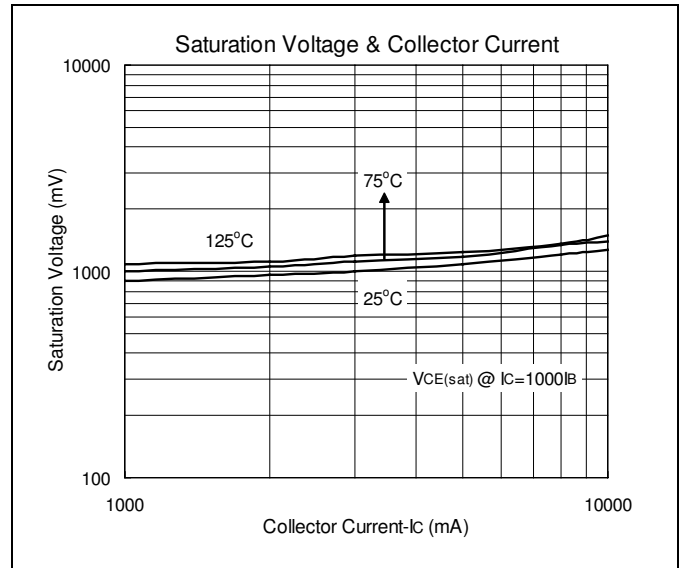
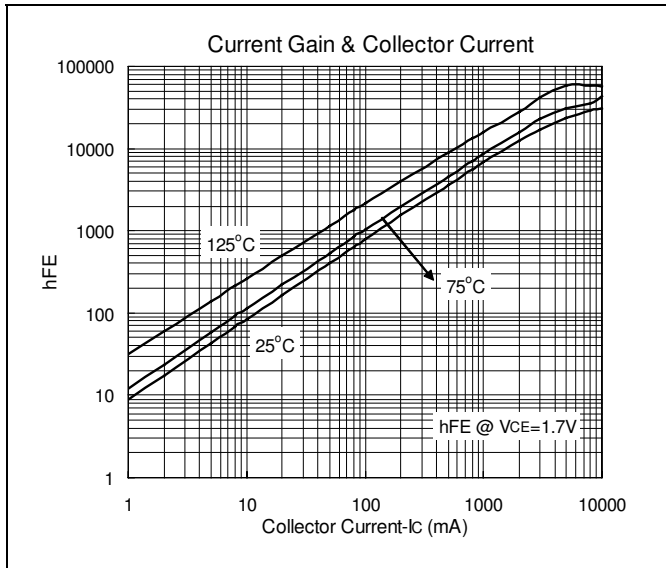
Electrical Characteristics ($T_A=25^{\circ}\text{C}$)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
I_{CBO}	-	-	-10	uA	$V_{CB}=-20\text{V}$
I_{CEO}	-	-	-20	uA	$V_{CE}=-15\text{V}$
I_{EBO}	-	-	-2	mA	$V_{EB}=-5\text{V}$
$*V_{CE(sat)}$	-	-	-1.5	V	$I_C=-10\text{A}, I_B=-10\text{mA}$
$V_{BE(on)}$	-	-	-2	V	$I_C=-5\text{A}, V_{CE}=-1.7\text{V}$
$*h_{FE1}$	2	-	60	K	$I_C=-500\text{mA}, V_{CE}=-1.7\text{V}$
$*h_{FE2}$	1	15	60	K	$I_C=-10\text{A}, V_{CE}=-1.7\text{V}$

*Pulse Test: Pulse Width $\leq 380\mu\text{s}$, Duty Cycle $\leq 2\%$



Characteristics Curve





TO-220AB Dimension

3-Lead TO-220AB
 Plastic Package
 HSMC Package Code: E

Marking:

Pb Free Mark
 Pb-Free: "•" (Note)
 Normal: None

Date Code Control Code

Note: Green label is used for pb-free packing

Pin Style: 1.Base 2.Collector 3.Emitter

Material:

- Lead solder plating: Sn60/Pb40 (Normal), Sn/3.0Ag/0.5Cu or Pure-Tin (Pb-free)
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0

DIM	Min.	Max.
A	5.58	7.49
B	8.38	8.90
C	4.40	4.70
D	1.15	1.39
E	0.35	0.60
F	2.03	2.92
G	9.66	10.28
H	-	*16.25
I	-	*3.83
J	3.00	4.00
K	0.75	0.95
L	2.54	3.42
M	1.14	1.40
N	-	*2.54
O	12.70	14.27
P	14.48	15.87

*: Typical, Unit: mm

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Soldering Methods for HSMC's Products

1. Storage environment: Temperature=10°C~35°C Humidity=65%±15%
2. Reflow soldering of surface-mount devices



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (T _L to T _p)	<3°C/sec	<3°C/sec
Preheat		
- Temperature Min (T _{smin})	100°C	150°C
- Temperature Max (T _{smax})	150°C	200°C
- Time (min to max) (ts)	60~120 sec	60~180 sec
T _{smax} to T _L		
- Ramp-up Rate	<3°C/sec	<3°C/sec
Time maintained above:		
- Temperature (T _L)	183°C	217°C
- Time (t _L)	60~150 sec	60~150 sec
Peak Temperature (T _p)	240°C +0/-5°C	260°C +0/-5°C
Time within 5°C of actual Peak Temperature (t _p)	10~30 sec	20~40 sec
Ramp-down Rate	<6°C/sec	<6°C/sec
Time 25°C to Peak Temperature	<6 minutes	<8 minutes

3. Flow (wave) soldering (solder dipping)

Products	Peak temperature	Dipping time
Pb devices.	245°C ±5°C	5sec ±1sec
Pb-Free devices.	260°C +0/-5°C	5sec ±1sec