

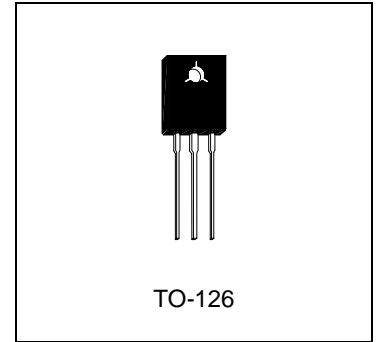


H2N6718T

NPN Epitaxial Planar Transistor

Description

The H2N6718T is designed for general purpose medium power amplifier and switching.



Absolute Maximum Ratings

- Maximum Temperatures
 - Storage Temperature -55 ~ +150 °C
 - Junction Temperature +150 °C Maximum
- Maximum Power Dissipation
 - Total Power Dissipation (T_A=25°C) 1 W
- Maximum Voltages and Currents
 - BVCBO Collector to Base Voltage 100 V
 - BVCEO Collector to Emitter Voltage 100 V
 - BVEBO Emitter to Base Voltage 5 V
 - IC Collector Current 1 A

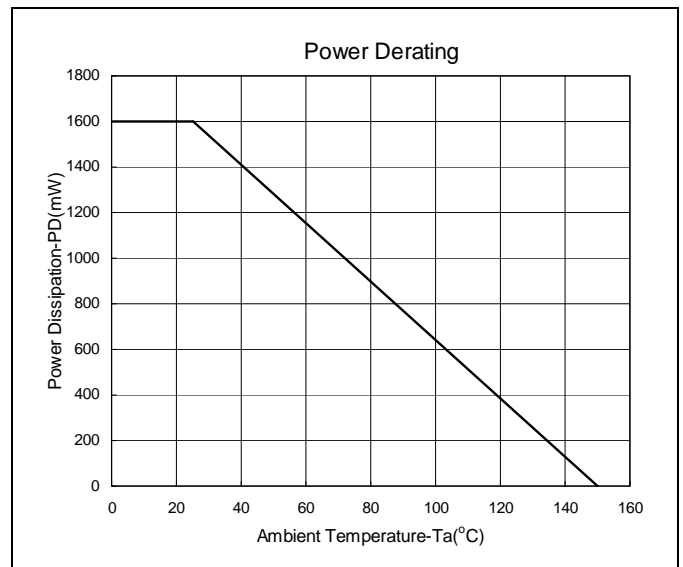
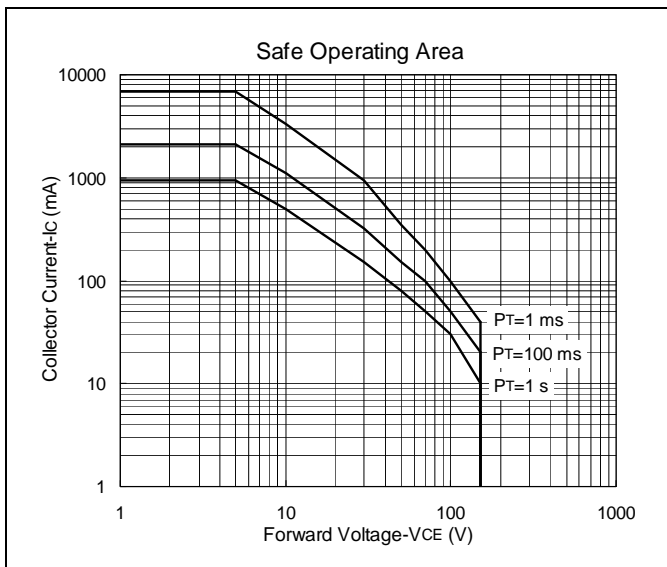
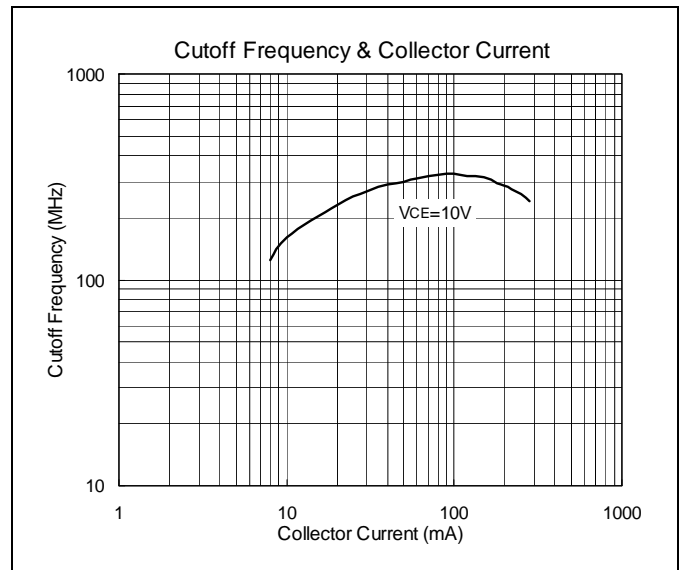
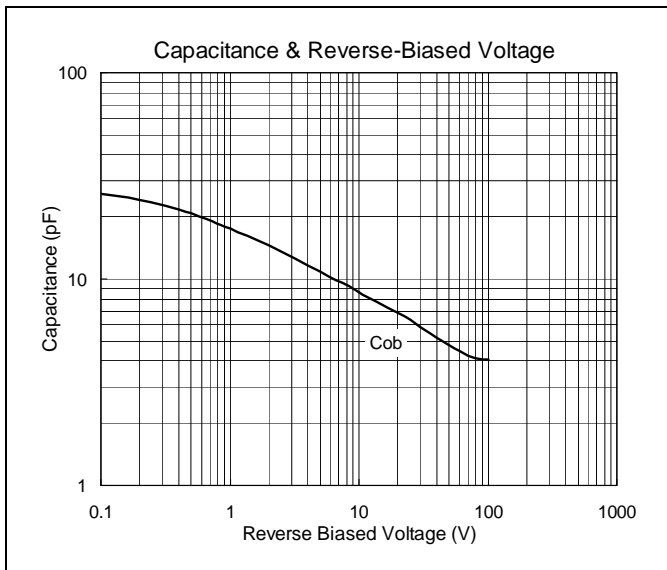
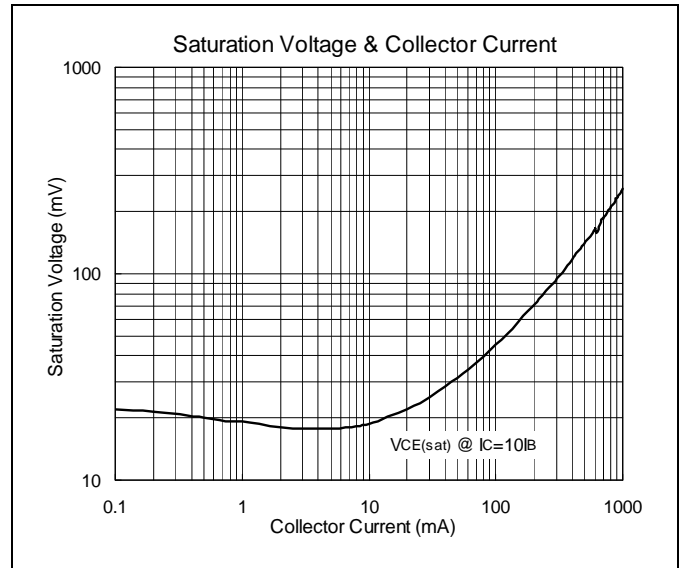
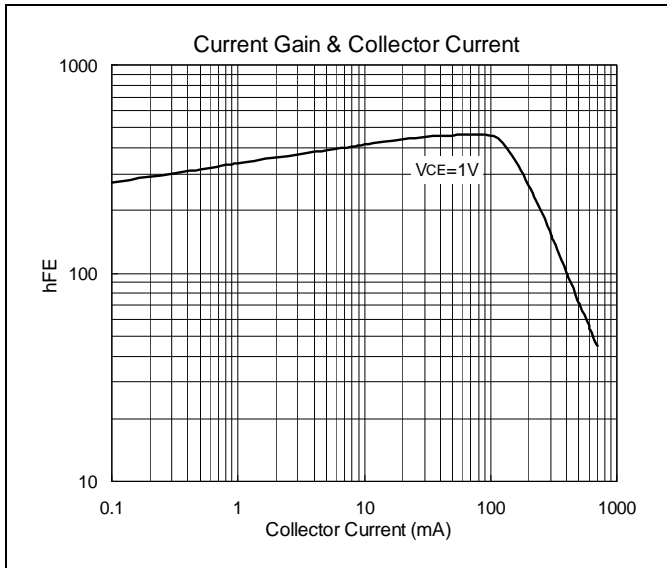
Electrical Characteristics (T_A=25°C)

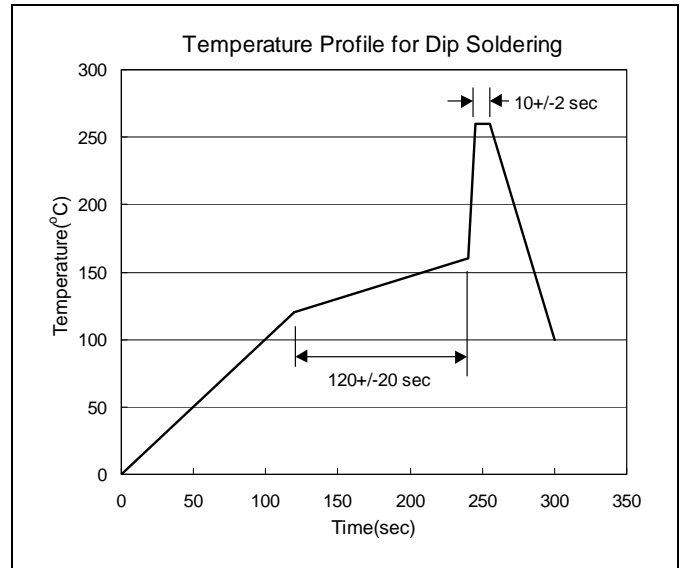
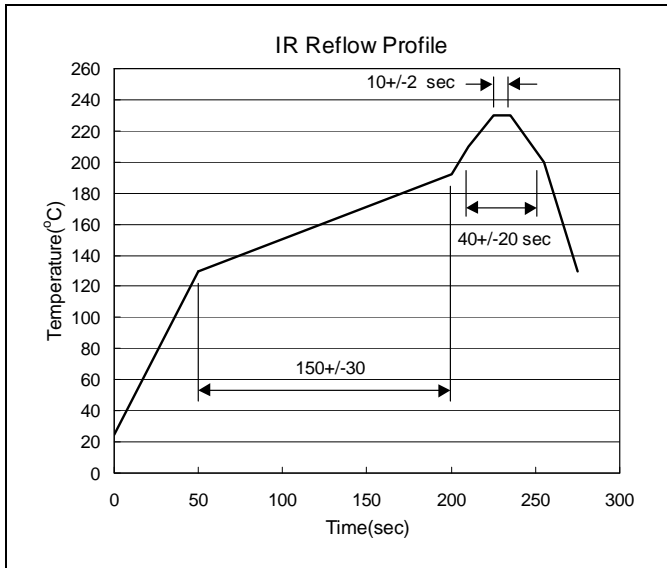
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
BV _{CBO}	100	-	-	V	I _C =100uA, I _E =0
BV _{CEO}	100	-	-	V	I _C =1mA, I _B =0
BV _{EBO}	5	-	-	V	I _E =10uA, I _C =0
I _{CBO}	-	-	100	nA	V _{CB} =80V, I _E =0
*V _{CE(sat)}	-	-	350	mV	I _C =350mA, I _B =35mA
*h _{FE1}	80	-	-		I _C =50mA, V _{CE} =1V
*h _{FE2}	50	-	250		I _C =250mA, V _{CE} =1V
*h _{FE3}	20	-	-		I _C =500mA, V _{CE} =1V
f _T	50	-	-	MHz	V _{CE} =10V, I _C =50mA, f=100MHz
Cob	-	-	20	pF	V _{CB} =10V, f=1MHz, I _E =0

*Pulse Test: Pulse Width ≤380us, Duty Cycle≤2%



Characteristics Curve







TO-126 Dimension

3-Lead TO-126
 Plastic Package
 HSMC Package Code: T

Marking:

Pb Free Mark
 Pb-Free: "●" (Note)
 Normal: None

Date Code: 6 7 1 8 T Control Code: 2 N

Note: Green label is used for pb-free packing
 Pin Style: 1.Emitter 2.Collector 3.Base

Material:

- Lead solder plating: Sn60/Pb40 (Normal), Sn/3.0Ag/0.5Cu or Pure-Tin (Pb-free)
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0

DIM	Min.	Max.
A	3.60	4.40
B	6.90	7.60
C	13.00	16.50
D	7.20	8.50
F	0.65	0.88
G	1.00	1.42
H	4.52	4.62
J	1.14	1.50
K	0.90	1.50
L	0.45	0.60
M	2.92	3.40
N	2.00	2.70

Unit: mm

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Head Office And Factory:

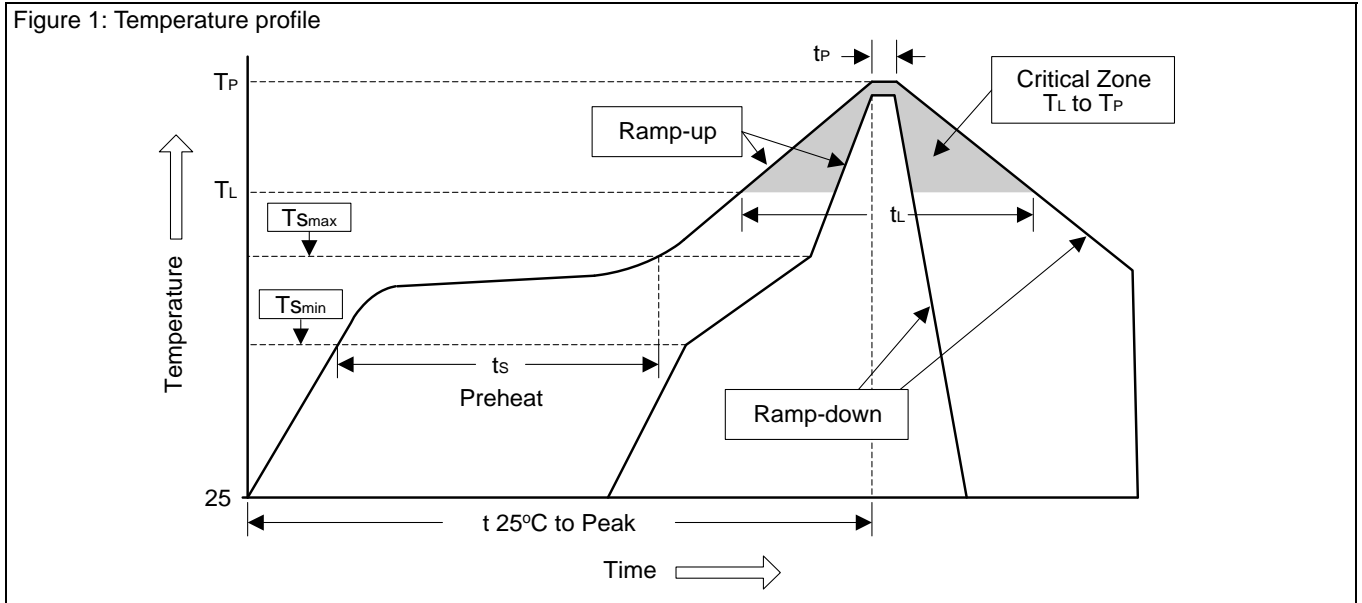
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 Tel: 886-3-5983621~5 Fax: 886-3-5982931



Soldering Methods for HSMC's Products

1. Storage environment: Temperature=10°C~35°C Humidity=65%±15%
2. Reflow soldering of surface-mount devices

Figure 1: Temperature profile



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (T_L to T_P)	$<3^\circ\text{C}/\text{sec}$	$<3^\circ\text{C}/\text{sec}$
Preheat		
- Temperature Min (T_{Smin})	100°C	150°C
- Temperature Max (T_{Smax})	150°C	200°C
- Time (min to max) (t_s)	60~120 sec	60~180 sec
T_{Smax} to T_L		
- Ramp-up Rate	$<3^\circ\text{C}/\text{sec}$	$<3^\circ\text{C}/\text{sec}$
Time maintained above:		
- Temperature (T_L)	183°C	217°C
- Time (t_L)	60~150 sec	60~150 sec
Peak Temperature (T_P)	240°C +0/-5°C	260°C +0/-5°C
Time within 5°C of actual Peak Temperature (t_P)	10~30 sec	20~40 sec
Ramp-down Rate	$<6^\circ\text{C}/\text{sec}$	$<6^\circ\text{C}/\text{sec}$
Time 25°C to Peak Temperature	<6 minutes	<8 minutes

3. Flow (wave) soldering (solder dipping)

Products	Peak temperature	Dipping time
Pb devices.	245°C ±5°C	10sec ±1sec
Pb-Free devices.	260°C ±5°C	10sec ±1sec