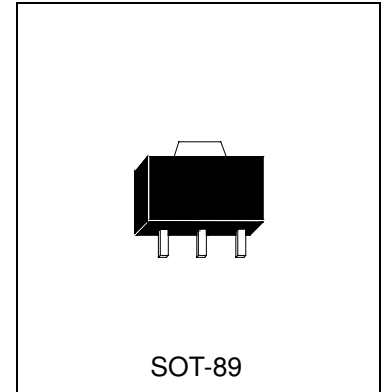




# HM669A

NPN EPITAXIAL PLANAR TRANSISTOR



## Description

Low frequency power amplifier complementary pair with HM649A

## Absolute Maximum Ratings (T<sub>A</sub>=25°C)

- Maximum Temperatures
  - Storage Temperature ..... -55 ~ +150 °C
  - Junction Temperature ..... +150 °C Maximum
- Maximum Power Dissipation
  - Total Power Dissipation (T<sub>A</sub>=25°C) ..... 1 W
  - Total Power Dissipation (T<sub>C</sub>=25°C) ..... 10 W
- Maximum Voltages and Currents
  - BV<sub>CBO</sub> Collector to Base Voltage ..... 180 V
  - BV<sub>CEO</sub> Collector to Emitter Voltage ..... 160 V
  - BV<sub>EBO</sub> Emitter to Base Voltage ..... 5 V
  - I<sub>C</sub> Collector Current (DC) ..... 1.5 A
  - I<sub>C</sub> Collector Current (Pulse) ..... 3 A

## Thermal Characteristic

Symbol	Characteristic	Max.	Unit
R <sub>θja</sub>	Thermal Resistance, junction to ambient (T <sub>A</sub> =25°C)	125	°C/W
R <sub>θjc</sub>	Thermal Resistance, junction to case (T <sub>C</sub> =25°C)	12.5	°C/W

## Electrical Characteristics (T<sub>A</sub>=25°C)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
BV <sub>CBO</sub>	180	-	-	V	I <sub>C</sub> =1mA, I <sub>E</sub> =0
BV <sub>CEO</sub>	160	-	-	V	I <sub>C</sub> =10mA, I <sub>B</sub> =0
BV <sub>EBO</sub>	5	-	-	V	I <sub>E</sub> =1mA, I <sub>C</sub> =0
I <sub>CBO</sub>	-	-	10	uA	V <sub>CB</sub> =160V, I <sub>E</sub> =0
*V <sub>CE(sat)</sub>	-	-	1	V	I <sub>C</sub> =500mA, I <sub>B</sub> =50mA
V <sub>BE(on)</sub>	-	-	1.5	V	I <sub>C</sub> =150mA, V <sub>CE</sub> =5V
*h <sub>FE1</sub>	100	-	320		I <sub>C</sub> =150mA, V <sub>CE</sub> =5V
*h <sub>FE2</sub>	30	-	-		I <sub>C</sub> =500mA, V <sub>CE</sub> =5V
f <sub>T</sub>	-	140	-	MHz	I <sub>C</sub> =150mA, V <sub>CE</sub> =5V

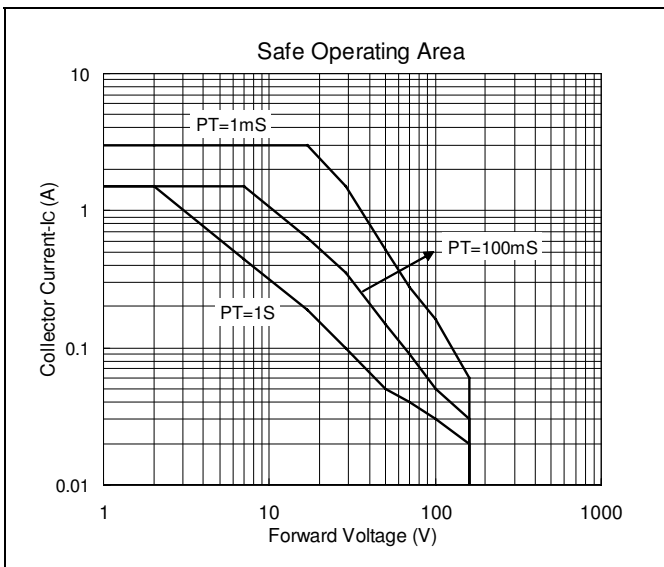
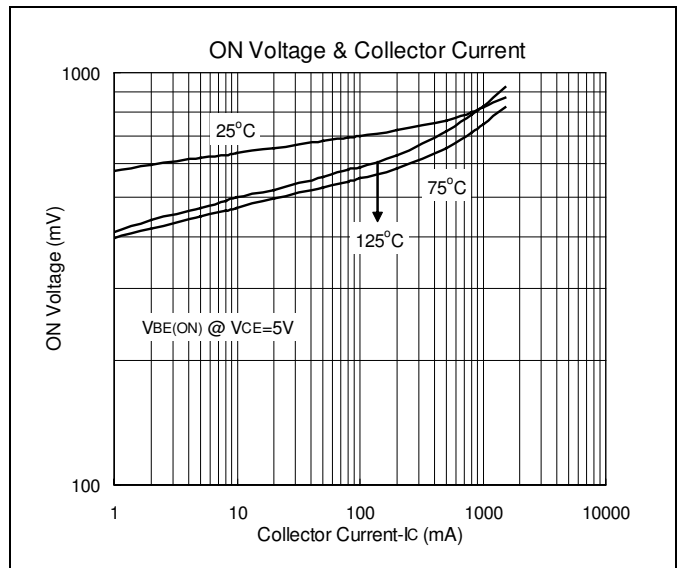
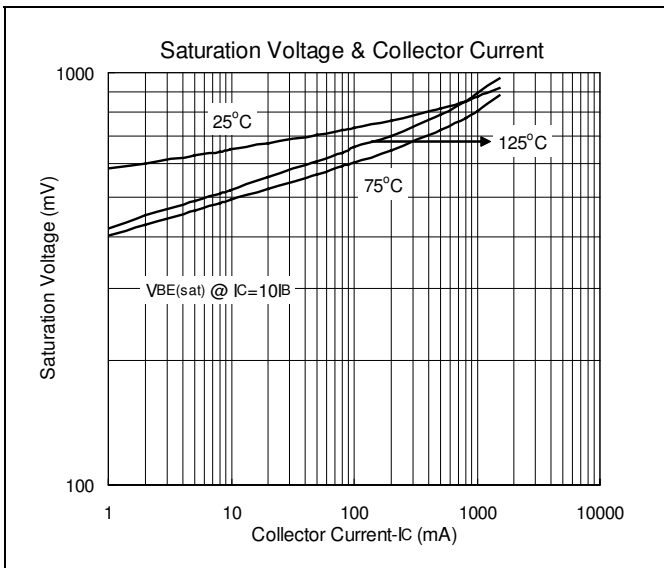
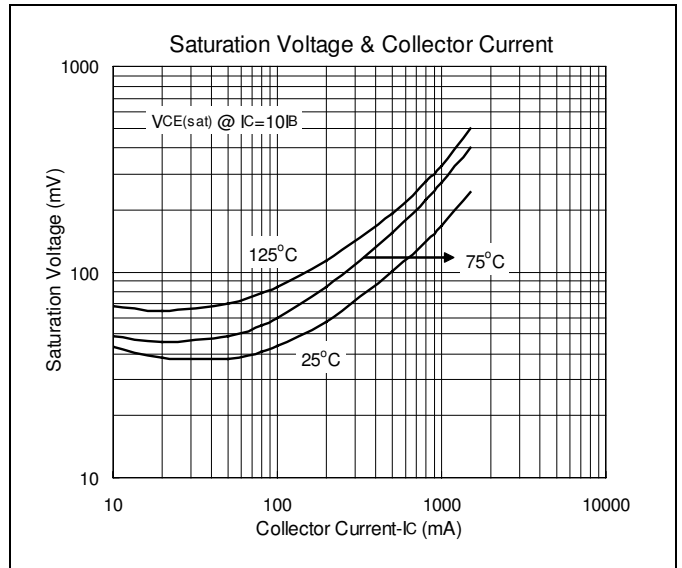
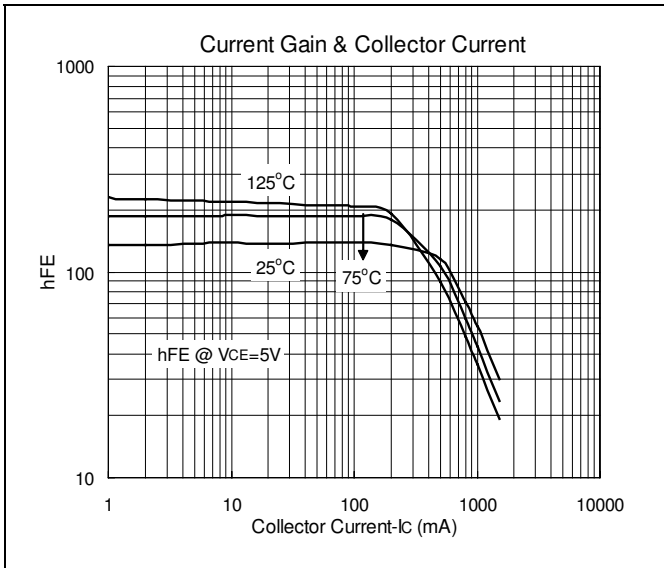
\*Pulse Test: Pulse Width ≤380us, Duty Cycle≤2%

## Classification Of hFE1

Rank	C	D
Range	100-200	180-320



### Characteristics Curve





### SOT-89 Dimension

3-Lead SOT-89 Plastic  
Surface Mounted Package  
HSMC Package Code: M

**Marking:**

Date Code      Control Code

Pb Free Mark  
Pb-Free: "•" (Note)  
Normal: None

Note: Green label is used for pb-free packing

Pin Style: 1.Base 2.Collector 3.Emitter

Material:

- Lead solder plating: Sn60/Pb40 (Normal), Sn/3.0Ag/0.5Cu or Pure-Tin (Pb-free)
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0

DIM	Min.	Max.
A	4.40	4.60
B	4.05	4.25
C	1.50	1.70
D	2.40	2.60
E	0.36	0.51
F	*1.50	-
G	*3.00	-
H	1.40	1.60
I	0.35	0.41

\*: Typical, Unit: mm

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### Soldering Methods for HSMC's Products

1. Storage environment: Temperature=10°C~35°C Humidity=65%±15%
2. Reflow soldering of surface-mount devices



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (T <sub>L</sub> to T <sub>p</sub> )	<3°C/sec	<3°C/sec
Preheat		
- Temperature Min (T <sub>smin</sub> )	100°C	150°C
- Temperature Max (T <sub>smax</sub> )	150°C	200°C
- Time (min to max) (ts)	60~120 sec	60~180 sec
T <sub>smax</sub> to T <sub>L</sub>		
- Ramp-up Rate	<3°C/sec	<3°C/sec
Time maintained above:		
- Temperature (T <sub>L</sub> )	183°C	217°C
- Time (t <sub>L</sub> )	60~150 sec	60~150 sec
Peak Temperature (T <sub>p</sub> )	240°C +0/-5°C	260°C +0/-5°C
Time within 5°C of actual Peak Temperature (t <sub>p</sub> )	10~30 sec	20~40 sec
Ramp-down Rate	<6°C/sec	<6°C/sec
Time 25°C to Peak Temperature	<6 minutes	<8 minutes

### 3. Flow (wave) soldering (solder dipping)

Products	Peak temperature	Dipping time
Pb devices.	245°C ±5°C	5sec ±1sec
Pb-Free devices.	260°C +0/-5°C	5sec ±1sec