

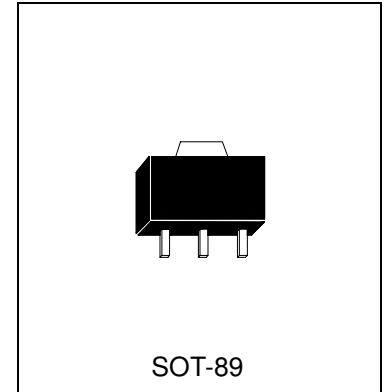


HM42

NPN EPITAXIAL PLANAR TRANSISTOR

Description

The HM42 is designed for application as a video output to drive color CRT, or as a dialer circuit in electronics telephone.



Absolute Maximum Ratings

- Maximum Temperatures
 - Storage Temperature -55 ~ +150 °C
 - Junction Temperature +150 °C Maximum
- Maximum Power Dissipation
 - Total Power Dissipation (T_A=25°C) 1 W
- Maximum Voltages and Currents (T_A=25°C)
 - V_{CBO} Collector to Base Voltage 300 V
 - V_{CEO} Collector to Emitter Voltage 300 V
 - V_{EBO} Emitter to Base Voltage 6 V
 - I_C Collector Current 500 mA

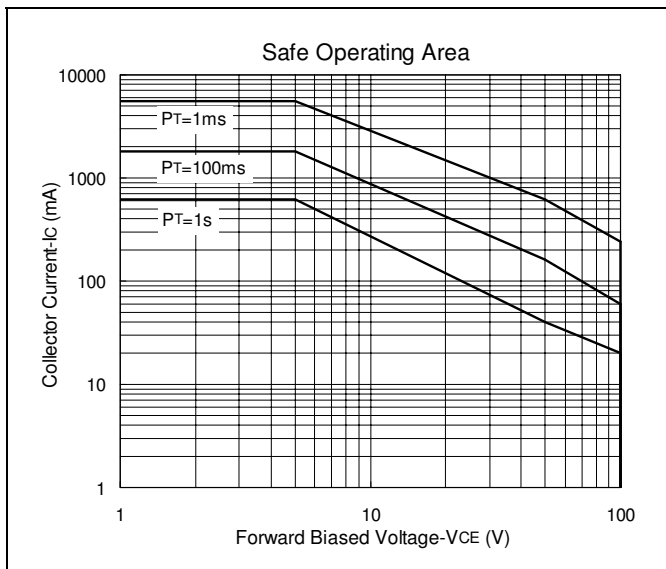
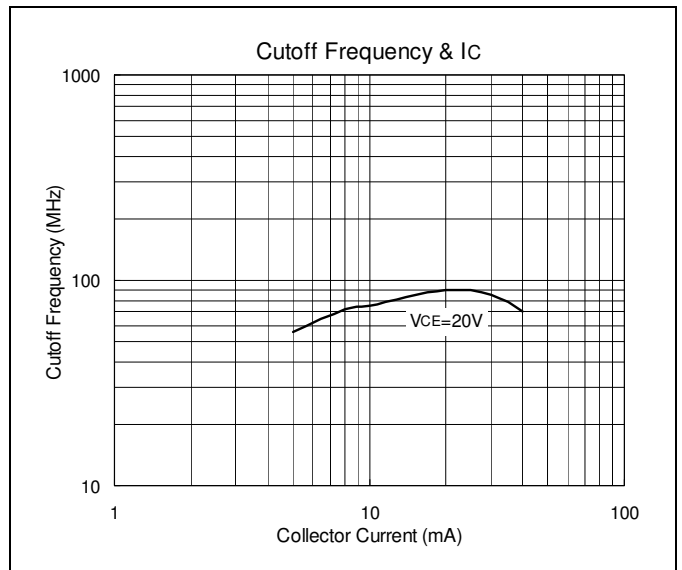
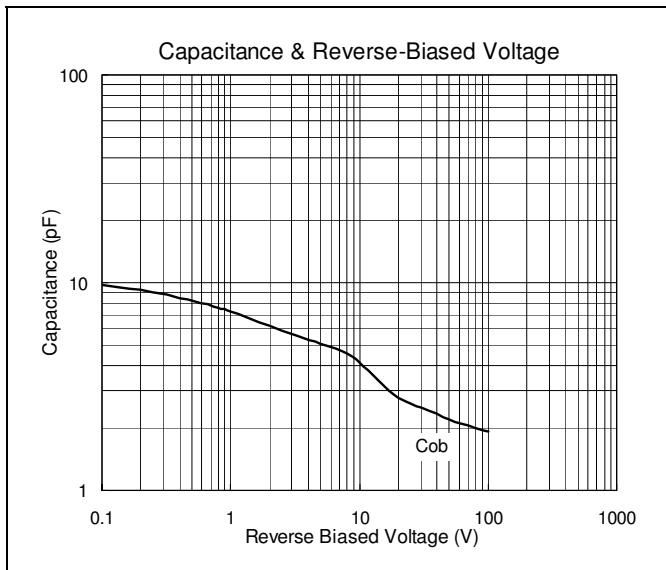
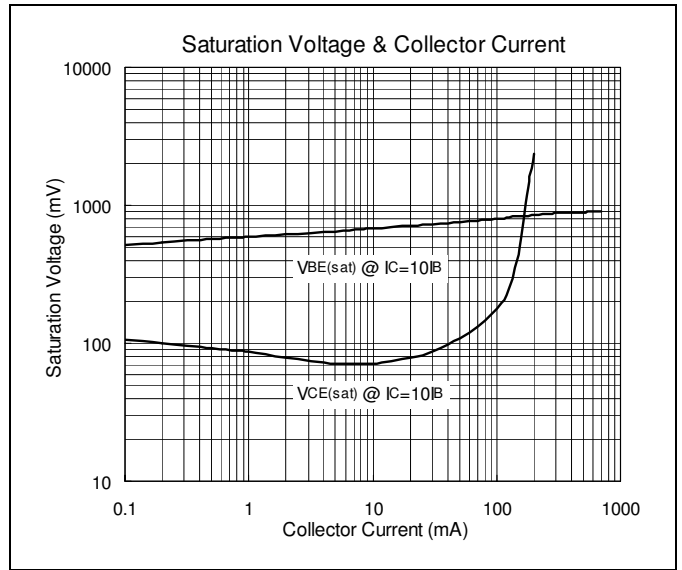
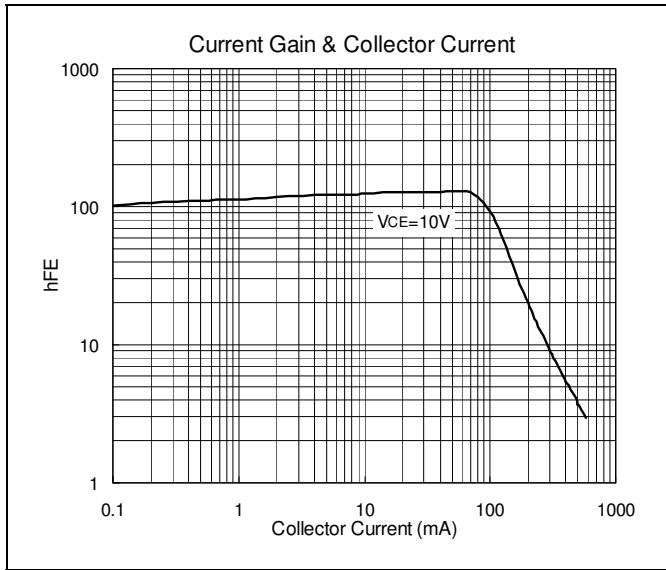
Electrical Characteristics (T_A=25°C)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
BV _{CBO}	300	-	-	V	I _C =100uA
BV _{CEO}	300	-	-	V	I _C =1mA
BV _{EBO}	6	-	-	V	I _E =10uA
I _{CBO}	-	-	100	nA	V _{CB} =260V
I _{EBO}	-	-	100	nA	V _{EB} =6V
*V _{CE(sat)}	-	-	500	mV	I _C =20mA, I _B =2mA
*V _{BE(sat)}	-	-	900	mV	I _C =20mA, I _B =2mA
*h _{FE1}	25	-	-		V _{CE} =10V, I _C =1mA
*h _{FE2}	40	-	-		V _{CE} =10V, I _C =10mA
*h _{FE3}	40	-	-		V _{CE} =10V, I _C =30mA
f _T	50	-	-	MHz	V _{CE} =20V, I _C =10mA, f=100MHz
Cob	-	3	-	pF	V _{CB} =20V

*Pulse Test: Pulse Width ≤380us, Duty Cycle ≤2%



Characteristics Curve





SOT-89 Dimension

3-Lead SOT-89 Plastic
Surface Mounted Package
HSMC Package Code: M

Marking:

Date Code Control Code

Pb Free Mark
Pb-Free: "●" (Note)
Normal: None

Note: Green label is used for pb-free packing

Pin Style: 1.Base 2.Collector 3.Emitter

Material:

- Lead solder plating: Sn60/Pb40 (Normal), Sn/3.0Ag/0.5Cu or Pure-Tin (Pb-free)
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0

DIM	Min.	Max.
A	4.40	4.60
B	4.05	4.25
C	1.50	1.70
D	2.40	2.60
E	0.36	0.51
F	*1.50	-
G	*3.00	-
H	1.40	1.60
I	0.35	0.41

*: Typical, Unit: mm

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Soldering Methods for HSMC's Products

1. Storage environment: Temperature=10°C~35°C Humidity=65%±15%
2. Reflow soldering of surface-mount devices



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (T_L to T_P)	<3°C/sec	<3°C/sec
Preheat		
- Temperature Min (T_{smin})	100°C	150°C
- Temperature Max (T_{smax})	150°C	200°C
- Time (min to max) (t_s)	60~120 sec	60~180 sec
T_{smax} to T_L		
- Ramp-up Rate	<3°C/sec	<3°C/sec
Time maintained above:		
- Temperature (T_L)	183°C	217°C
- Time (t_L)	60~150 sec	60~150 sec
Peak Temperature (T_P)	240°C +0/-5°C	260°C +0/-5°C
Time within 5°C of actual Peak Temperature (t_p)	10~30 sec	20~40 sec
Ramp-down Rate	<6°C/sec	<6°C/sec
Time 25°C to Peak Temperature	<6 minutes	<8 minutes

3. Flow (wave) soldering (solder dipping)

Products	Peak temperature	Dipping time
Pb devices.	245°C ±5°C	5sec ±1sec
Pb-Free devices.	260°C +0/-5°C	5sec ±1sec