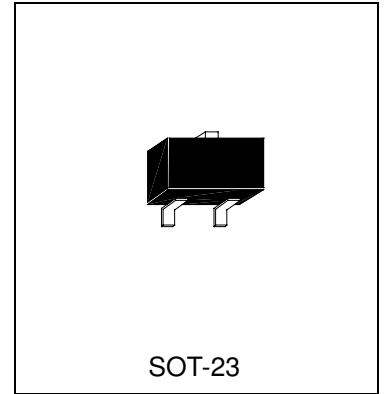




HBC856

PNP EPITAXIAL PLANAR TRANSISTOR



Description

The HBC856 is designed for switching and AF amplifier amplification suitable for automatic insertion in thick and thin-film circuits.

Absolute Maximum Ratings

- Maximum Temperatures
Storage Temperature..... -55 to +150 °C
Junction Temperature..... +150 °C
- Maximum Power Dissipation
Total Power Dissipation (T_A=25°C)..... 225 mW
- Maximum Voltages and Currents (T_A=25°C)
V_{CBO} Collector to Base Voltage -80 V
V_{CEO} Collector to Emitter Voltage..... -65 V
V_{EBO} Emitter to Base Voltage..... -5 V
I_C Collector Current..... -100 mA

Electrical Characteristics (T_A=25°C)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
BV _{CBO}	-80	-	-	V	I _C =-100uA
BV _{CEO}	-65	-	-	V	I _C =-1mA
BV _{EBO}	-5	-	-	V	I _E =-10uA
I _{CBO}	-	-	-15	nA	V _{CB} =-30V
*V _{CE(sat)1}	-	-75	-300	mV	I _C =-10mA, I _B =-0.5mA
*V _{CE(sat)2}	-	-250	-650	mV	I _C =-100mA, I _B =-5mA
*V _{BE(sat)1}	-	-700	-	mV	I _C =-10mA, I _B =-0.5mA
*V _{BE(sat)2}	-	-850	-	mV	I _C =-100mA, I _B =-5mA
V _{BE(on)1}	-600	-	-750	mV	V _{CE} =-5V, I _C =-2mA
V _{BE(on)2}	-	-	-820	mV	V _{CE} =-5V, I _C =-10mA
*h _{FE}	75	-	800		V _{CE} =-5V, I _C =-2mA
f _T	-	150	-	MHz	V _{CE} =-5V, I _C =-10mA
Cob	-	4.5	-	pF	V _{CB} =-10V, f=1MHz

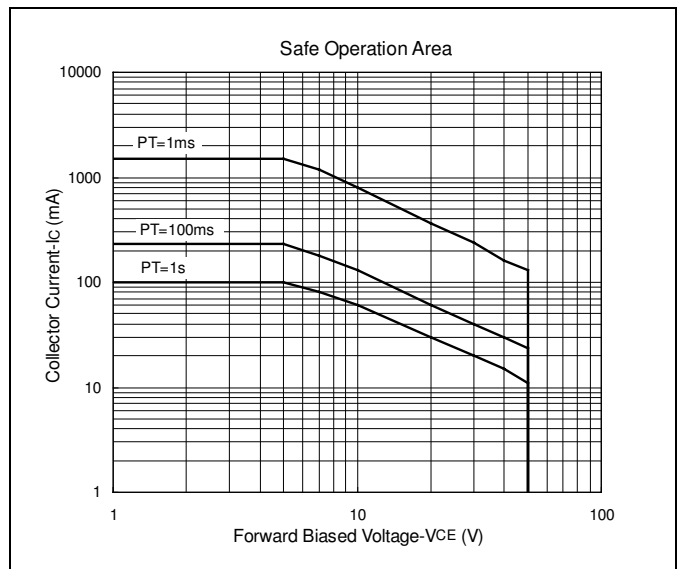
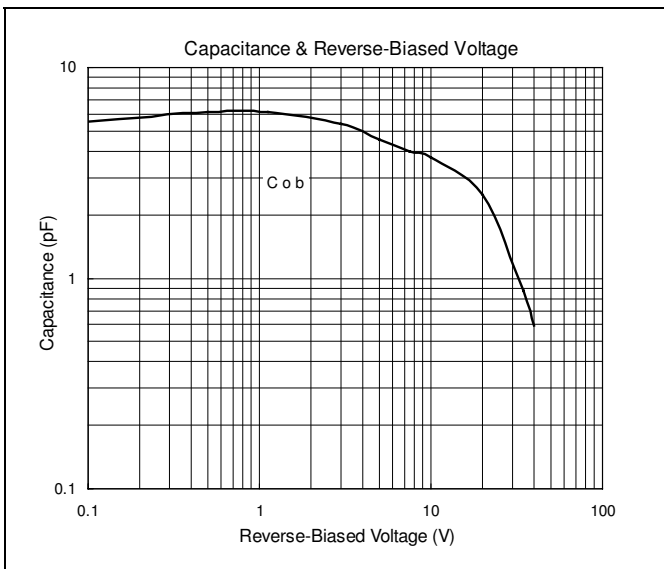
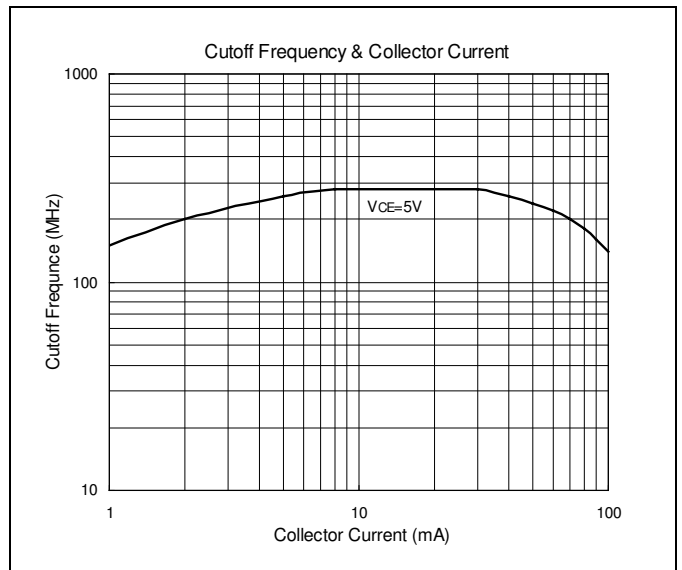
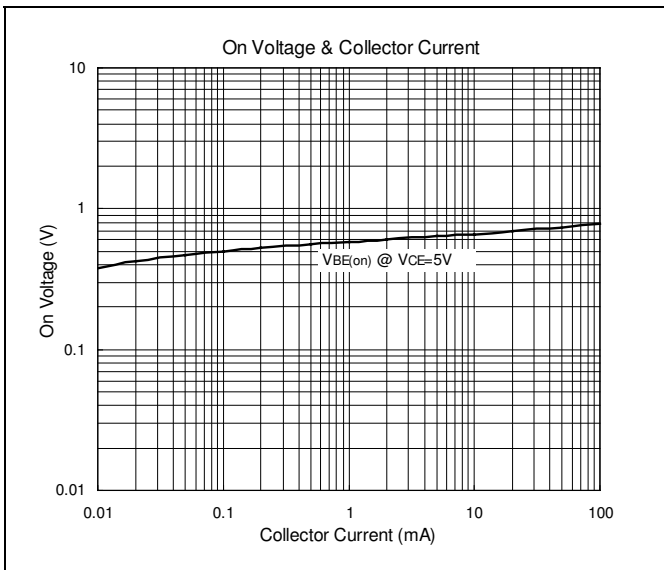
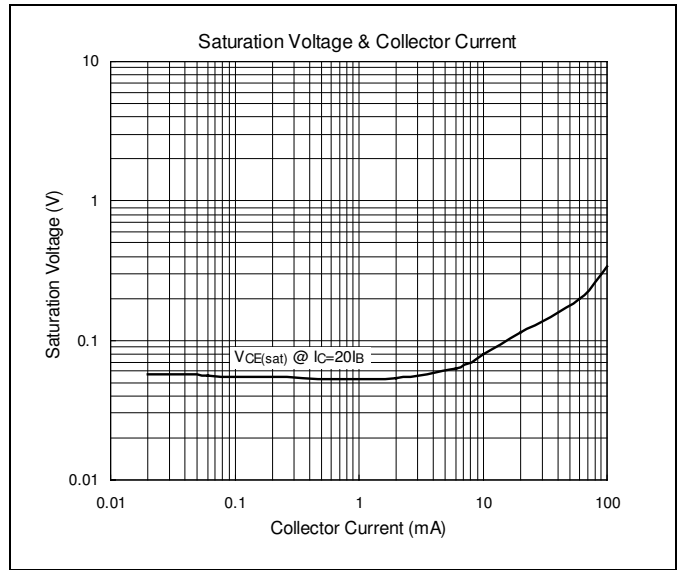
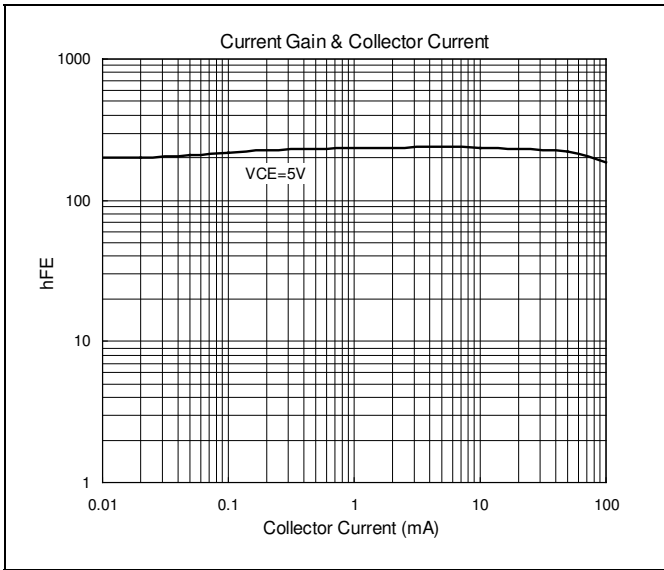
*Pulse Test: Pulse Width ≤380us, Duty Cycle≤2%

Classification Of hFE

Rank	9AA(A)	9AB(B)	9AC(C)	9A(Normal)
hFE	125-250	220-475	420-800	75-475



Characteristics Curve





SOT-23 Dimension

3-Lead SOT-23 Plastic
Surface Mounted Package
HSMC Package Code: N

Marking:

Rank Code
(A,B,C,None)

Pb Free Mark
Pb-Free: "●" (Note)
Normal: None

Note: Pb-free product can distinguish by the green label or the extra description on the right side of the label.

Pin Style: 1.Base 2.Emitter 3.Collector

Material:

- Lead solder plating: Sn60/Pb40 (Normal), Sn/3.0Ag/0.5Cu or Pure-Tin (Pb-free)
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0

DIM	Min.	Max.
A	2.80	3.04
B	1.20	1.60
C	0.89	1.30
D	0.30	0.50
G	1.70	2.30
H	0.013	0.10
J	0.085	0.177
K	0.32	0.67
L	0.85	1.15
S	2.10	2.75
V	0.25	0.65

*: Typical, Unit: mm

Important Notice:

- All rights are reserved. Reproduction in whole or in part is prohibited without the prior written approval of HSMC.
- HSMC reserves the right to make changes to its products without notice.
- **HSMC semiconductor products are not warranted to be suitable for use in Life-Support Applications, or systems.**
- HSMC assumes no liability for any consequence of customer product design, infringement of patents, or application assistance.

Head Office And Factory:

- **Head Office** (Hi-Sincerity Microelectronics Corp.): 10F.,No. 61, Sec. 2, Chung-Shan N. Rd. Taipei Taiwan R.O.C.
Tel: 886-2-25212056 Fax: 886-2-25632712, 25368454
- **Factory 1:** No. 38, Kuang Fu S. Rd., Fu-Kou Hsin-Chu Industrial Park Hsin-Chu Taiwan. R.O.C
Tel: 886-3-5983621~5 Fax: 886-3-5982931



Soldering Methods for HSMC's Products

1. Storage environment: Temperature=10°C~35°C Humidity=65%±15%
2. Reflow soldering of surface-mount devices



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (T _L to T _p)	<3°C/sec	<3°C/sec
Preheat		
- Temperature Min (T _{smin})	100°C	150°C
- Temperature Max (T _{smax})	150°C	200°C
- Time (min to max) (ts)	60~120 sec	60~180 sec
T _{smax} to T _L		
- Ramp-up Rate	<3°C/sec	<3°C/sec
Time maintained above:		
- Temperature (T _L)	183°C	217°C
- Time (t _L)	60~150 sec	60~150 sec
Peak Temperature (T _p)	240°C +0/-5°C	260°C +0/-5°C
Time within 5°C of actual Peak Temperature (t _p)	10~30 sec	20~40 sec
Ramp-down Rate	<6°C/sec	<6°C/sec
Time 25°C to Peak Temperature	<6 minutes	<8 minutes

3. Flow (wave) soldering (solder dipping)

Products	Peak temperature	Dipping time
Pb devices.	245°C ±5°C	5sec ±1sec
Pb-Free devices.	260°C +0/-5°C	5sec ±1sec