

## **F75387SG/RG Datasheet**

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**±1°C Accuracy H/W Monitor IC with Automatic Fan Speed Control**

**Release Date: July, 2007**  
**Revision: V0.27P**

## F75387 Datasheet Revision History

Version	Date	Page	Revision History
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0.21P	Mar/2005	41-42	Update DC spec
0.22P	Apr/2005	37~40	Update description of Index A4~A8/B4~B8
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**Table of Contents**

<b>1. GENERAL DESCRIPTION .....</b>	<b>4</b>
<b>2. FEATURES .....</b>	<b>4</b>
<b>3. KEY SPECIFICATIONS .....</b>	<b>5</b>
<b>4. PIN CONFIGURATION .....</b>	<b>5</b>
<b>5. PIN DESCRIPTION.....</b>	<b>5</b>
5.1.    POWER PIN .....	6
5.2.    MONITORING ITEMS AND FAN SPEED CONTROL .....	6
5.3.    ALERT SIGNALS AND OTHERS.....	6
<b>6. FUNCTION DESCRIPTION .....</b>	<b>7</b>
6.1.    GENERAL DESCRIPTION .....	7
6.2.    ANALOG INPUTS .....	7
6.3.    ACCESS INTERFACE AND POWER ON CONFIGURATION .....	8
6.4.    TYPICAL OPERATING CHARACTERISTIC .....	9
6.5.    TEMPERATURE MONITORING.....	9
6.6.    TEMPERATURE MEASUREMENT MACHINE .....	10
6.7.    MONITOR TEMPERATURE FROM THERMISTOR .....	10
6.8.    MONITOR TEMPERATURE FROM THERMAL DIODE .....	11
6.9.    ADC NOISE FILTERING.....	11
6.10.   OVER TEMPERATURE SIGNAL (OVT#).....	11
6.11.   FAN SPEED MONITORING AND CONTROL .....	12
FAN SPEED CONTROL.....	12
FAN SPEED CONTROL MECHANISM.....	13
PWMOUT DUTY-CYCLE OPERATING PROCESS .....	14
6.12.   FAN_FAULT#.....	15
6.13.   SMI# .....	15
TEMPERATURE .....	15
VOLTAGE .....	16
FAN .....	16
6.14.   VOLT_FAULT# (VOLTAGE FAULT SIGNAL).....	17
<b>7. PCB LAYOUT GUIDE.....</b>	<b>17</b>
<b>8. REGISTER DESCRIPTION.....</b>	<b>18</b>
8.1.    CONFIGURATION REGISTER — INDEX 00H.....	18

8.2.	CONFIGURATION REGISTER — INDEX 01H.....	19
8.3.	CONFIGURATION REGISTER — INDEX 02H.....	19
8.4.	CONFIGURATION REGISTER — INDEX 03H.....	20
8.5.	SERIAL BUS ADDRESS REGISTER — INDEX 04H .....	20
8.6.	VALUE RAM — INDEX 10H- 2FH .....	21
8.7.	IRQ/SMI# ENABLE REGISTER 1 — INDEX 30H.....	22
8.8.	INTERRUPT STATUS REGISTER 1 — INDEX 31H.....	23
8.9.	REAL TIME STATUS REGISTER 1 — INDEX 32H.....	23
8.10.	IRQ/SMI# ENABLE REGISTER 2 — INDEX 33H.....	24
8.11.	INTERRUPT STATUS REGISTER 2 — INDEX 34H.....	24
8.12.	VOLT_FAULT/OVT REAL TIME STATUS REGISTER — INDEX 35H.....	25
8.13.	REAL TIME FAULT STATUS REGISTER 1 — INDEX 36H.....	26
8.14.	CHIPID(1) REGISTER – INDEX 5AH .....	26
8.15.	CHIPID(2) REGISTER – INDEX 5BH .....	26
8.16.	VENDOR ID(1) REGISTER – INDEX 5DH.....	26
8.17.	VENDOR ID(2) REGISTER – INDEX 5EH .....	26
8.18.	SMART FAN LOOKUP TABLE HYSTERESIS AND FAN MODE REGISTER -- INDEX 60H.....	26
8.19.	FAN FAULT TIME REGISTER -- INDEX 61H.....	28
8.20.	BOUNDARY HYSTERESIS REGISTER -- INDEX 62H.....	28
8.21.	FAN FULL TIME REGISTER -- INDEX 63H.....	28
8.22.	FILTER FAST UPDATE VALUE REGISTER -- INDEX 67H.....	29
8.23.	PWMOUT1 START UP DUTY-CYCLE — INDEX 69H.....	29
8.24.	T1 OVT TARGET TEMPERATURE HIGH— INDEX 6AH .....	30
8.25.	T1 OVT TARGET TEMPERATURE LOW — INDEX 6BH.....	30
8.26.	T2 OVT TARGET TEMPERATURE HIGH— INDEX 6CH .....	30
8.27.	T2 OVT TARGET TEMPERATURE LOW — INDEX 6DH .....	30
8.28.	FAN1 FULL SPEED COUNT REGISTER 0 — INDEX 70H .....	30
8.29.	FAN1 FULL SPEED COUNT REGISTER 1— INDEX71H.....	30
8.30.	FAN1 EXPECT COUNT REGISTER-- INDEX 74H .....	31
8.31.	FAN1 EXPECT COUNT REGISTER-- INDEX 75H .....	31
8.32.	FAN1 PWM_DUTY -- INDEX 76H .....	31
8.33.	FAN2 FULL SPEED COUNT REGISTER 0 — INDEX 80H .....	31
8.34.	FAN2 FULL SPEED COUNT REGISTER 1— INDEX81H.....	31
8.35.	FAN2 EXPECT COUNT REGISTER-- INDEX 84H .....	32
8.36.	FAN2 EXPECT COUNT REGISTER-- INDEX 85H .....	32
8.37.	FAN2 PWM_DUTY -- INDEX 86H .....	32
8.38.	GPIOX OUTPUT CONTROL REGISTER – INDEX 90H .....	32

8.39.	GPIOx OUTPUT DATA REGISTER – INDEX 91H .....	33
8.40.	GPIO1x INPUT STATUS REGISTER – INDEX 92H .....	33
8.41.	VT1 BOUNDARY 1 TEMPERATURE – INDEX A0H.....	33
8.42.	VT1 BOUNDARY 2 TEMPERATURE – INDEX A1H.....	34
8.43.	VT1 BOUNDARY 3 TEMPERATURE – INDEX A2H.....	34
8.44.	VT1 BOUNDARY 4 TEMPERATURE – INDEX A3H.....	34
8.45.	FAN1 SEGMENT 1 SPEED COUNT – INDEX A4H.....	35
8.46.	FAN1 SEGMENT 2 SPEED COUNT – INDEX A5H.....	35
8.47.	FAN1 SEGMENT 3 SPEED COUNT – INDEX A6H.....	35
8.48.	FAN1 SEGMENT 4 SPEED COUNT – INDEX A7H.....	35
8.49.	FAN1 SEGMENT 5 SPEED COUNT – INDEX A8H.....	36
8.50.	VT2 BOUNDARY 1 TEMPERATURE – INDEX B0H.....	36
8.51.	VT2 BOUNDARY 2 TEMPERATURE – INDEX B1H.....	36
8.52.	VT2 BOUNDARY 3 TEMPERATURE – INDEX B2H.....	37
8.53.	VT2 BOUNDARY 4 TEMPERATURE – INDEX B3H.....	37
8.54.	FAN2 SEGMENT 1 SPEED COUNT – INDEX B4H.....	37
8.55.	FAN2 SEGMENT 2 SPEED COUNT – INDEX B5H.....	38
8.56.	FAN2 SEGMENT 3 SPEED COUNT – INDEX B6H.....	38
8.57.	FAN2 SEGMENT 4 SPEED COUNT – INDEX B7H.....	38
8.58.	FAN2 SEGMENT 5 SPEED COUNT – INDEX B8H.....	38
<b>9.</b>	<b>ELECTRICAL CHARACTERISTIC.....</b>	<b>39</b>
9.1.	ABSOLUTE MAXIMUM RATINGS .....	39
9.2.	DC CHARACTERISTICS .....	39
9.3.	AC CHARACTERISTICS .....	40
<b>10.</b>	<b>ORDERING INFORMATION.....</b>	<b>41</b>
<b>11.</b>	<b>PACKAGE DIMENSIONS.....</b>	<b>41</b>
<b>12.</b>	<b>F75387 APPLICATION CIRCUIT .....</b>	<b>43</b>
<b>13.</b>	<b>F75387 APPLICATION CIRCUIT &amp; CO-LAYOUT CIRCUIT WITH F75375 .....</b>	<b>45</b>

## 1. General Description

F75387 is a system hardware monitoring and automatic fan speed controlling IC specific designed for graphic cards and PC etc. The F75387 can monitor several critical hardware parameters of the system, including voltages, temperatures and fan speeds which are very important for the system to work stably and properly.

An 11-bit analog-to-digital converter (ADC) was built inside F75387. The chip can monitor up to 4 analog voltage inputs, 2 fan tachometer inputs and 3 temperature inputs (2 remote and 1 local sensor). The remote temperature sensor can be performed by thermistor, transistor 2N3906 and CPU/GPU thermal diode. The F75387 can provide automatic fan speed control so that the system can operate at the minimum acoustic noise. This chip supports not only PWM duty mode (PWMOOUT) but also linear mode (DACOUT 0 ~ 3.3V) for fan speed control. Internal oscillator was built in this chip and user can use external clock input if users need accurate fan speed count. Also the users can set up the upper and lower limits (alarm thresholds) of all monitored parameters and the F75387 can also issue warning messages for system protection when there is something wrong with monitored items.

Through the BIOS or application software, the users can read all the monitored parameters of system all the time. And a pop-up warning can be also activated when the monitored item was out of the proper/pre-setting range. The F75387 is in the package of 16-pin SOP/SSOP and powered by 3.3V

## 2. Features

- 4 voltage inputs
- Provide 2 remote and 1 on-chip local temperature sensor
- Up to 2 fan speed monitoring inputs and 2 automatic fan speed control
- Power on fan speed configuration
  - 100% or 60% PWM duty cycle output on initial
  - 3.3V or 1.98V DAC linear voltage output on initial
- Provide up to 4 system protection signals
  - OVT#, SMI#, FAN\_FAULT#, VOLT\_FAULT# signals
- Programmable limited and setting points(alert threshold) for all monitored items
- 2-wire SMBus interface
- V<sub>CC</sub>3V operation and 16-SOP/SSOP package(150mil)

*Noted: Patented TW207103 TW207104 TW220442 US6788131 B1 TW235231 TWI263788*

### 3. Key Specifications

- |                                     |                           |
|-------------------------------------|---------------------------|
| ■ Supply Voltage                    | 3.0V to 3.6V              |
| ■ Measured Range                    | 0 ~ 145 °C                |
| ■ Remote Diode Temperature Accuracy | ±1°C from +60°C to +100°C |
| ■ Local Temperature Accuracy        | ±3°C from +60°C to +100°C |

### 4. Pin Configuration

FANIN1	1	16	VCC
GPIO0/FANIN2/VOLT_FAULT#	2	15	D1+
PWM1/DAC1/ADDR_TRAP	3	14	D2+
GPIO1/PWM2/DAC2/FAN_FAULT#	4	13	VREF
GPIO2/SMI#/FAN_FAULT#/LED	5	12	VIN1
OVT#/VOLT_FAULT#/GPIO3/CLKIN	6	11	VIN2
SCLK	7	10	VIN3
SDATA	8	9	GND

### 5. Pin Description

- I/OOD<sub>12t</sub> - TTL level bi-directional pin, can select to OD or OUT by register, with 12 mA source-sink capability.
- I/OOD<sub>16t</sub> - TTL level bi-directional pin, can select to OD or OUT by register, with 16 mA source-sink capability.
- I/OD<sub>12ts5V</sub> - TTL level bi-directional pin and schmitt trigger, Open-drain output with 12 mA sink capability, 5V tolerance.
- I/O<sub>8t</sub> - TTL level bi-directional pin with 8 mA sink capability.
- I/O<sub>8t-u47,5V</sub> - TTL level bi-directional pin with 8 mA sink capability, pull-up 47k ohms, 5V tolerance.
- I/O<sub>12ts5V</sub> - TTL level bi-directional pin and schmitt trigger with 12 mA sink capability, 5V tolerance.
- O<sub>12</sub> - Output pin with 12 mA source-sink capability.
- AOUT - Output pin(Analog).
- OD<sub>12</sub> - Open-drain output pin with 12 mA sink capability.
- IN<sub>ts5V</sub> - TTL level input pin and schmitt trigger, 5V tolerance.
- AIN - Input pin(Analog).
- P - Power.

**5.1. Power Pin**

Pin No.	Pin Name	Type	Description
16	VCC	P	3.3V power supply voltage input
9	GND(D-)	P	GND(If sensing CPU/GPU thermal diode or transistor 3906, please connect D- to this pin. Refer to application circuit)

**5.2. Monitoring Items and Fan Speed Control**

Pin No.	Pin Name	Type	Description
1	FANIN1	IN <sub>ts5V</sub>	0V to +3.3V amplitude fan tachometer input.
2	GPIO0(Default)	IN <sub>ts5V</sub> /OD <sub>12</sub>	General purpose I/O pin. Default Open drain
	FANIN2	IN <sub>ts5V</sub>	0V to +3.3V amplitude fan tachometer input.
	VOLT_FAULT#	OD <sub>12</sub>	This pin will be a logic <b>LOW</b> when the voltage exceeds its limit.
3	PWM1(Default)	OOD <sub>12</sub>	Use PWM duty cycle to control fan1 speed.
	DAC1	AOUT	Use linear voltage output (0~3.3V) to control fan1 speed.
	ADDR_TRAP	IN <sub>ts5V</sub>	Address power on trapping pin. Internal weak pull down. The internal pull-down resistor will be turn-off after power-on trapping. For detail description, please see register description index01h
4	GPIO1(Default)	IN <sub>ts5V</sub> /OD <sub>12</sub>	General purpose I/O pin. Default Open drain
	PWM2	OOD <sub>12</sub>	Use PWM duty cycle to control fan2 speed.
	DAC2	AOUT	Use linear voltage output (0~3.3V) to control fan2 speed.
	FAN_FAULT#	OD <sub>12</sub>	This pin will be a logic <b>LOW</b> when the fan speed is abnormal.
10	VIN3	AIN	0V to 2.048V FSR Analog Inputs
11	VIN2	AIN	0V to 2.048V FSR Analog Inputs
12	VIN1	AIN	0V to 2.048V FSR Analog Inputs
14	D2+	AIN	Thermistor / transistor 2N3906/ CPU/GPU thermal diode terminal input
15	D1+	AIN	Thermistor / transistor 2N3906/ CPU/GPU thermal diode terminal input

**5.3. Alert Signals and Others**

Pin No.	Pin Name	Type	Description
5	GPIO2(Default)	IN <sub>ts5V</sub> /OD <sub>12</sub>	General purpose I/O function. Default pure open drain
	SMI#	OD <sub>12</sub>	System management interrupt (Pure open drain). This pin will be active low when there is something wrong with voltage, temperature and fan. See register description index 33h
	VOLT_FAULT#	OD <sub>12</sub>	Active-Low output. This pin will be a logic <b>LOW</b> when the voltage exceeds its limit.

6	OVT#(Default)	OD <sub>12</sub>	Default open drain active-low output. This pin will be a logic <b>LOW</b> when the temperature exceeds its limit. Default output enable when the temperature exceeds 100°C on initial.
	GPIOS	IN <sub>1.5V</sub> /OD <sub>12</sub>	General purpose I/O function.
	FAN_FAULT#	OD <sub>12</sub>	This pin will be a logic <b>LOW</b> when the fan speed is abnormal.
	CLKIN	IN <sub>1.5V</sub>	24MHz/48MHz external clock input for chip operation source. When input external clock, the fan speed count will be more accurate.
13	VREF	AOUT	Reference voltage 2.304V.
8	SDATA	IN <sub>1.5V</sub> /OD <sub>12</sub>	Serial bus data
7	SCLK	IN <sub>1.5V</sub>	Serial bus clock

## 6. Function Description

### 6.1. General Description

F75387 is a system hardware monitoring and automatic fan speed controlling IC specific designed for graphic cards and PC etc. An 11-bit analog-to-digital converter (ADC) was built inside F75387. The chip can monitor up to 4 analog voltage inputs, 2 fan tachometer inputs and 3 temperature inputs (2 remote and 1 local sensor). The remote temperature sensor can be performed by thermistor, transistor 2N3906 and CPU/GPU thermal diode. The F75387 can provide automatic fan speed control so that the system can operate at the minimum acoustic noise. This chip supports not only PWM duty mode (PWMOUT) but also linear mode (DACOUT) for fan speed control. Also the users can set up the upper and lower limits (alarm thresholds) of all monitored parameters and the F75387 can also issue warning messages for system protection when there is something wrong with monitored items.

### 6.2. Analog Inputs

For the 11-bit ADC has the 8mv LSB, the maximum input voltage of the analog pin is 2.048V. Therefore the voltage under 2.048V (ex:1.5V) can be directly connected to these analog inputs. The voltage higher than 2.048V should be reduced by a factor with external resistors so as to obtain the input range. Only 3Vcc is an exception for it is main power of the F75387. Therefore 3Vcc can directly connect to this chip's power pin and need no external resistors. There are two functions in this pin with 3.3V. The first function is to supply internal analog power of the F75387 and the second function is that this voltage with 3.3V is connected to internal serial resistors to monitor the +3.3V voltage. The internal serial resistors are two 150K ohm, so that the internal reduced voltage is half of +3.3V.

There are four voltage inputs in the F75387 and the voltage divided formula is shown as follows:

$$VIN = V_{+12V} \times \frac{R_2}{R_1 + R_2} \quad \text{where } V_{+12V} \text{ is the analog input voltage, for example.}$$

If we choose  $R_1=27K$ ,  $R_2=5.1K$ , the exact input voltage for  $V_{+12V}$  will be 1.907V, which is within the tolerance.

As for application circuit, it can be refer to the figure as follows.

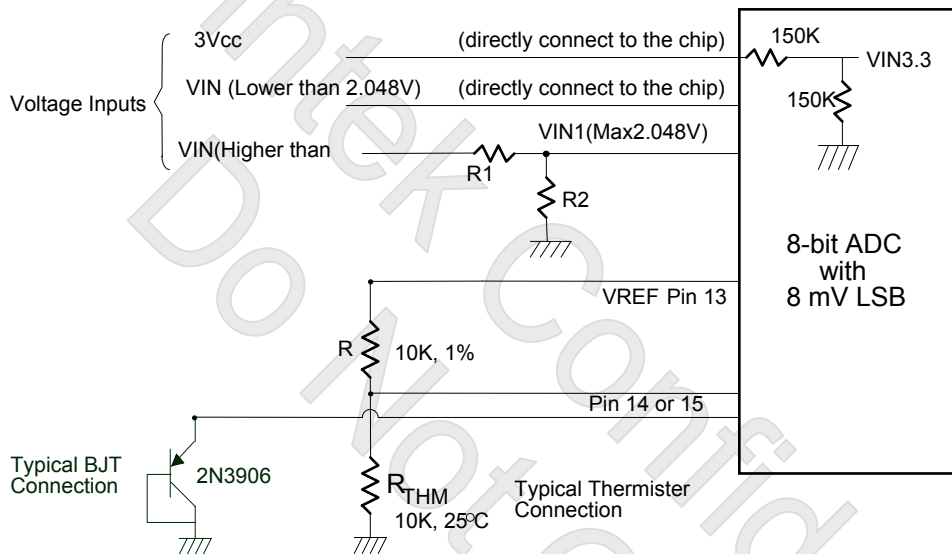


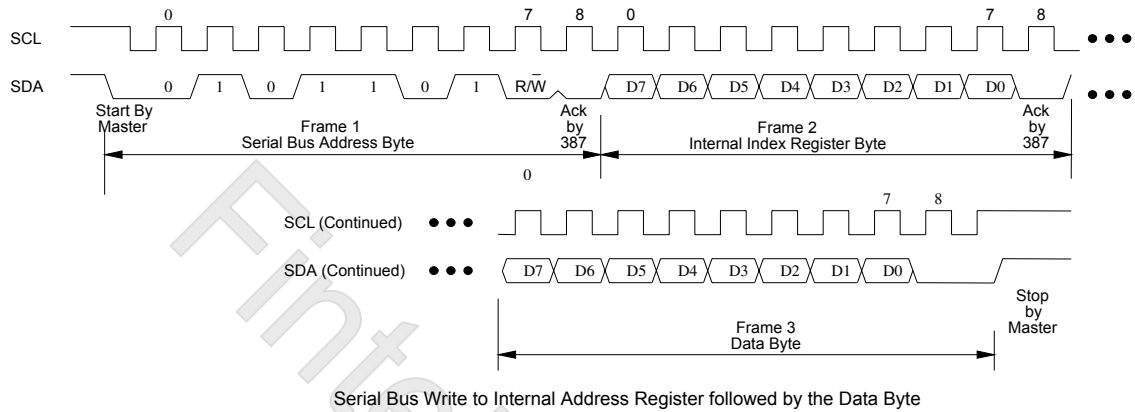
Figure 6-1

### 6.3. Access Interface and Power On Configuration

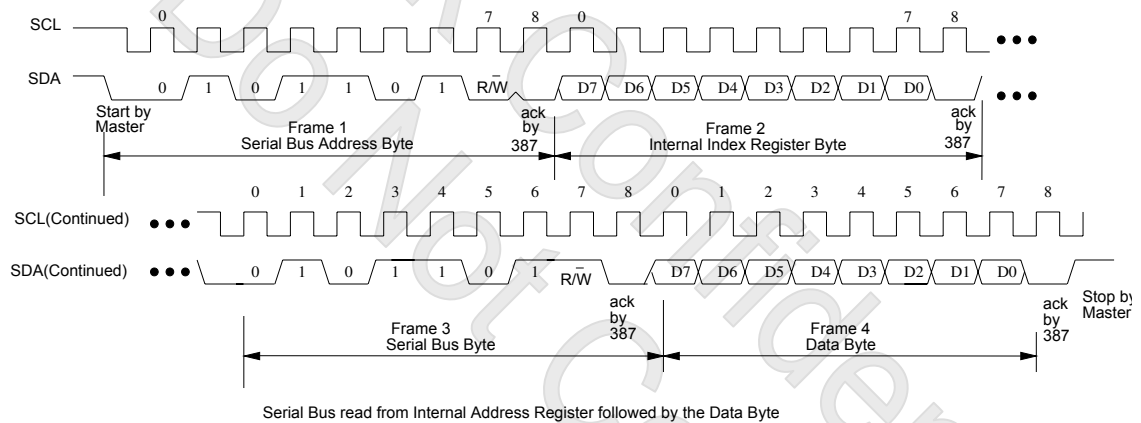
The F75387 provides one serial access interface, Serial Bus, to read/write internal registers. The address of Serial Bus is configurable by using power-on trapping. The pin 3 (PWM1/DAC1/ADDR\_TRAP) is a multi-function pin. During power-on, this pin serves as input detection of logic high or logic low. Two Serial Bus address 0x5A (0101\_1010) and 0x5C (0101\_1100) can be selected by changing external pull-up resistors. Following table indicates the configuration:

External Resistor	NC	200K	10K	2.2K
Address	0x5A	0x5C	0x5C	0x5A
Power On PWM Duty Cycle/DAC Linear Voltage Output	60% x 3VCC	100% x 3VCC	60% Duty Cycle	100% Duty Cycle
Power On DACOUT/PWMOUT Mode	DACOUT	DACOUT	PWMOUT	PWMOUT

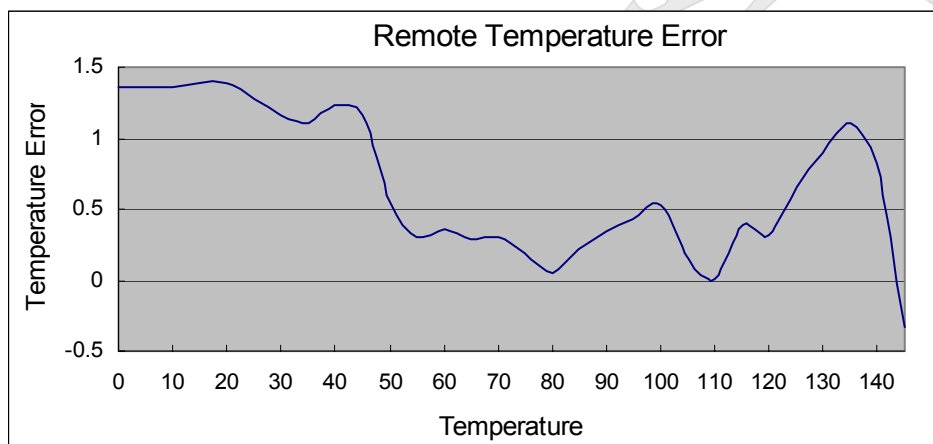
(a) *Serial bus write to internal address register followed by the data byte*



**(b) Serial bus read form internal address register followed by the data byte**



**6.4. Typical operating characteristic**



**6.5. Temperature monitoring**

The F75387 monitors a local and a remote temperature sensor. Both can be measured from 0°C to 140.875°C.

The temperature format is as the following table:

Temperature ( High Byte )	Digital Output	Temperature ( Low Byte )	Digital Output
0°C	0000 0000	0°C	000 0 0000
1°C	0000 0001	0.125°C	001 0 0000
25°C	0001 1001	0.250°C	010 0 0000
50°C	0011 0010	0.375°C	011 0 0000
75°C	0100 1011	0.500°C	100 0 0000
90°C	0101 1010	0.625°C	101 0 0000
100°C	0110 0100	0.750°C	110 0 0000
140°C	1000 1100	0.875°C	111 0 0000

**Remote-sensor transistor manufacturers**

Manufacturer	Model Number
Panasonic	2SB0709 2N3906
Philips	PMBT3906

**6.6. Temperature Measurement Machine**

The temperature data format is 11-bit unsigned for thermal sensor. The 8-bit temperature data can be obtained by reading through two registers. The format of the temperature data is as below

Temperature	8-Bit Digital Output	
	11-Bit Binary	11-Bit Hex
+125.5°C	0111,1101.100	7D.8h
+25.125°C	0001,1001.001	19.2h
+2.875°C	0000,0010.111	02.Eh
+1.25°C	0000,0001.010	01.4h
+0.0°C	0000,0000	00.0h

**6.7. Monitor Temperature from thermistor**

The F75387 can connect two thermistors to measure environment temperature or remote temperature. The specification of thermistor should be considered to (1)  $\beta$  value is 3435K (2) resistor value is 10K ohm at 25°C. In the Figure 6-1, the thermistor is connected by a serial resistor with 10K ohm, then connected to VREF (pin13).