

# **F71862**

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## **Super Hardware Monitor + LPC I/O**

**Release Date: July, 2008**  
**Version: V0.27P**

**F71862 Datasheet Revision History**

Version	Date	Page	Revision History
0.10P	2006/04/21	-	Preliminary Version
0.20P	2006/06/14	-	Release Version
0.21P	2006/11/23	62	Modified the description of Wakeup Control Register 2Dh bit 7(SPI_CS1_EN)
0.22P	2007/7/6	-	Company readdress
0.23P	2007/9/21	-	Modify typesetting
0.24P	2008/1/30	97	Modify operating temperature
0.25P	2008/5/2	-	Modify power type of pin PWROK description
0.26P	2008/5/26	-	Add register description of ACPI register F4h/F5h bit7
0.27P	2008/7/14	104	Update application circuit (V0.11P)

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## 1. General Description

The F71862 is the featured IO chip for PC system. Equipped with one IEEE 1284 Parallel Port, two UART Ports, Hardware Keyboard Controller, Serial Peripheral Interface (SPI), SIR and one FDC. The F71862 integrated with hardware monitor, 9 sets of voltage sensor, 3 sets of creative auto-controlling fans and 3 temperature sensor pins for the accurate dual current type temp. measurement for CPU thermal diode or external transistors 2N3906. Others, the F71862 supports newest AMDSI and Intel PECE interfaces for temperature sensing.

The F71862 provides flexible features for multi-directional application. For instance, supports 4-In and 4-Out pins CPU VID controlling with offset implement., provides 30 GPIO pins (multi-pin), IRQ sharing function also designed in UART feature for particular usage and accurate current mode H/W monitor will be worth in measurement of temperature, provides 3 modes fan speed control mechanism included Manual Mode/Stage Auto Mode/Linear Auto Mode for users' selection.

The F71862 also integrated SPI interface. The SPI interface is for BIOS usage including bridge function and back up function. User can implement BIOS data in second flash to boot system when primary BIOS error. These features as above description will help you more and improve product value. Finally, the F71862 is powered by 3.3V voltage, with the LPC interface in the green package of 128-PQFP.

## 2. Feature List

### ● General Functions

- Comply with LPC Spec. 1.0
- Support DPM (Device Power Management), ACPI
- 4-VIDIN and 4-VIDOUT for Vcore use.
- Provides one FDC, two UARTs, Hardware KBC and Parallel Port
- H/W monitor functions
- SPI interface for BIOS usage
- Support AMD SID/SIC interface and Intel PECE interface
- 30 GPIO Pins for flexible application
- 24/48 MHz clock input
- Packaged in 128-PQFP and powered by 3.3VCC