eSB Series

Tiny Turbo Controller with Two Speech Channels

Product Specification

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ELAN MICROELECTRONICS CORP.

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Specification Revision History

Doc. Version	Revision Description	Date
1.0	eSB Series Initial Specification	2005/06/1
1.1	Added Auto Optional Oscillator	2005/11/15
1.2	Modify Oscillation Frequency	2006/03/22

1 General Description

The eSB series IC's are 4-bit microcontroller based sound processor with two-channel speech audio function. The series has a powerful 4-bit CPU that handles most of the speech functions. Wide range of sampling rates and diverse volume levels are supported. It provides a synthesized speech to obtain good quality speech as well as one 4-bit input port, three 4-bit I/O ports, and one 4-bit output port (applicable to eSB100/120/170/200/270/320). By programming through the microcontroller, applications, such as section combination, trigger mode, output control, keyboard matrix, and other logic functions are easily put into effect.

In addition to Sleep mode, the eSB series IC's also offer Green mode which allows continuous operation at reduced or very low power consumption. Normal operation resumes at a preset time.

The enhanced functions will facilitate users in creating a wide variety of devices with new fancy features.

2 Features

- System clock:
  - 4 MHz @ 2.2 volts ~ 5.1 volts
  - 6 MHz @ 2.4 volts ~ 5.1 volts
- Auto optional crystal oscillator or RC oscillator
- Input/Output ports
  - One Input port (P1) with software controlled pull low resistor
  - Three input / output ports (P2 ~ P4)**:**
    - ∗ P2 & P3 are software controlled with pull low resistor and wakeup function
    - ∗ P4 is applicable to eSB065/080/100/120/170/ 200/270/320
  - One output port (P5) applicable to eSB100/120/170/200/270/320
- Sleep mode to conserve power, less than 1μA @ 3V standby current
- Green mode for continuous operation at reduced or very low power consumption (less than 15μA @ 3V)
- 4 bits RISC type controller, each instruction takes 2 (90%) or 4 clock cycles
- 12 bits width per instruction, each instruction takes 1 (90%) or 2 words
- Total of 4 interrupts are available**:**
  - 2 interrupts for two speech channels operation
  - 1 interrupt for timer
  - 1 general purpose external interrupt

- 32K words maximum program address (except for eSB015 which has 28K words max).
- 8 total stacks
- 128 nibbles RAM
- Two channels can be arbitrarily assigned as speech Channel 1 or Channel 2
- PCM/ADPCM algorithm for speech synthesis, which is transparent to users
- 16 steps DA volume control for channel output
- Optional 8 bits PWM or 10 bits traditional current DA
- 4 Flash with volume level options**:** 1/2, 1/4, 1/8/, & 1/16

3 Parts List

IC Type	Time (sec)	Stack	Program Size (words)	ROM (bits)	RAM (bits)	I/O (x4)	Cryst /Rst	IR	Chan'l	DA
eSB015	15	8	28K	28K x 16	128 x 4	P1, P2, P3	Yes	Yes	2	1
eSB020	20	8	32K	32K x 16	128 x 4	P1, P2, P3	Yes	Yes	2	1
eSB030	30	8	32K	56K x 16	128 x 4	P1, P2, P3	Yes	Yes	2	1
eSB040	40	8	32K	64K x 16	128 x 4	P1, P2, P3	Yes	Yes	2	1
eSB065	65	8	32K	108K x 16	128 x 4	P1, P2, P3, P4	Yes	Yes	2	1
eSB080	80	8	32K	128K x 16	128 x 4	P1, P2, P3, P4	Yes	Yes	2	1
eSB100	100	8	32K	168K x 16	128 x 4	P1, P2, P3, P4, P5	Yes	Yes	2	1
eSB120	120	8	32K	192K x 16	128 x 4	P1, P2, P3, P4, P5	Yes	Yes	2	1
eSB170	170	8	32K	280K x 16	128 x 4	P1, P2, P3, P4, P5	Yes	Yes	2	1
eSB200	200	8	32K	320K x 16	128 x 4	P1, P2, P3, P4, P5	Yes	Yes	2	1
eSB270	270	8	32K	432K x 16	128 x 4	P1, P2, P3, P4, P5	Yes	Yes	2	1
eSB320	320	8	32K	512K x 16	128 x 4	P1, P2, P3, P4,P5	Yes	Yes	2	1

4 Applications

- Voice playback appliances
- Educational learning tools

5 Pin Descriptions

Symbol	I/O	Function Description
OSCI	I	Crystal Oscillator In / RC Oscillator In (Normal mode)
OSCO	O	Crystal Oscillator Out / RC Oscillator In (Green mode)
P1.0~3	I	Bits 0~3 of Port 1
P2.0~3	I/O	Bits 0~3 of Port 2
P3.0~3	I/O	Bits 0~3 of Port 3
P4.0~3	I/O	Bits 0~3 of Port 4 (Applicable to eSB065/080/100/120/170/200/270/320)
P5.0~3	O	Bits 0~3 of Port 5 (Applicable to eSB100/120/170/200/270/320)
VO1A	O	PWM Voice Output/Traditional DA
VO1B	O	PWM Voice Output
VDD0	I	Power
VSS0	I	Ground
RESETB	I	Reset pin (internal pull-high)
IRin	I	IR Receiver Pad
IRout	O	IR Transmission Pad
VDD1	I	Power
VSS1	I	Ground

6 Special Function Description

6.1 Green Mode

Green mode is a very useful feature for conserving power (see table below) and in extending the life span of batteries. With Green mode, it is possible to achieve continuous operation at reduced or very low power consumption (less than 15μA @ 3V) and to resume normal operation at a preset time.

Mode	Current Consumption	Suitable Usage Condition
Normal mode	Maximum of 3 mA @ V_{DD} = 3V	Complex computing, scenario flow control, high power consumption
Green mode	Maximum of 15 μA @ V_{DD} = 3V	Long (preset) continuous operation but with reduced or very low power consumption
Sleep mode	Maximum of 1 μA@ V_{DD} = 3V	Sleep (no operation) & wake-up only, to conserve power

6.2 Interrupt Mode

A total of four interrupts are available. Each interrupt can be enabled or disabled and the interrupt status can be checked thru their corresponding flags.

Interrupt	Set	Behavior
Speech Channel 1	1	8-bit resolution with pre-load counter
Speech Channel 2	1	8-bit resolution with pre-load counter
Timer	1	4 bits pre-load counter
External Interrupt	1	Occurs when P1.3 pad has a rising edge change

6.3 I/O Ports Description

The eSB series supports a total of five ports. Each port contains 4 bits. See Parts List (Section 3) to check which chip possesses which port.

Port 1: For Input application only with pull low resistor and wake-up mechanism. The pull low resistor can be enabled or disabled, and the wake-up mechanism is always available (enabled).

Port 2: Available as input or output as defined by the Control Register. When set to input status, the pull low resistor can be enabled or disabled. When set to output status, another register is used to select port to sink or drive outside the circuit. The port also has a wake-up mechanism which can be enabled or disabled in either input or output mode.

Port 3: Port 3 characteristics are the same as Port 2 except for the wake-up mechanism which can be enabled or disabled in input mode only.

Port 4: Applicable in input or output mode which is controllable through the Control Register. Both wake-up and pull low registers are not available under input mode. Thus, under input status, external signal cannot be set to floating state.

Port 5: For output application only.

Each of the above port configurations can be summarized as follows**:**

Port	Available	Controllable I/O Direction	Input Mechanism	
			Pull Low	Wake-up
Port 1	I	×	√ (c)	√
Port 2	I/O	√ (c)	√ (c)	√ (c)
Port 3	I/O	√ (c)	√ (c)	√ (c)
Port 4	I/O	√ (c)	×	×
Port 5	O	×	×	×

Legend: **I**: input; **I/O**: input/output; **O**: output
×: Not available
√: Available
(c): Can be enabled/disabled by register

6.3.1 Pull Low Structure of Ports 1, 2, and 3

The pull low resistor is only valid when ports are in input mode. Under input mode, a control register is used to enable or disable the pull low resistors.

A strong pull low resistor (100KΩ order) protects the pads from noise interference and is turned off to conserve power when pads status is "1" (High). The weak pull low resistor (1MΩ order) keeps the pads' default value at "0" (Low).

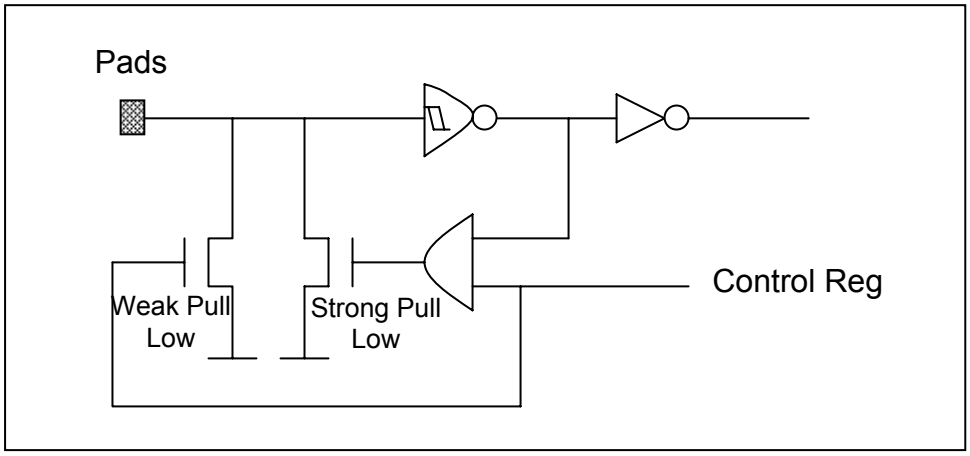

Figure 6-1 Ports 1, 2, & 3 Pull Low Structure

7 Specifications

7.1 Absolute Maximum Ratings

Parameter	Specification
Supply Voltage (V_{DDx} – Vssx)	-0.3V to +6.0V
Input Voltage	Vssx -0.3V to VDDx +0.3V
Operating Temperature	0°C to 70°C
Storage Temperature	-55°C to 125°C

7.2 Electrical Characteristics

■ **VDDx = 3V, VSSx = 0V, Ta = 25°C unless otherwise specified**

Items	Sym.	Min	Typ.	Max.	Unit	Condition
Operating Voltage	V_{DDx}	2.2	3.0	5.1	V	Fhigh = 4 MHz
	V_{DDx}	2.4	3.0	5.1	V	Fhigh = 6 MHz
Standby Current	I_{DDS}	-	-	1.0	μA	Sleep Mode, No Load
Operating Mode Current	I_{green}	-	10	15	μA	Green Mode, No Load (F = Flo = 32kHz)
	I_{op}	-	1.5	3	mA	No Load, D/A stop, (F = Fhigh = 4 / 6 MHz)
Drive Current of P2, P3, P4, P5, IRout	I_{OD}	2.0	5.0		mA	VOD = 2.4V
Sink Current of P2, P3, P4, P5, IRout	I_{OS}	2.3	6.0		mA	VOS = 0.4V
Input Current of P1, P2, P3, P4, IRin	I_{IH}	-	3.0	5	μA	
Output Current of VO1A	I_{VO1A}		3		mA	VVO1A = 0.7V (Traditional Current DA)
Output Current of VO1A, VO1B	$I_{VO1A/B}$		200		mA	Vvo1A/B=1/2 VDD (PWM DA)
Oscillation Resistor	R_{osch}	-	51.0	-	KΩ	Fhigh = 4 MHz
	R_{osch}	-	33.0	-	KΩ	Fhigh = 6 MHz
	R_{osclo}	-	1	-	MΩ	Flo = 32 kHz
Oscillation Frequency	F_{High}		4		MHz	Normal Mode, Rosc= 51KΩ
	F_{High}		6		MHz	Normal Mode, Rosc= 33KΩ
	F_{lo}		32		kHz	Green Mode