



### Features

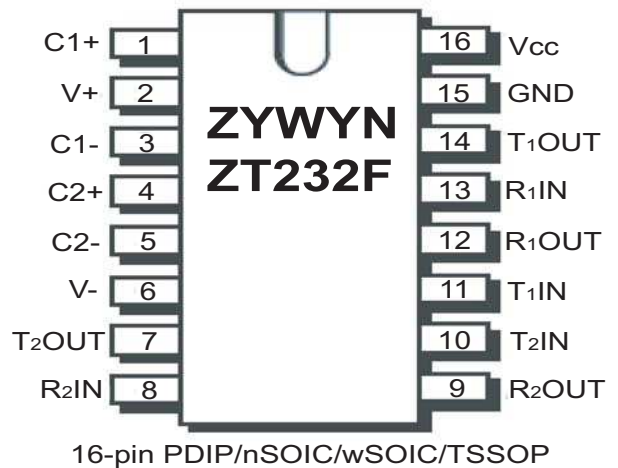
- Meets EIA/TIA-232F and CCITT V.28/V.24 specifications for  $V_{CC}$  at  $+5V \pm 10\%$
- Low Quiescent Current – 3mA typ., 5mA max.
- Low Shutdown Current (where applicable) - 1 $\mu$ A typical, 5 $\mu$ A max.
- Guaranteed High Data Rate 1,000kbps
- Proprietary Switch-Capacitor Regulated Voltage Converters (patent pending)
- Use Small 0.1 $\mu$ F Capacitors
- Wake Up Feature (where applicable) in Shutdown Mode
- Tri-State Receiver Outputs
- Latch-up Free
- ESD Protection for RS-232 I/O's  
 $\pm 15kV$  Human Body Model (HBM)
- Standard Data Rate at 250kbps Available on ZT232E Series

### General Description

The ZT232F series devices are +5V powered EIA/TIA-232 and CCITT V.28/V.24 communication interfaces with low power requirements. These transceivers consist of two line drivers, two line receivers and the proprietary switch-capacitor regulated voltage converters. The ZT310F and ZT312F feature a low power shutdown mode which draws as little current as 1 $\mu$ A typical with receiver outputs tri-stated and in wake-up. These devices operate from a single +5V power supply at the guaranteed high data rate of 1,000k bits/sec with enhanced electrostatic discharge (ESD) protection in all RS232 I/O pins exceeding  $\pm 15kV$  HBM.

### Applications

- Single Power Supply Applications
- Industrial and Embedded PCs
- Set Top Boxes
- Terminal Adapters
- POS terminals
- Peripherals Interface
- Routers and HUBs



### Product Selection Guide And Cross Reference

Part Number	# of RS232 Tx	# of RS232 Rx	# of Rx active in SD	# of 0.1 $\mu$ F caps	Shut Down	Wake Up	TTL Tri-State	Data Rate (kbps)	ESD HBM on RS232 I/O	Pin-to-Pin Cross EXAR	Pin-to-Pin Cross MAXIM
ZT202F	2	2	0	4	No	No	No	1000	$\pm 15kV$	N/A	N/A
ZT232F	2	2	0	4	No	No	No	1000	$\pm 15kV$	N/A	N/A
ZT310F	2	2	0	4	Yes	No	Yes	1000	$\pm 15kV$	N/A	N/A
ZT312F	2	2	2	4	Yes	Yes	Yes	1000	$\pm 15kV$	N/A	N/A



## Absolute Maximum Ratings

These are stress ratings only and functional operation of the device at these ratings or any other above those indicated in the operation sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods of time may affect reliability.

Power Supply, ( $V_{CC}$ ) ..... -0.3V to +6.0V  
 $V+$  ..... -0.3V to +7.0V  
 $V-+0.3V$  to -7.0V

$|V+| + |V-|$  ..... +13.0V

$I_{CC}$  (DC  $V_{CC}$  or GND current) .....  $\pm 100mA$

### Input Voltages

$TxIN$ , SHUTDOWN, EN ..... -0.3V to +6.0V

$RxIN$  .....  $\pm 25V$

### Output Voltages

$TxOUT$  .....  $\pm 12V$

$RxOUT$  ..... -0.3V to ( $V_{CC} + 0.3V$ )

### Short-Circuit Duration

$TxOUT$  ..... Continuous

Operating Temperature ..... -40°C to +85°C

Storage Temperature ..... -65°C to +150°C

### Power Dissipation Per Package

16-pin PDIP (derate 11.20mW/°C above +70°C) ..... 896mW

16-pin nSOIC (derate 10.00mW/°C above +70°C) ..... 720mW

16-pin wSOIC (derate 10.10mW/°C above +70°C) ..... 787mW

16-pin SSOP (derate 7.20mW/°C above +70°C) ..... 584mW

16-pin TSSOP (derate 6.80mW/°C above +70°C) ..... 556mW

18-pin PDIP (derate 12.60mW/°C above +70°C) ..... 962mW

18-pin wSOIC (derate 11.10mW/°C above +70°C) ..... 850mW

20-pin PDIP (derate 12.80mW/°C above +70°C) ..... 976mW

20-pin SSOP (derate 8.10mW/°C above +70°C) ..... 647mW

20-pin wSOIC (derate 11.10mW/°C above +70°C) .... 850mW

20-pin TSSOP (derate 7.20mW/°C above +70°C) ..... 584mW

## Storage Considerations

Storage in a low humidity environment is preferred. Large high density plastic packages are moisture sensitive and should be stored in Dry Vapor Barrier Bags. Prior to usage, the parts should remain bagged and stored below 40°C and 60%RH. If the parts are removed from the bag, they should be used within 168 hours or stored in an environment at or below 20%RH. If the above conditions cannot be followed, the parts should be baked for 12 hours at 125°C in order to remove moisture prior to soldering. Zywyn ships product in Dry Vapor Barrier Bags with a humidity indicator card and desiccant pack. The humidity indicator should be below 30%RH. The MSL of this product is 3.

The information furnished by Zywyn has been carefully reviewed for accuracy and reliability. Its application or use, however, is solely the responsibility of the user. No responsibility of the use of this information become part of the terms and conditions of any subsequent sales agreement with Zywyn. Specifications are subject to change without the responsibility for any infringement of patents or other rights of third parties which may result from its use. No license or proprietary rights are granted by implication or otherwise under any patent or patent rights of Zywyn Corporation.

## Electrical Characteristics

Unless otherwise stated,  $V_{CC} = +5.0V$ ,  $T_A = T_{min}$  to  $T_{max}$ ,  $C1$  to  $C4 = 0.1\mu F$ , typical values apply at  $V_{CC} = +5.0V$  and  $T_A = 25^\circ C$ .

Parameter	Condition	Min	Typ	Max	Units
TTL Logic Input TTL Logic Output RS-232 Input RS-232 Output Charge Pump Pin Power Pin	$T_1IN, T_2IN, \overline{EN}, \overline{SHDN}$ $R_1OUT, R_2OUT$ $R_1IN, R_2IN$ $T_1OUT, T_2OUT$ $C_1P, C_1N, C_2P, C_2N$ $V_{CC}, V_{GND}, V_{DD}, V_{SS}$	see specifications below			
Charge Pump Caps Temp $0^\circ C$ to $+70^\circ C$ Temp $-40^\circ C$ to $+85^\circ C$ $V_{CC}$ Voltage Range	$C_1P, C_1N, C_2P, C_2N$ Commercial Grade Industrial Grade $V_{CC} = +5.0V$ Supply	0.1 0 -40 4.5	0.1 +25 +25 5	1.0 +70 +85 5.5	$\mu F$ $^\circ C$ $^\circ C$ V
Supply Current Quiescent	TTL Inputs = $V_{CC}/GND$ , RS-232 Input = float, $T_A = 25^\circ C$ $V_{CC} = +5.0V \pm 10\%$ , No load on transmitter outputs (For ZT232F) (For ZT202F, ZT310F, ZT312F)		3 4	5 8	mA mA
Supply Current Transmitters Loaded	TTL Inputs = $V_{CC}/GND$ , RS-232 Inputs = float, $T_A = 25^\circ C$ $V_{CC} = +5.0V$ , All transmitter outputs loaded with $R_L = 3k\Omega$		15		mA
Supply Current, SHUTDOWN Enabled	$\overline{SHDN} = GND$ , TTL Inputs = $V_{CC}/GND$ , $T_A = 25^\circ C$ $\overline{RS-232}$ Inputs = float, $V_{CC} = +5.0V$ (For ZT310F/ZT312F)		1	5	$\mu A$
TTL LOGIC Input Input Threshold Low Input Threshold High Input Hysteresis Input Leakage Current	$V_{CC} = +5.0V$ Supply $T_1IN, T_2IN, \overline{EN}, \overline{SHDN}$ $T_1IN, T_2IN, \overline{EN}, \overline{SHDN}$ $T_1IN, T_2IN$ $T_xIN = GND$	2.4	0.5	0.8	V V V $\mu A$
TTL LOGIC Output Output Voltage Low Output Voltage High Output Leakage Current	$I_{OUT} = 3.2mA$ $I_{OUT} = -1.0mA$ $\overline{SHDN} = GND, \overline{EN} = V_{CC}; GND \leq V_{OUT} \leq V_{CC}$ (For ZT310F/ZT312F)	3.5	0.05	0.4	V V $\mu A$
Receiver Input Input Voltage Range Input Threshold Low Input Threshold High Input Hysteresis Input Resistance	$T_A = T_{min} - T_{max}$ $T_A = 25^\circ C, V_{CC} = 5.0V$ $V_{CC} = +5.0V$ Supply $T_A = 25^\circ C$ $V_{IN} = \pm 25V, T_A = 25^\circ C$	-25 0.8 0.2 3	1.2 1.7 0.5	25 2.4 1.0 7	V V V V k $\Omega$
Transmitter Output Output Voltage Swing Output Resistance Output Short-Circuit Current Output Leakage Current	$R_L = 3\sim 7k\Omega$ , All Outputs are loaded (For ZT232F) $R_L = 3\sim 7k\Omega$ , All Outputs are loaded, $V_{CC} = 5.25V$ (For ZT202F, ZT310F, ZT312F) $V_{CC} = V_{DD} = V_{SS} = GND, V_{OUT} = \pm 2V$ $V_{OUT} = GND$ Transmitter Disabled, $V_{OUT} = \pm 12V$	$\pm 5$ $\pm 5$ 300	$\pm 6$ $\pm 9$ $\pm 20$ $\pm 5$	$\pm 60$	V V $\Omega$ mA $\mu A$

## Electrical Characteristics

Unless otherwise stated,  $V_{CC} = +5.0V$ ,  $T_A = T_{min}$  to  $T_{max}$ ,  $C1$  to  $C4 = 0.1\mu F$ , typical values apply at  $V_{CC} = +5.0V$  and  $T_A = 25^\circ C$ .

Parameter	Condition	Min	Typ	Max	Units
Timing Characteristics					
Maximum Data Rate One Transmitter (1Tx/1Rx) Switching	$R_L = 3\sim 7k\Omega$ , $C_L = 50pF\sim 2500pF$ , $T_A = 25^\circ C$	1000			kbps
Transition-Region Slew Rate	$R_L = 3\sim 7k\Omega$ , $C_L = 50pF\sim 2500pF$ , One Transmitter Switching, $T_A = 25^\circ C$ , Measured from +3V to -3V or -3V to +3V, $V_{CC} = 4.5V$		90		V/ $\mu s$
Transmitter Propagation $t_{PLH}$ Transmitter Propagation $t_{PHL}$ Transmitter Skew Transmitter Output Enable Time Transmitter Output Disable Time	All transmitters loaded with $R_L = 3k\Omega$ , $C_L = 1000pF$ All transmitters loaded with $R_L = 3k\Omega$ , $C_L = 1000pF$ $t_{PHL} - t_{PLH}$ (For ZT310F/ZT312F) (For ZT310F/ZT312F)		2.0 2.0 100 0.4 0.25		$\mu s$ $\mu s$ ns $\mu s$ $\mu s$
Receiver Propagation $t_{PLH}$ Receiver Propagation $t_{PHL}$ Receiver Skew Receiver Output Enable Time Receiver Output Disable Time	$C_L = 150pF$ $C_L = 150pF$ $t_{PHL} - t_{PLH}$ (For ZT310F/ZT312F) (for ZT310F/ZT312F)		0.15 0.15 50 0.2 0.2		$\mu s$ $\mu s$ ns $\mu s$ $\mu s$
ESD Tolerance RS-232 I/Os ESD HBM			$\pm 15$		kV
TTL/CMOS I/Os ESD HBM			$\pm 2$		kV

$\overline{SHDN}$	$\overline{EN}$	Power Up/Down	Receiver Outputs
0	0	Down	Enable
0	1	Down	Tri-State
1	0	Up	Enable
1	1	Up	Tri-State

Table 1. Wake-Up Truth Table for ZT312F

## Circuit Description

### Proprietary Switch-Capacitor Regulated Voltage Converter

Different from other suppliers, Zywyn uses a patent pending switch-capacitor voltage-controlled source and sink current generators design to provide powerful bipolar voltages to maintain compliant EIA/RS232 levels regardless of power supply fluctuations. The design consists of an internal regulated oscillator, a two phase clock cycling, regulated complementary MOS switches, fast switching diode and switch capacitors.

The switch capacitor bi-directional current generators operate with Zywyn's proprietary smartly regulated complementary MOS switches and fast switching diode from its proprietary high voltage process technology. The efficiency of these bi-directional current generators is well over 70%. The switching frequency is generated by an internal oscillator and regulated by the current loads. The switch capacitor pump design delivers higher negative bucked voltage than the positive boosted voltage to achieve a balanced voltage controlled source and sink current generators resulting a balanced bipolar voltage supplies to the chip.

With its unique proprietary design technique, Zywyn's interface product series provide a better power efficient, stable and compliant EIA/RS232 levels with superior low power consumption.

### Controlled Enable and Power-Down

The ZT310F and ZT312F both feature an enable input, which allows the receiver outputs to be either tri-stated or enabled. This can be especially useful when the receiver is tied directly to a microprocessor data bus. For the ZT310F, enable is active low, in which a logic HIGH applied to the OFF pin will enable the receiver outputs. For the ZT310F, enable is active high in which a logic HIGH applied to the EN pin will enable the receiver outputs.

ZT310F and ZT312F have a low-power shutdown mode controlled by the ON/OFF pin for the ZT310F and the SHDN pin for the ZT312F. During shutdown the driver output and the switch-capacitor regulated voltage converter are disabled with the supply current falls to less than 1 $\mu$ A.

ZT312F includes a wakeup function that enables both receivers during a shutdown state. With only the receivers active during the shutdown state, the devices draw 5-10 $\mu$ A of supply current. A typical application is when a RS232 cable is connected or when the peripheral is enabled such as a modem, the devices will automatically become active again. After the supply voltage to the ZT312F reaches +5.0V, the SHDN pin can be disabled, taking the ZT312F out of the shutdown mode. All receivers that are active during shutdown maintain 500mV (typ.) of hysteresis.

### ESD Immunity

Electro-Static Discharge (ESD) is an important factor when implementing a serial port into a system. In some applications, it is crucial that the ESD protection for the system must meet a certain tolerance level. Since RS232 transceiver devices are exposed to the outside world, there are many environmental factors that can

effect the serial port and even subject it to transients that could potentially damage the transceiver itself.

The RS232 transceiver is usually routed from the serial port connector to the transceiver IC through the metal trace on the printed circuit board. This trace will have some small amount of resistance that will add some protection in terms of limiting transient current to the IC. However for added voltage protection, transient voltage suppressors (TVS) or transzorb, which are back-to-back diode arrays clamp, are usually necessary to protect the serial port circuitry.

To further reduce cost within their system, more engineers are requiring higher ESD tolerances from the transceiver ICs themselves without having to add costly TVS circuitry. Zywyn's RS232 transceivers includes built-in transient voltage suppression where external ESD circuitry is not necessary to meet the MIL-STD-883, Method 3015, Human Body Model and the EN61000-4-2 Air/Contact Discharge tests.

The Human Body Model has been the generally accepted ESD testing method for semiconductors. This test is intended to simulate the human body's potential to store electrostatic energy and discharge it to an integrated circuit upon close proximity or contact. This method will test the IC's capability to withstand an ESD transient during normal handling such as in manufacturing areas where the ICs tend to be handled frequently.

EN61000-4-2 is used for testing ESD on equipment and systems. For system manufacturers, they must guarantee a certain amount of ESD protection since the system itself is exposed to the outside environment and human presence. EN61000-4-2 specifies that the system is required to withstand an amount of static electricity when ESD is applied to exposed metal points and surfaces of the equipment that are accessible to personnel during normal usage. The transceiver IC receives most of the ESD current when the ESD source is applied to the connector pins.

There are two methods within EN61000-4-2, the Air Discharge method and the Contact Discharge method. With the Air Discharge Method, an ESD voltage is applied to the equipment under test through air, which simulates an electrically charged person ready to connect a cable onto the rear of the system and the high energy potential on the person discharges through an arcing path to the rear panel of the system before he or she even touches the system. The Contact Discharge Method applies the ESD current directly to the EUT. This method was devised to reduce the unpredictability of the ESD arc. The discharge current rise time is constant since the energy is directly transferred without the air-gap arc inconsistencies.

## RS232 Signal Characteristics

The charge pump voltage converter efficiently converts the necessary voltage for the driver's output transistors so that the RS232 output is close to the ideal rail voltage of 10V.

While loaded with a typical RS232 load, the driver's output level only drops 0.2V from its open circuit voltage. Zywyn's low-drop driver circuitry working with its efficient voltage regulator allows superior line driving capability while meeting the requirements of TIA/EIA-232-E.

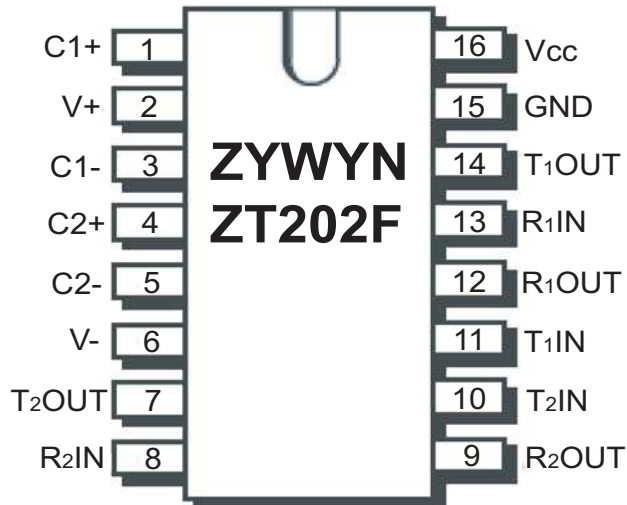
The drivers are inverting transmitters, which accept TTL or CMOS inputs and produces the RS-232 compliant signals that is inverted relative to the input logic levels. Typically the RS232 output voltage swing is  $\pm 6V$ . Even under the worst case loading conditions of 3kohms and 2500pF, the output is guaranteed to be  $\pm 5V$ , which adheres to the RS232 standard specifications. The transmitter outputs are protected against infinite short-circuits to ground without degradation in reliability. The instantaneous slew rate of the transmitter output is internally limited to a maximum of 30V/ $\mu s$  in order to meet the TIA/EIA-232-E requirements.

The receivers convert RS-232 input signals to inverted TTL signals. The inputs have a typical hysteresis margin of 500mV in order to account for signal degradation caused by system interference and other noise related disturbers. This ensures that the receiver is relatively immune to noisy transmission lines. The input thresholds are 0.8V minimum and 2.4V maximum, which are within the TIA/EIA-232 requirements. The receiver inputs are also protected against voltages up to  $\pm 25V$ . Should an input be left unconnected, a 5kohm pulldown resistor to ground will force the output of the receiver to a high state.

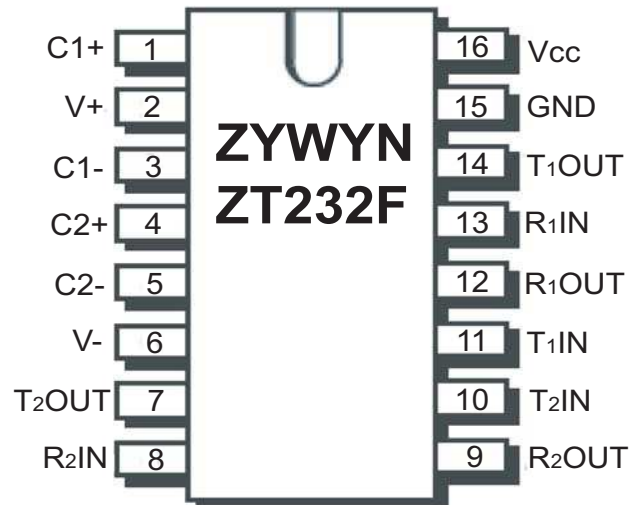
Specification	RS-232D	RS-423A	RS-422	RS-485	RS-562
Mode of Operation	Single-Ended	Single-Ended	Differential	Differential	Single-Ended
No. of Drivers and Receivers Allowed on One Line	1 Driver 1 Receiver	1 Driver 10 Receivers	1 Driver 10 Receivers	32 Drivers 32 Receivers	1 Driver 1 Receiver
Maximum Cable Length	50 feet	4,000 feet	4,000 feet	4,000 feet	$C \leq 2,500 \text{ pF@ } <20\text{kbps}$ ; $C \leq 1,000 \text{ pF@ } >20\text{kbps}$
Maximum Data Rate	20 kbps	100 kbps	10 Mbps	10 Mbps	64 kbps
Driver Output Maximum Voltage	$\pm 25V$	$\pm 6V$	-0.25V to +6V	-7V to +12V	-3.7V to +13.2V
Driver Output Signal Level					
Loaded	$\pm 5V$	$\pm 3.6V$	$\pm 2V$	$\pm 1.5V$	$\pm 3.7V$
Unloaded	$\pm 15V$	$\pm 6V$	$\pm 5V$	$\pm 5V$	$\pm 13.2V$
Driver Load Impedance	3 ~ 7K $\Omega$	450 $\Omega$	100 $\Omega$	54 $\Omega$	3 ~ 7K $\Omega$
Maximum Driver Output Current (High Impedance State)					
Power On				$\pm 100\mu A$	
Power Off	$V_{MAX}/300$	100 $\mu A$	$\pm 100\mu A$	$\pm 100\mu A$	
Slew Rate	30V/ $\mu s$ max.	Controls Provided			30V/ $\mu s$ max.
Receiver Input Voltage Range	$\pm 15V$	$\pm 12V$	-7V to +7V	-7V to +12V	$\pm 15V$
Receiver Input Sensitivity	$\pm 3V$	$\pm 200mV$	$\pm 200mV$	$\pm 200mV$	$\pm 3V$
Receiver Input Resistivity	3 ~ 7K $\Omega$	4K $\Omega$ min.	4K $\Omega$ min.	12K $\Omega$ min.	3 ~ 7K $\Omega$

**Table 2. EIA Standard Parameter Summary**

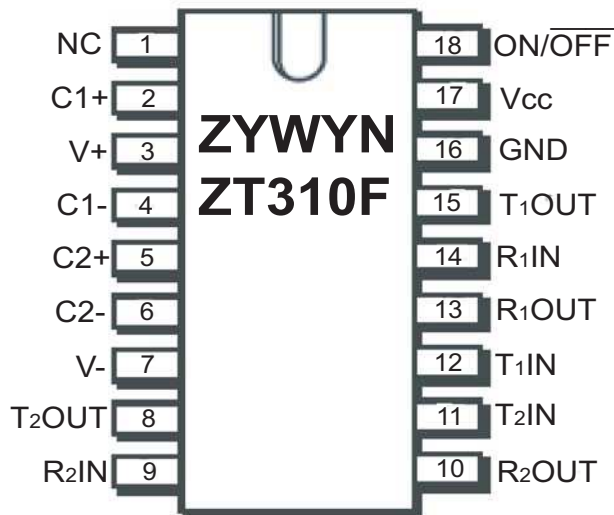
Pin Configuration



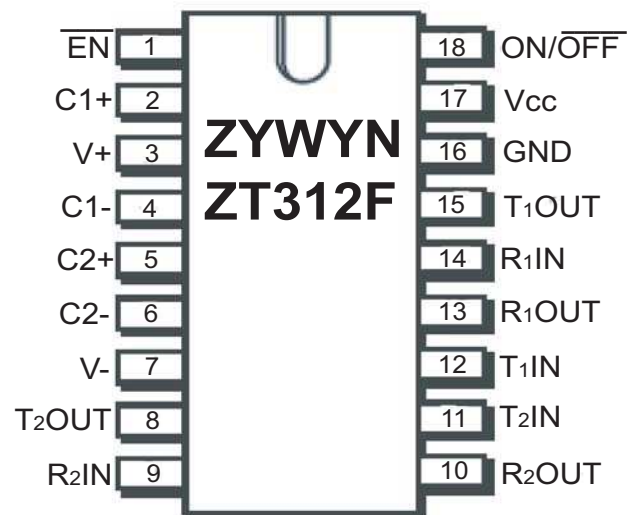
16-pin PDIP/nSOIC/wSOIC/TSSOP



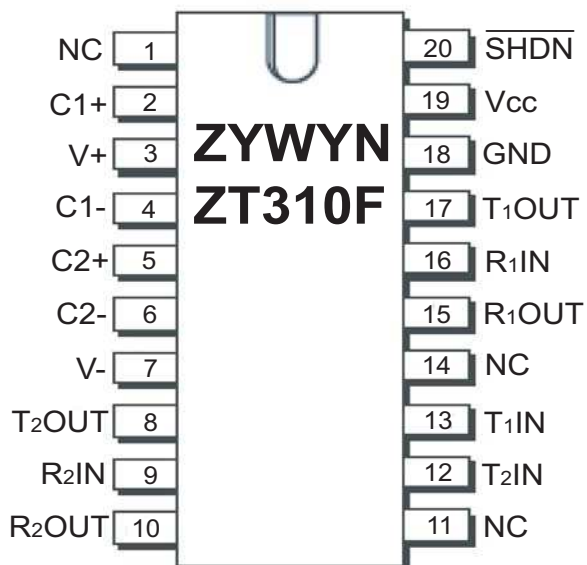
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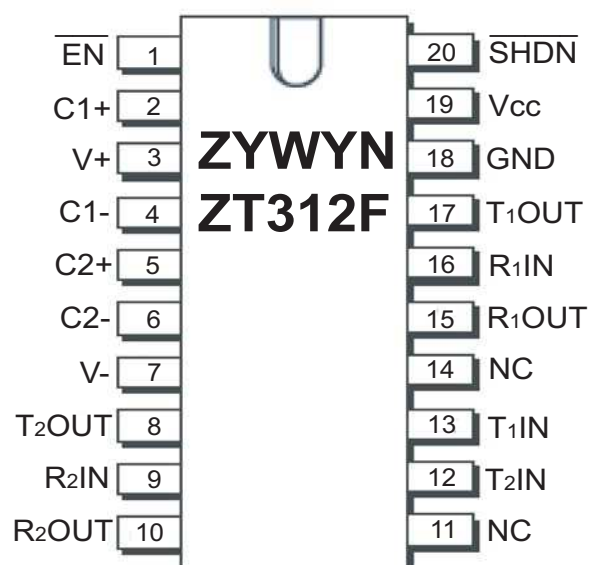
18-pin PDIP/wSOIC



18-pin PDIP/wSOIC

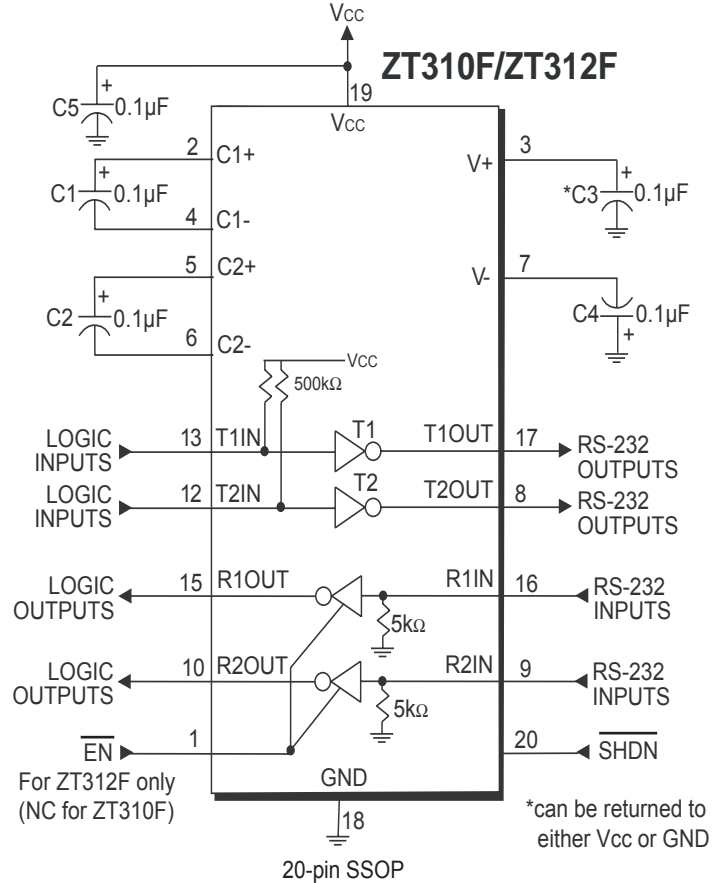
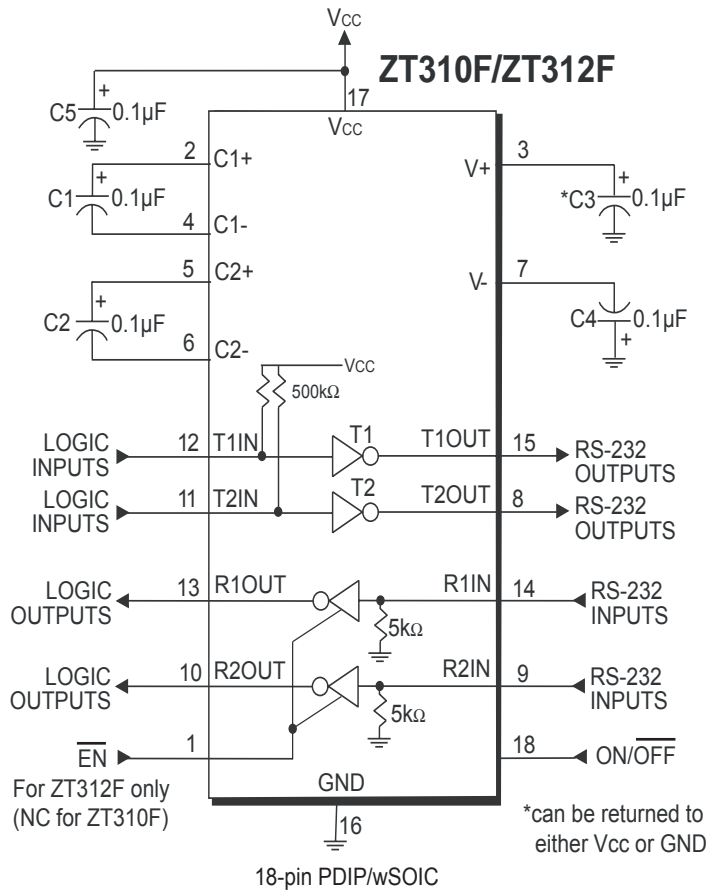
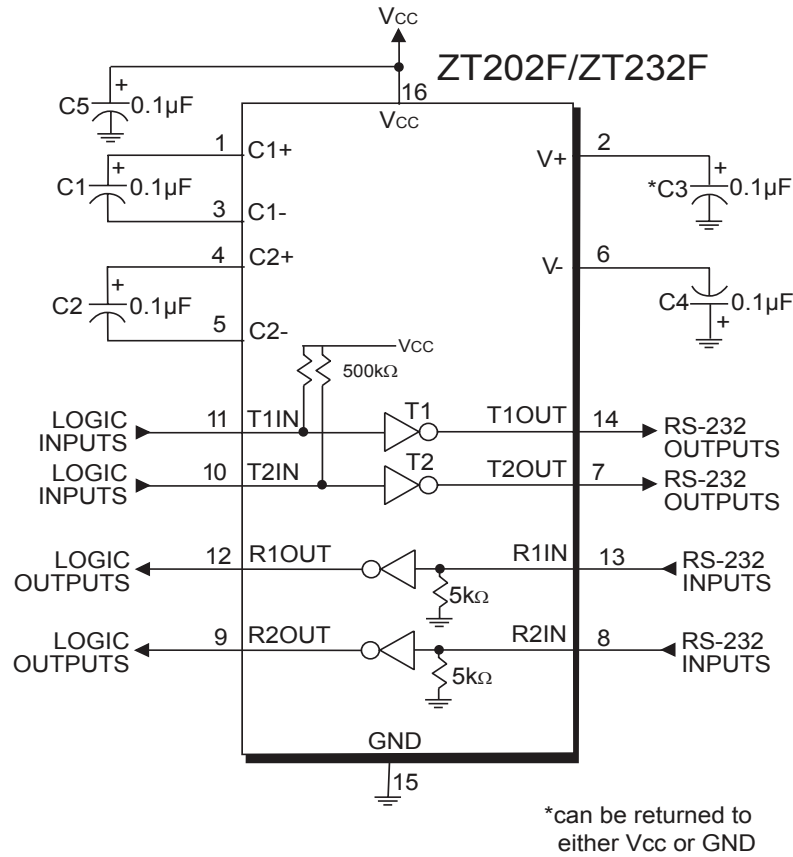


20-pin SSOP



20-pin SSOP

Typical Application Circuits



Package Information

**NOTE :**

1. CONTROLLING DIMENSION : INCH
2. LEAD FRAME MATERIAL : COPPER 194
3. DIMENSION "D" DOES NOT INCLUDE MOLD FLASH, TIE BAR BURRS AND GATE BURRS. MOLD FLASH, TIE BAR BURRS AND GATE BURRS SHALL NOT EXCEED 0.006[0.15mm] PER END. DIMENSION "E1" DOES NOT INCLUDE INTERLEAD FLASH. INTERLEAD FLASH SHALL NOT EXCEED 0.010[0.25mm] PER SIDE.
4. DIMENSION "b" DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.003[0.08mm] TOTAL IN EXCESS OF THE "b" DIMENSION AT MAXIMUM MATERIAL CONDITION. DAMBAR CANNOT BE LOCATED ON THE LOWER RADIUS OR THE FOOT. MINIMUM SPACE BETWEEN PROTRUSION AND AN ADJACENT LEAD TO BE 0.0028[0.07mm]
5. TOLERANCE : ±0.010[0.25mm] UNLESS OTHERWISE SPECIFIED.
6. OTHERWISE DIMENSION FOLLOW ACCEPTABLE SPEC.
7. REFERENCE DOCUMENT : JEDEC SPEC MS-012

SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.47	1.60	1.73	0.058	0.063	0.068
A1	0.10	—	0.25	0.004	—	0.010
A2	—	1.45	—	—	0.057	—
b	0.33	0.41	0.51	0.013	0.016	0.020
c	0.19	0.20	0.25	0.0075	0.008	0.0098
D	9.80	9.91	10.01	0.386	0.390	0.394
E	5.79	5.99	6.20	0.228	0.236	0.244
E1	3.81	3.91	3.99	0.150	0.154	0.157
e	—	1.27	—	—	0.050	—
L	0.38	0.71	1.27	0.015	0.028	0.050
y	—	—	0.076	—	0.003	—
Ø	0"	—	8"	0"	—	8"

**16-pin nSOIC**

CUSTOMER :		ZYWYN CORPORATION	
APPROVED BY :	DATE :	TITLE :	
DRAW BY: Monica Chen	11/09/09	16L SMALL OUTLINE PACKAGE DRAWING(0.150")	
CHECK BY: Leo Chen	11/10/09	DWG. NO. PO-SOP-003	REV. 0
APPROVAL: Paul Lau	11/10/09	UNIT : INCH	SCALE : 12/1
APPROVAL: Jack Su	11/11/09	SHEET 1 OF 1	

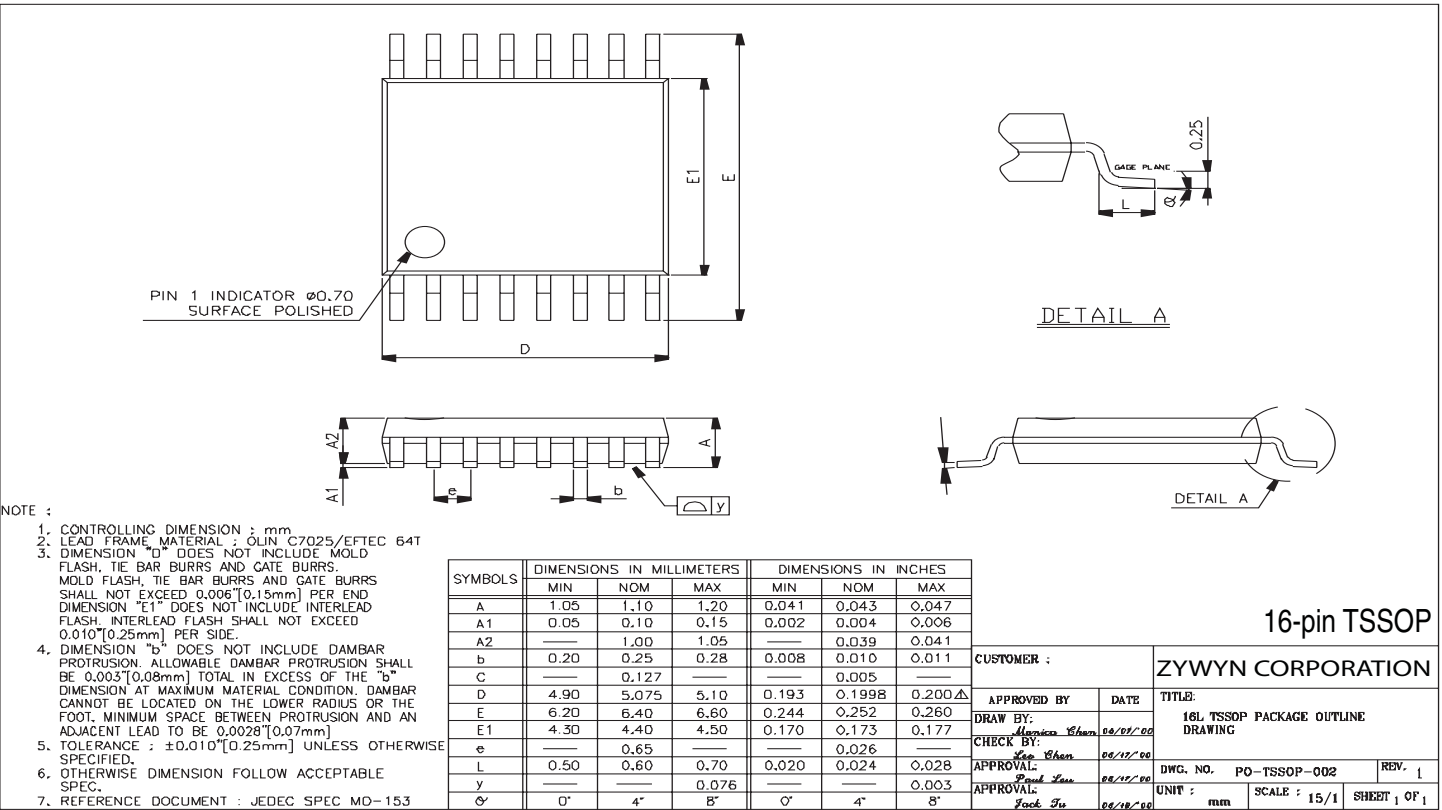
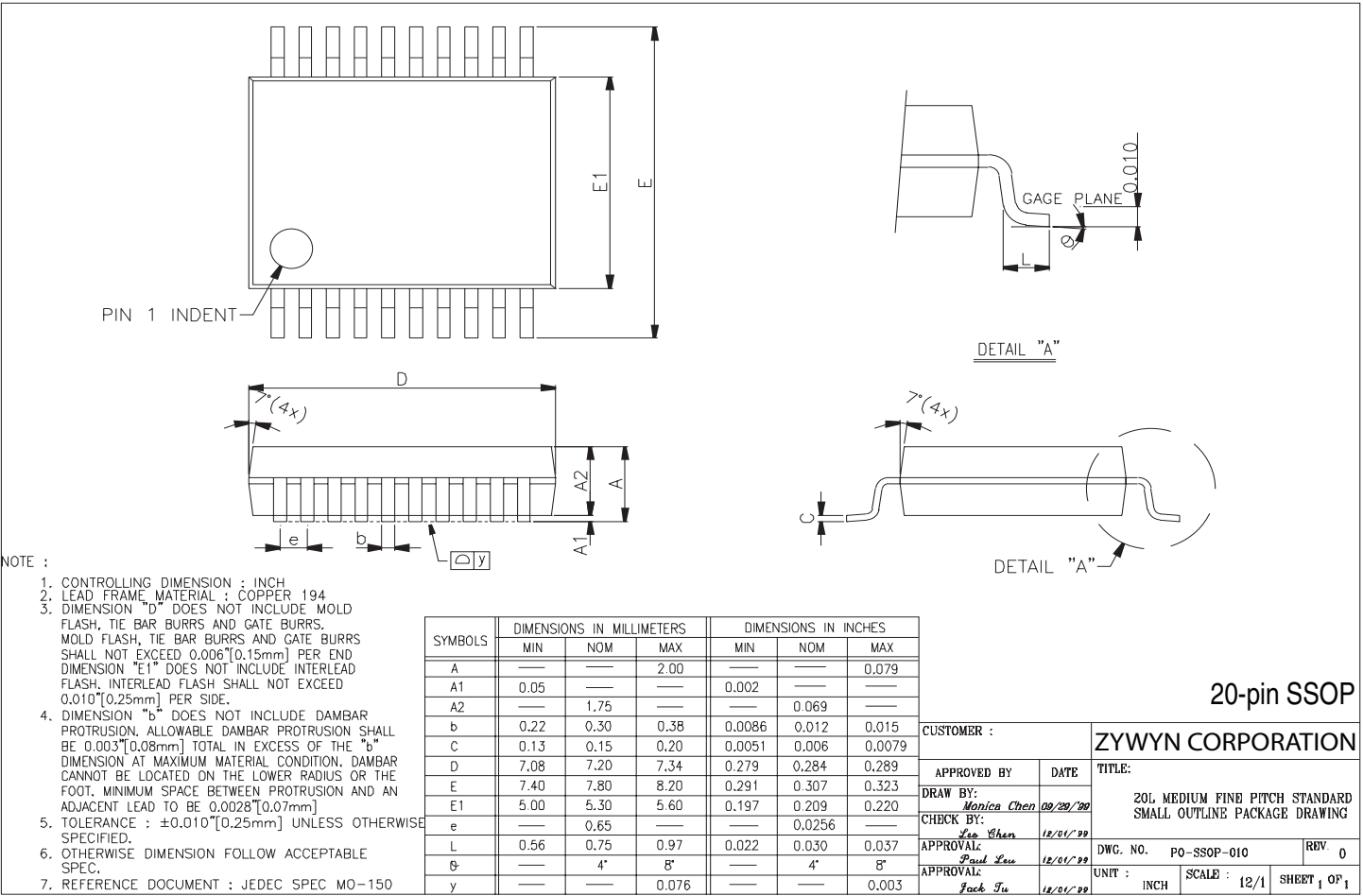
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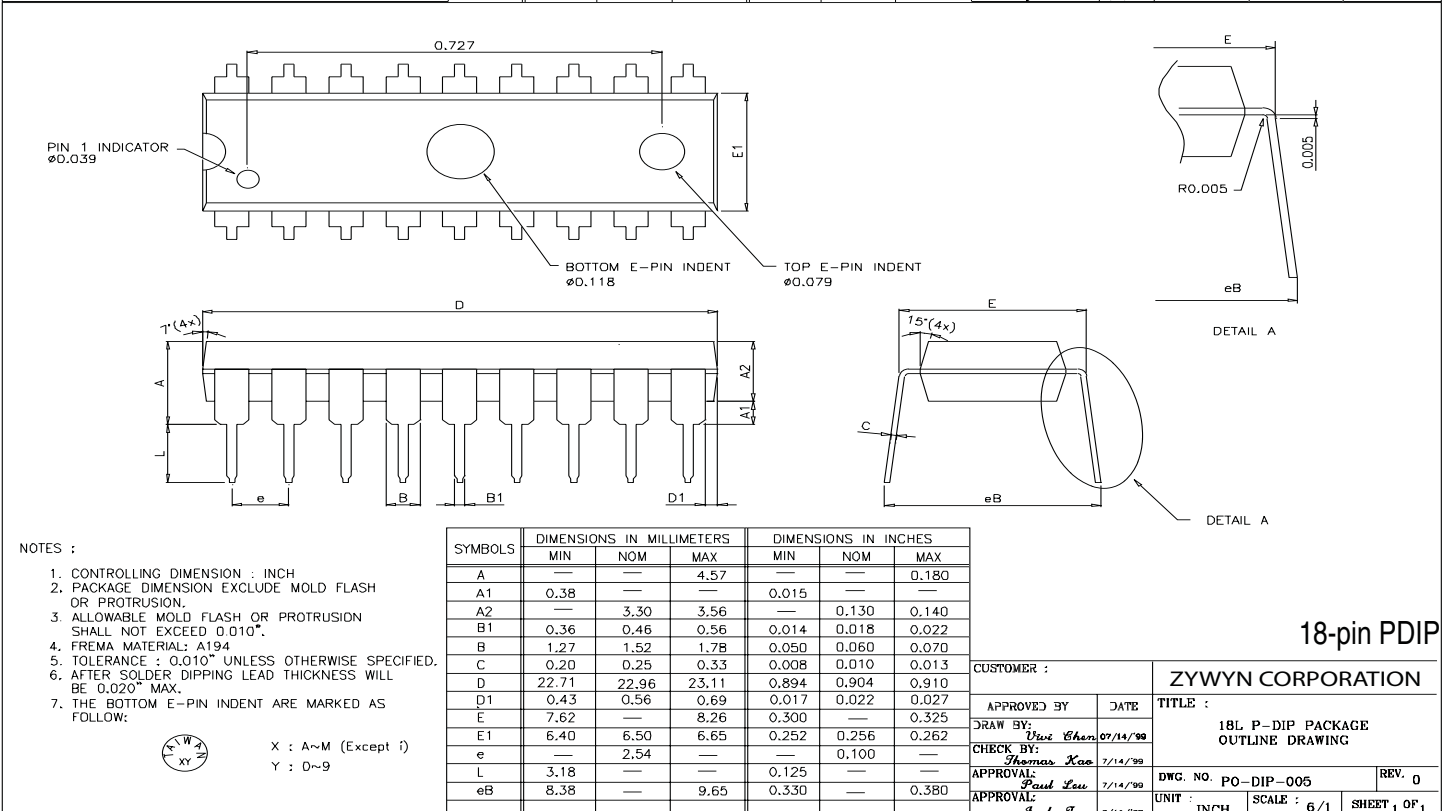
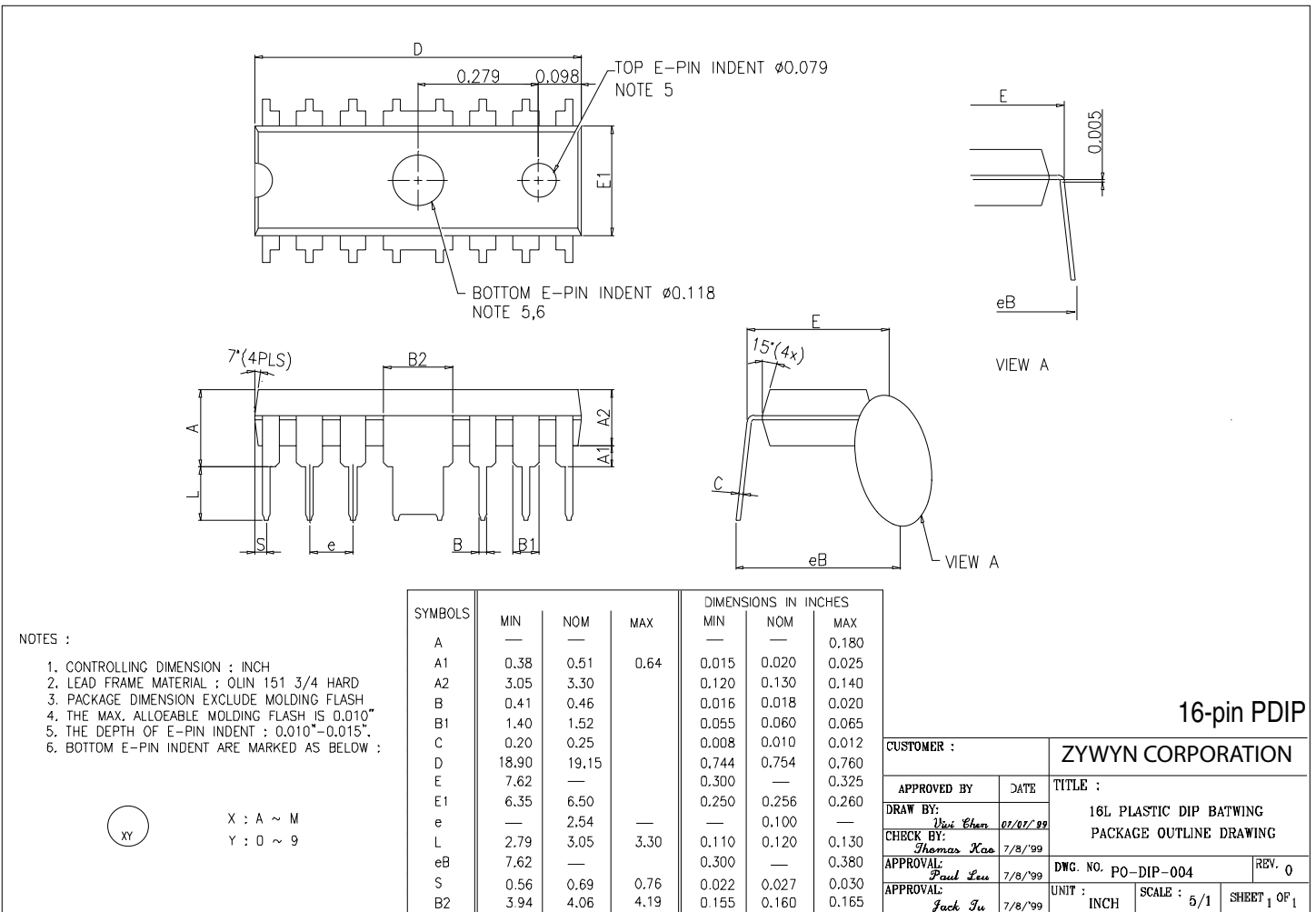
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5. TOLERANCE : ±0.010[0.25mm] UNLESS OTHERWISE SPECIFIED.
6. OTHERWISE DIMENSION FOLLOW ACCEPTABLE SPEC.
7. REFERENCE DOCUMENT : JEDEC SPEC MS-013

SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	2.36	2.49	2.64	0.093	0.098	0.104
A1	0.10	—	0.30	0.004	—	0.012
A2	—	2.31	—	—	0.091	—
b	0.33	0.41	0.51	0.013	0.016	0.020
c	0.18	0.23	0.28	0.007	0.009	0.011
D	10.08	10.31	10.49	0.397	0.406	0.413
E	10.01	10.31	10.64	0.394	0.406	0.419
E1	7.39	7.49	7.59	0.291	0.295	0.299
e	—	1.27	—	—	0.050	—
L	0.38	0.81	1.27	0.015	0.032	0.050
y	—	—	0.076	—	0.003	—
Ø	0"	—	8"	0"	—	8"

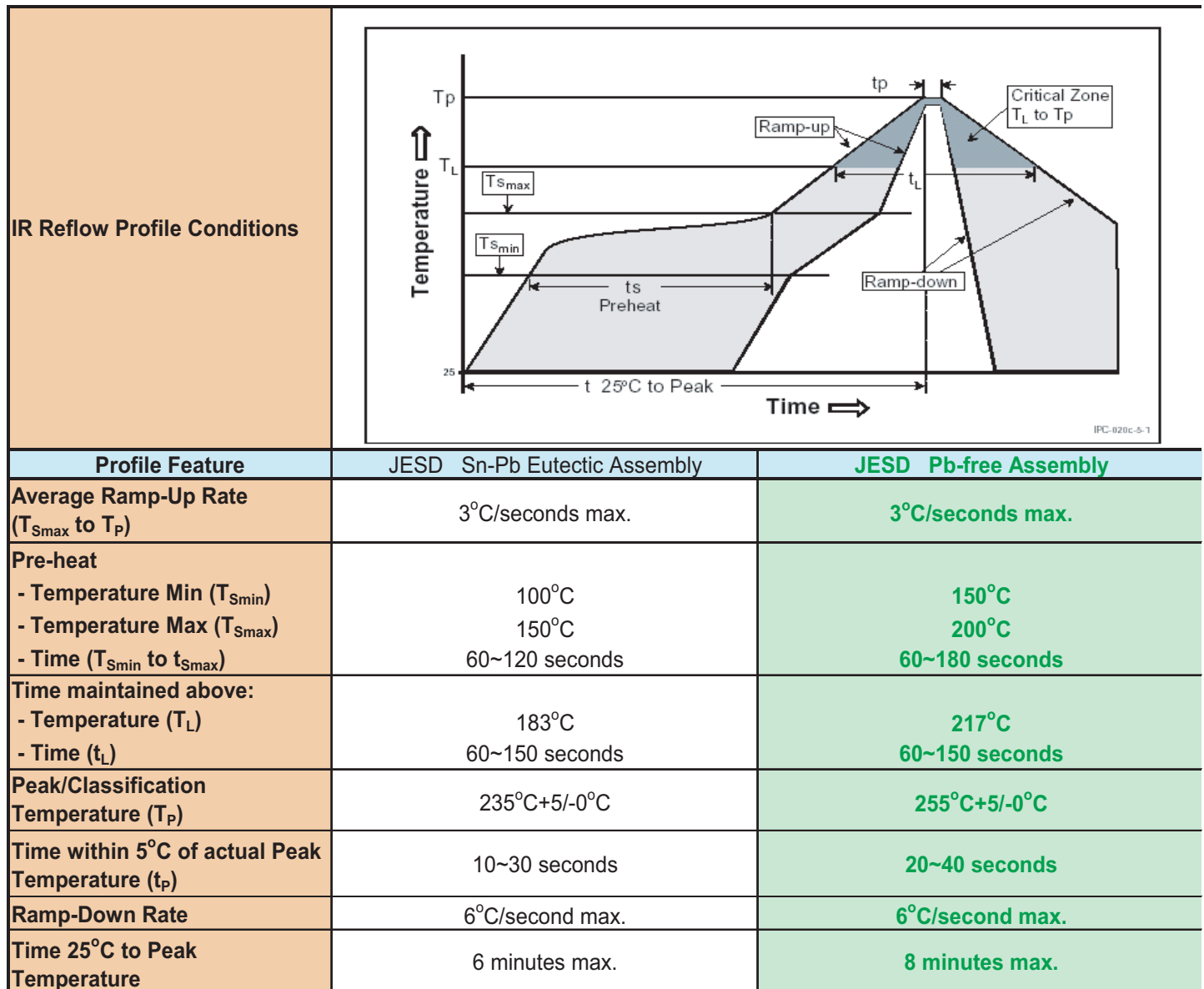
**16-pin wSOIC**

CUSTOMER :		ZYWYN CORPORATION	
APPROVED BY :	DATE :	TITLE :	
DRAW BY: Monica Chen	10/28/09	16L WIDE BODY SMALL OUTLINE PACKAGE DRAWING	
CHECK BY: Leo Chen	11/01/09	DWG. NO. PO-SOP-004	REV. 0
APPROVAL: Paul Lau	11/02/09	UNIT : INCH	SCALE : 8/1
APPROVAL: Jack Su	11/02/09	SHEET 1 OF 1	

















Green Package SMD IR Reflow Profile Information



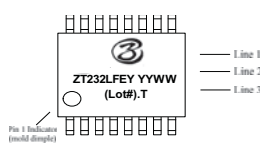
Zywyn Green Packages are Pb-free and RoHS compliance.

Ordering Information

Part Number	Drivers	Receivers	Temperature Range	Package Type	
ZT202LFEN	2	2	-40°C to +85°C	16-pin nSOIC	
ZT202LFEP	2	2	-40°C to +85°C	16-pin PDIP	
ZT202LFET	2	2	-40°C to +85°C	16-pin wSOIC	
ZT202LFEY	2	2	-40°C to +85°C	16-pin TSSOP	
ZT232LFEN	2	2	-40°C to +85°C	16-pin nSOIC	
ZT232LFEP	2	2	-40°C to +85°C	16-pin PDIP	
ZT232LFET	2	2	-40°C to +85°C	16-pin wSOIC	
ZT232LFEY	2	2	-40°C to +85°C	16-pin TSSOP	
ZT310LFET	2	2	-40°C to +85°C	18-pin wSOIC	
ZT310LFEA	2	2	-40°C to +85°C	20-pin SSOP	
ZT312LFET	2	2(with $\overline{EN}$ )	-40°C to +85°C	18-pin wSOIC	
ZT312LFEA	2	2(with $\overline{EN}$ )	-40°C to +85°C	20-pin SSOP	

Please contact the factory for pricing, availability on Tape-and-Reel options.


**TOPSIDE MARK INSTRUCTIONS:**



Line 1: Zywyn (logo)  
 Line 2: Zywyn Part Number "ZT232LFEY", Space " ", Date Code (Prod Year & Week)  
 Line 3: Lot #, dot and Country ".T"  
 Note: Pin # 1 "△" Indicator Required if no mold dimple

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**BOTTOMSIDE MARK INSTRUCTIONS:**



No Bottom -side Marking

Zywyn Corporation

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