

600V / 2.0A
N-Channel Enhancement Mode MOSFET

600V, $R_{DS(ON)}=4.6\Omega@V_{GS}=10V, I_D=1.0A$

Features

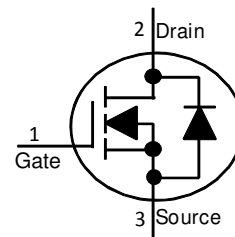
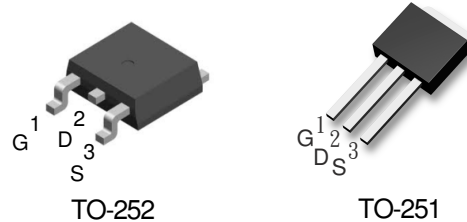
- Low ON Resistance
- Fast Switching
- Low Gate Charge & Low C_{RSS}
- Fully Characterized Avalanche Voltage and Current
- Specially Designed for AC Adapter, PFC and SMPS
- In compliance with EU RoHs 2002/95/EC Directives

Mechanical Information

- Case: TO-252 / TO-251 Molded Plastic
- Terminals : Solderable per MIL-STD-750, Method 2026

Marking & Ordering Information

TYPE	MARKING	PACKAGE	PACKING
HY2N60D	2N60D	TO-252	2500PCS/REEL
HY2N60M	2N60M	TO-251	70PCS/TUBE



Absolute Maximum Ratings ($T_c=25^\circ\text{C}$ unless otherwise noted)

Parameter		Symbol	HY2N60D	HY2N60M	Units
Drain-Source Voltage		V_{DS}	600		V
Gate-Source Voltage		V_{GS}	± 30		V
Continuous Drain Current	$T_c=25^\circ\text{C}$	I_D	2	2	A
Pulsed Drain Current ¹⁾		I_{DM}	8	8	A
Maximum Power Dissipation	$T_c=25^\circ\text{C}$	P_D	43.8	43	W
Derating Factor			0.35	0.35	
Avalanche Energy with Single Pulse $I_{AS}=2A, V_{DD}=50V, L=55mH$		E_{AS}	110		mJ
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55 to +150		$^\circ\text{C}$

Note : 1. Maximum DC current limited by the package

Thermal Characteristics

PARAMETER	Symbol	HY2N60D	HY2N60M	Units
Junction-to-Case Thermal Resistance	$R_{\theta JC}$	2.85	2.9	$^\circ\text{C/W}$
Junction-to Ambient Thermal Resistance	$R_{\theta JA}$	50	110	$^\circ\text{C/W}$

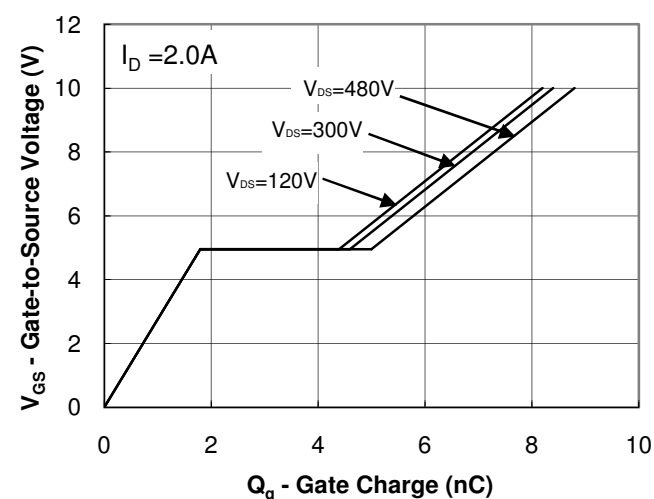
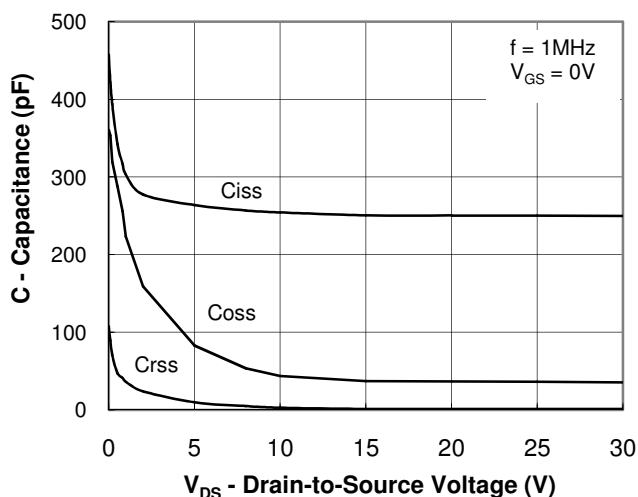
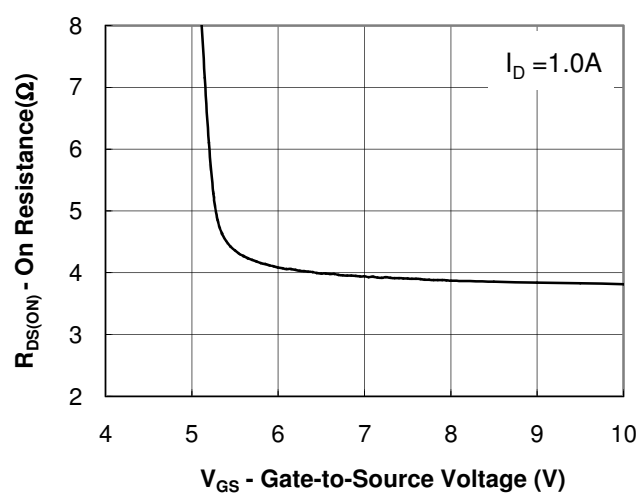
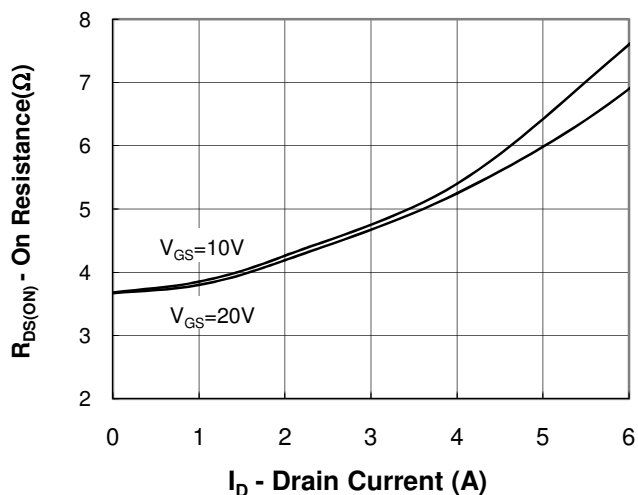
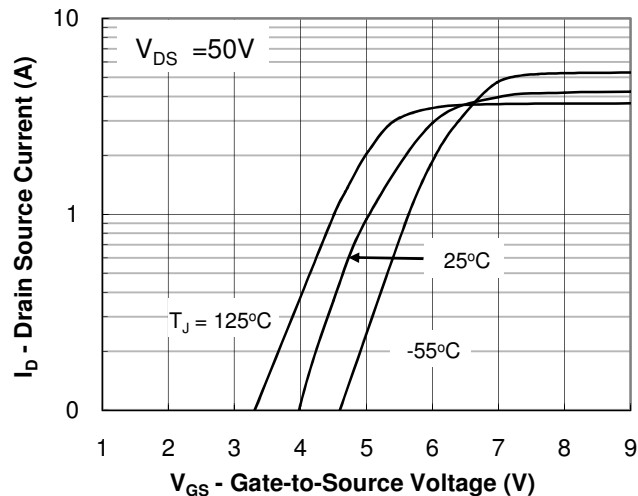
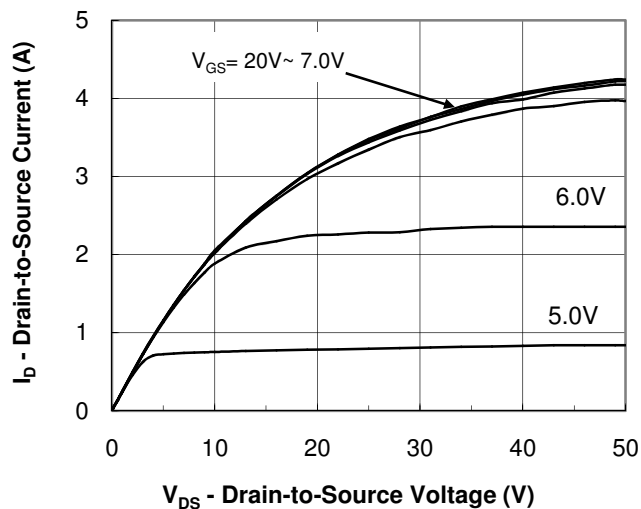
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Electrical Characteristics ($T_c=25^{\circ}\text{C}$ unless otherwise noted)

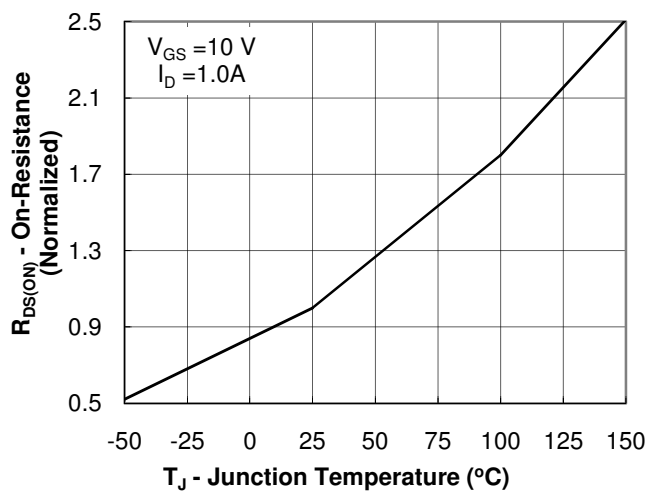
Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Units
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	600	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2.0	-	4.0	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=1.0A$	-	3.9	4.6	Ω
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=600V, V_{GS}=0V$	-	-	10	μA
Gate Body Leakage	I_{GSS}	$V_{GS}=\pm 30V, V_{DS}=0V$	-	-	± 100	nA
Dynamic						
Total Gate Charge	Q_g	$V_{DS}=480V, I_D=2.0A$ $V_{GS}=10V$	-	8.8	12	nC
Gate-Source Charge	Q_{gs}		-	1.8	-	
Gate-Drain Charge	Q_{gd}		-	3.2	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=300V, I_D=2.0A$ $V_{GS}=10V, R_G=25\Omega$	-	11.2	18	ns
Turn-On Rise Time	t_r		-	12.6	16	
Turn-Off Delay Time	$t_{d(off)}$		-	24.2	32	
Turn-Off Fall Time	t_f		-	10.2	12.2	
Input Capacitance	C_{iss}	$V_{DS}=25V, V_{GS}=0V$ $f=1.0MHz$	-	250	360	pF
Output Capacitance	C_{oss}		-	42	66	
Reverse Transfer Capacitance	C_{rss}		-	1.2	4.2	
Source-Drain Diode						
Max. Diode Forward Current	I_S	-	-	-	2.0	A
Max.Pulsed Source Current	I_{SM}	-	-	-	8.0	A
Diode Forward Voltage	V_{SD}	$I_S=2.0A, V_{GS}=0V$	-	-	1.4	V
Reverse Recovery Time	t_{rr}	$V_{GS}=0V, I_F=2.0A$ $di/dt=100A/\mu s$	-	250	-	ns
Reverse Recovery Charge	Q_{rr}		-	1.6	-	μC

NOTE : Plus Test : Pluse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.

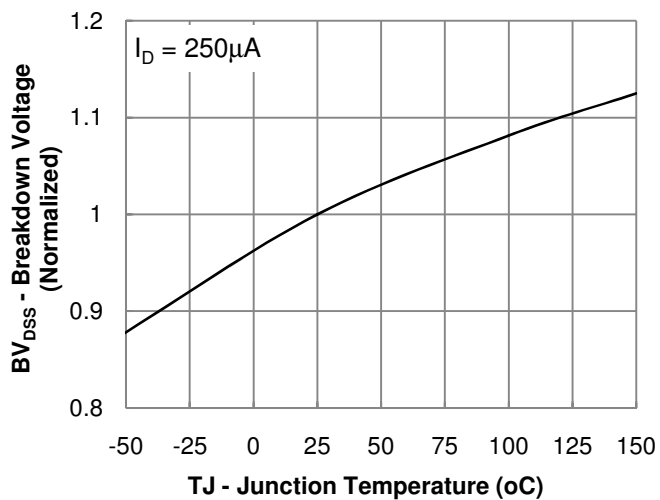
Typical Characteristics Curves ($T_C=25^\circ\text{C}$, unless otherwise noted)



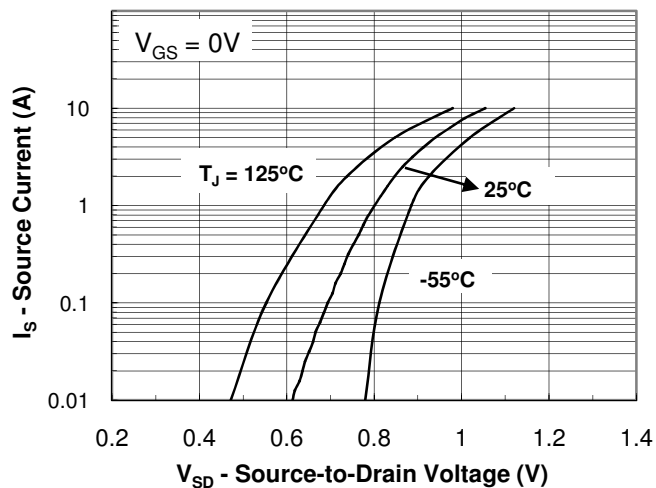
Typical Characteristics Curves ($T_C=25^{\circ}\text{C}$, unless otherwise noted)



**Fig.7 On-Resistance
vs Junction Temperature**



**Fig.8 Breakdown Voltage
vs Junction Temperature**



**Fig.9 Body Diode
Forward Voltage Characteristic**