

500V / 5A
N-Channel Enhancement Mode MOSFET

500V, $R_{DS(ON)}=1.5\Omega@V_{GS}=10V, I_D=2.5A$

Features

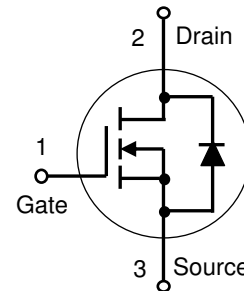
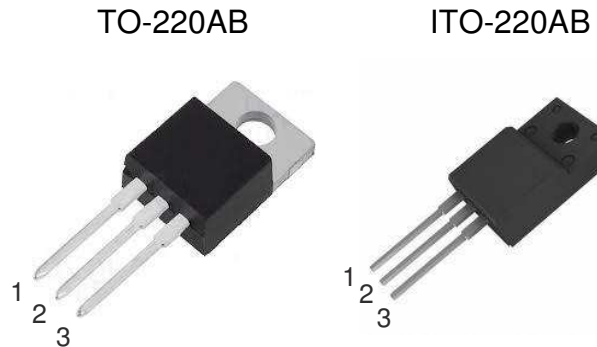
- Low On-State Resistance
- Fast Switching
- Low Gate Charge & Low C_{RSS}
- Fully Characterized Avalanche Voltage and Current
- Specially Designed for AC Adapter, Battery Charger and SMPS
- In compliance with EU RoHs 2002/95/EC Directives

Mechanical Information

- Case: TO-220AB / ITO-220AB Molded Plastic
- Terminals : Solderable per MIL-STD-750, Method 2026

Marking & Ordering Information

TYPE	MARKING	PACKAGE	PACKING
HY5N50T	5N50T	TO-220AB	50PCS/TUBE
HY5N50FT	5N50FT	ITO-220AB	50PCS/TUBE



Absolute Maximum Ratings ($T_c=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	HY5N50T	HY5N50FT	Units
Drain-Source Voltage	V_{DS}	500		V
Gate-Source Voltage	V_{GS}	± 30		V
Continuous Drain Current	I_D	5	5	A
Pulsed Drain Current ¹⁾	I_{DM}	20	20	A
Maximum Power Dissipation	P_D	71	27	W
Derating Factor		0.57	0.22	
Avalanche Energy with Single Pulse	E_{AS}	135		mJ
$I_{AS}=5A, V_{DD}=95V, L=11mH$				
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 to +150		$^\circ\text{C}$

Note : 1. Maximum DC current limited by the package

Thermal Characteristics

Parameter	Symbol	HY5N50T	HY5N50FT	Units
Junction-to-Case Thermal Resistance	$R_{\theta JC}$	1.76	4.6	$^\circ\text{C/W}$
Junction-to-Air Thermal Resistance	$R_{\theta JA}$	50	110	$^\circ\text{C/W}$

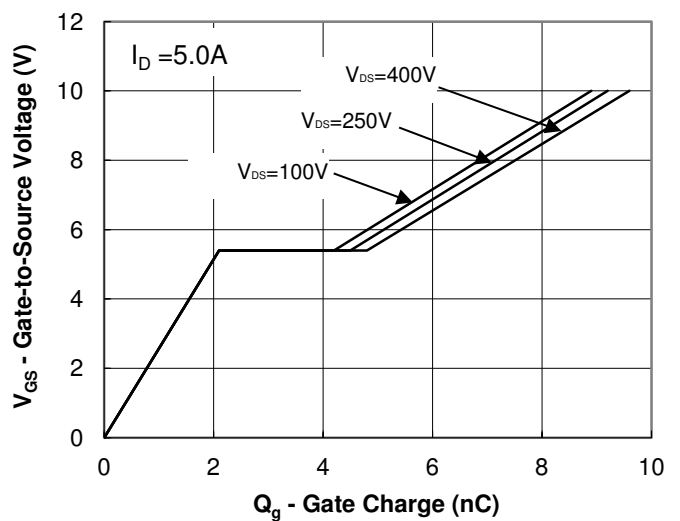
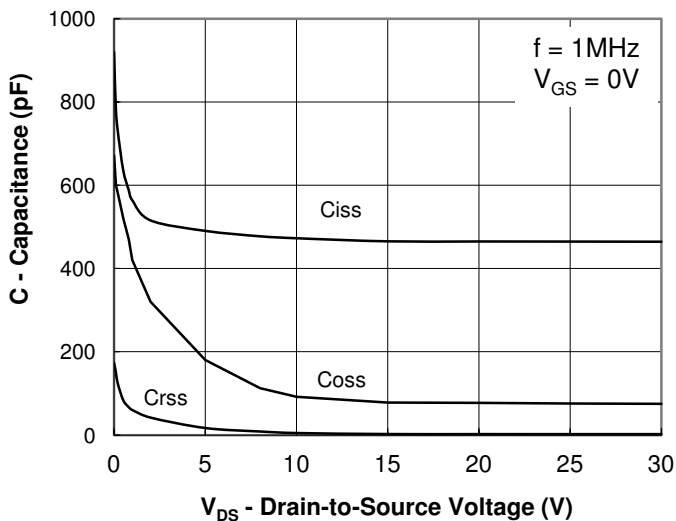
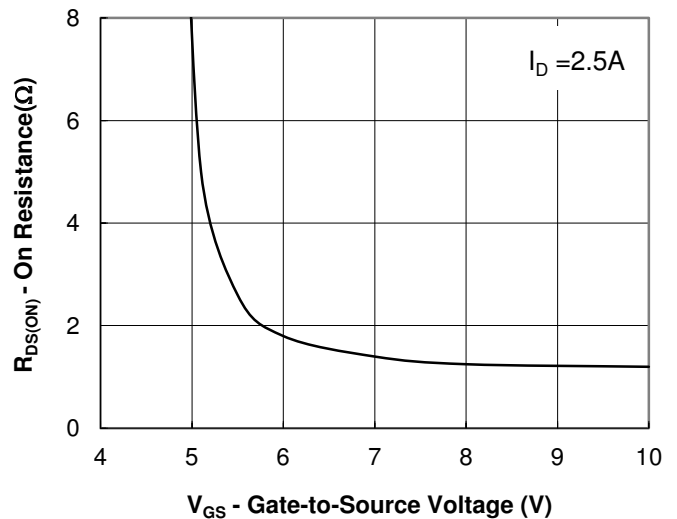
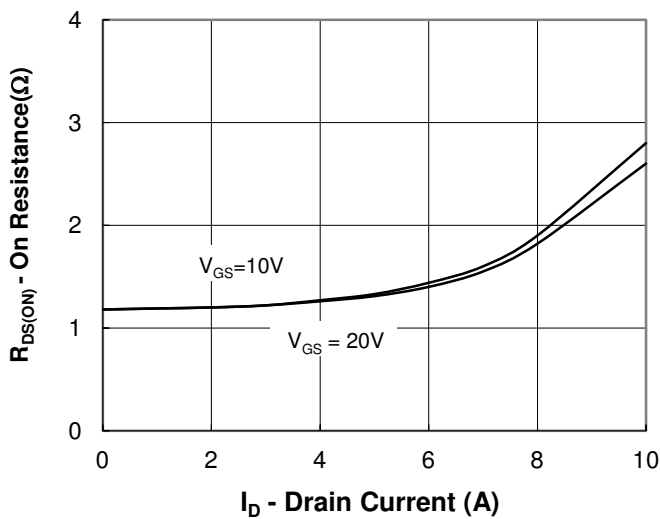
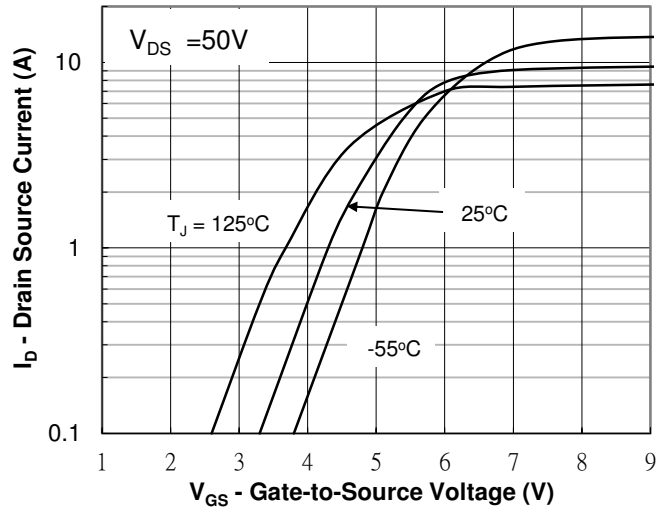
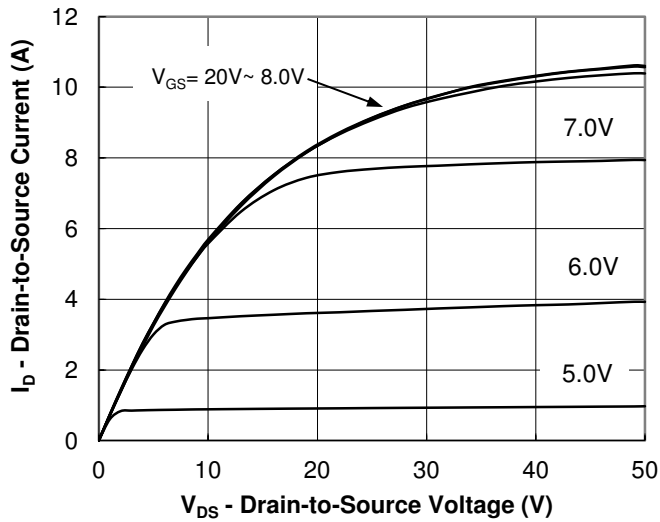
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Electrical Characteristics ($T_c=25^{\circ}\text{C}$, Unless otherwise noted)

Paramter	Symbol	Test Condition	Min.	Typ.	Max.	Units
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V \cdot I_D=250\mu A$	500	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS} \cdot I_D=250\mu A$	2.0	-	4.0	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10V \cdot I_D=2.5A$	-	1.2	1.5	Ω
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=500V \cdot V_{GS}=0V$	-	-	10	μA
Gate Body Leakage Current	I_{GSS}	$V_{GS}=\pm 30V \cdot V_{DS}=0V$	-	-	± 100	nA
Dynamic						
Total Gate Charge	Q_g	$V_{DS}=400V \cdot I_D=5A$ $V_{GS}=10V$	-	9.6	16	nC
Gate-Source Charge	Q_{gs}		-	2.1	-	
Gate-Drain Charge	Q_{gd}		-	2.7	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=250V \cdot I_D=5A$ $V_{GS}=10V \cdot R_G=25\Omega$	-	15.8	21	ns
Turn-On Rise Time	t_r		-	36.2	48	
Turn-Off Delay Time	$t_{d(off)}$		-	22.6	28	
Turn-Off Fall Time	t_f		-	18.8	26	
Input Capacitance	C_{iss}	$V_{DS}=25V \cdot V_{GS}=0V$ $f=1.0M_{HZ}$	-	465	-	pF
Output Capacitance	C_{oss}		-	76	-	
Reverse Transfer Capacitance	C_{riss}		-	2.1	-	
Source-Drain Diode						
Max. Diode Forwad Voltage	I_S	-	-	-	5.0	A
Max. Pulsed Source Current	I_{SM}	-	-	-	20	A
Diode Forward Voltage	V_{SD}	$I_S=5A \cdot V_{GS}=0V$	-	-	1.4	V
Reverse Recovery Time	t_{rr}	$V_{GS}=0V \cdot I_S=5A$ $di/dt=100A/\mu s$	-	245	-	ns
Reverse Recovery Charge	Q_{rr}		-	2.2	-	μC

NOTE : Pulse Test : Pulse Width $\leq 300\mu s$, duty cycle $\leq 2\%$

Typical Characteristics Curves ($T_C=25^\circ\text{C}$, unless otherwise noted)



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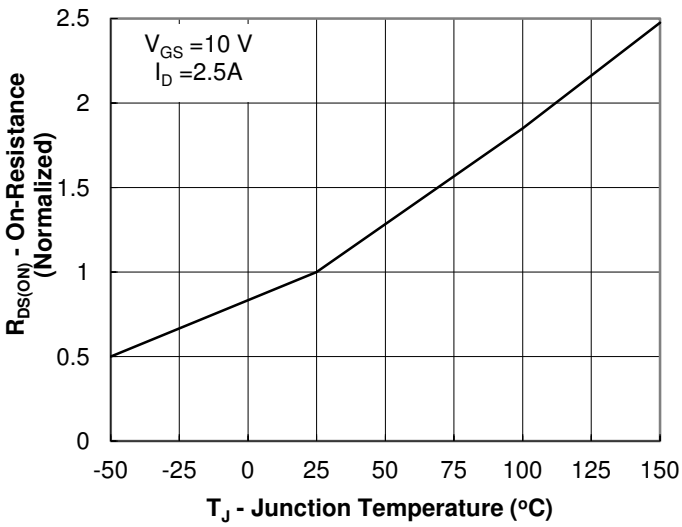


Fig.7 On-Resistance vs Junction Temperature

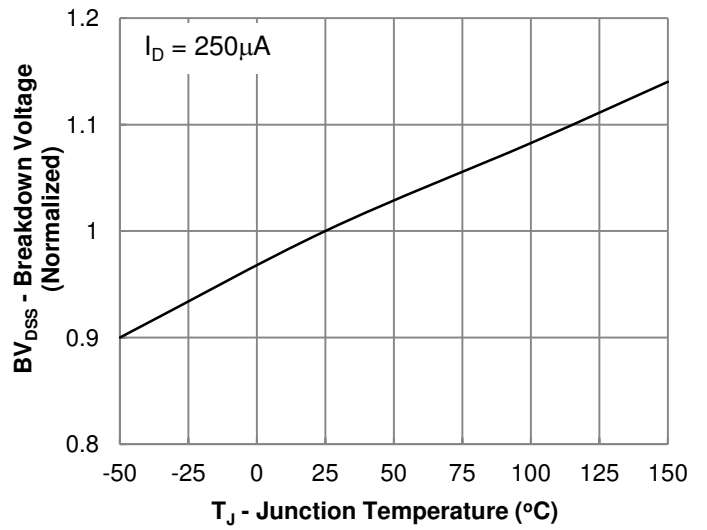


Fig.8 Breakdown Voltage vs Junction Temperature

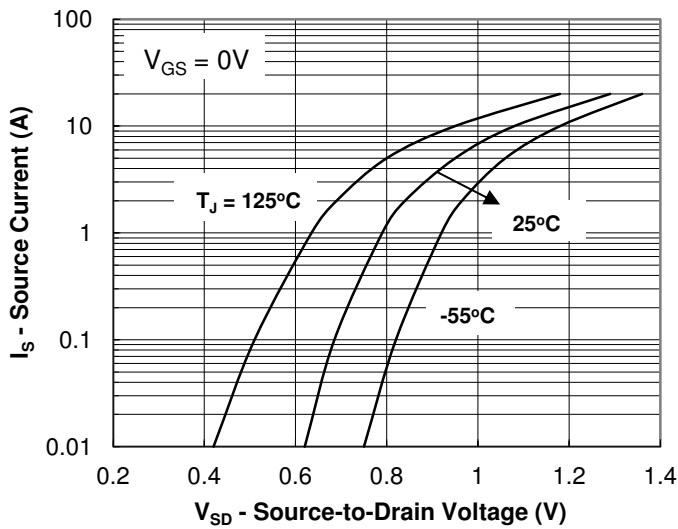


Fig.9 Body Diode Forward Voltage Characteristic