

The EPU672xS is a compact, cost-effective, and powerful platform for developing high performance network and control applications including imaging, industrial machine control, networking and medical device control. At only 3.5" x 2.2" x 1.0", the board stack will fit into compact spaces, while allowing access to the powerful features of the Altera Cyclone V device.

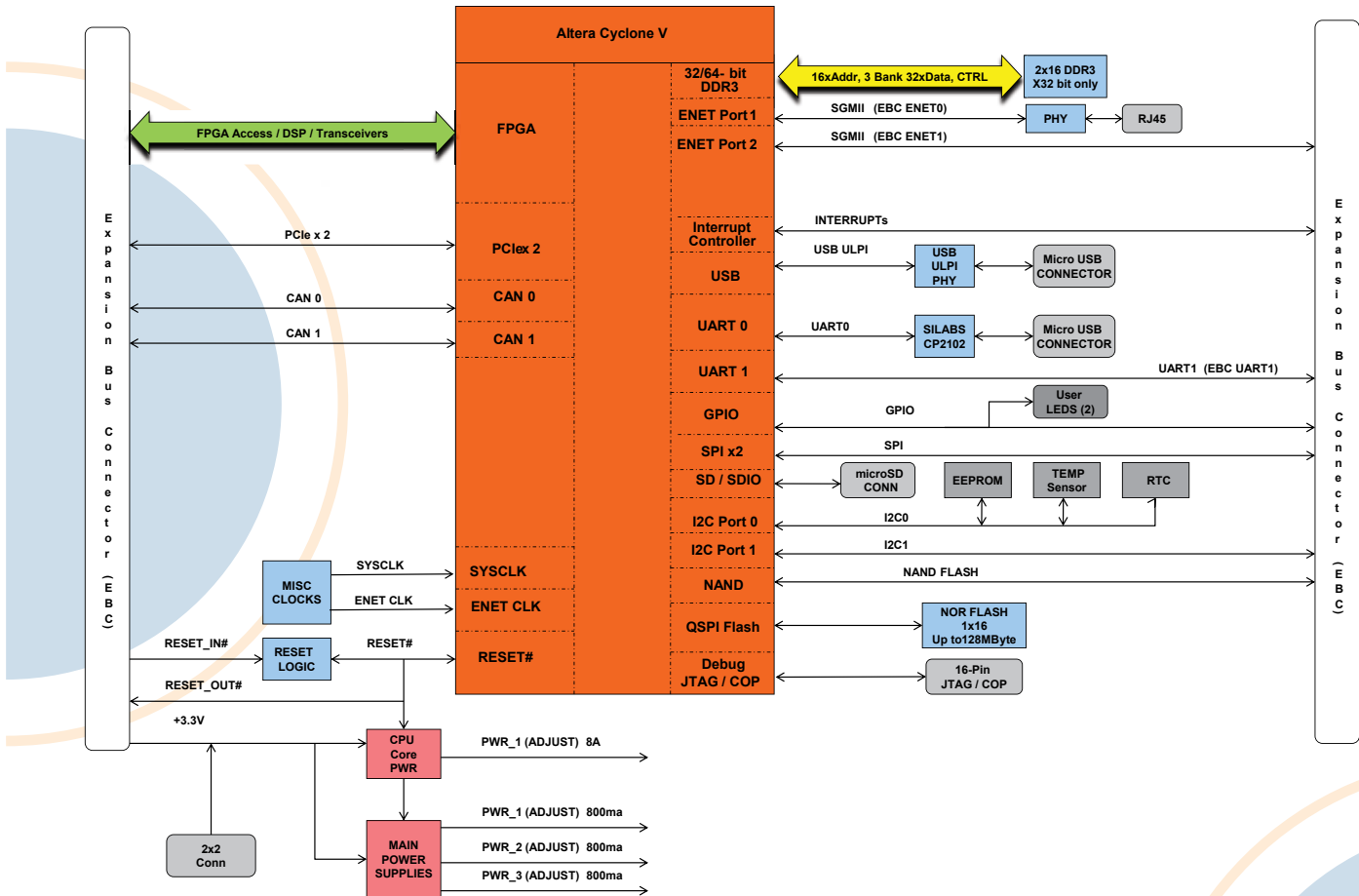
At the heart of the EPU672 is the Altera Cyclone V U672 device. The Cyclone V U672 device is a highly integrated FPGA / SoC combination that includes two ARM A9 cores at speeds of up to 800MHz, dual floating point units, NAND flash controller, DDR3 RAM controller, USB 2.0 host and device controllers, and dual Gigabit Ethernet. This Altera device allows users to create single chip solution for both CPU / RTOS and FPGA. The high level of integration increases performance, while lowering system power consumption and cost.

The EPU670 is supported by the U-Boot bootloader and BSPs for Linux, VxWorks, and Green Hills INTEGRITY.

### Feature Summary:

Specification:	Description:
Processor:	Altera Cyclone V with Dual-core ARM A9 MPCore at 800MHz per core
Memory:	RAM: Up to 256MB DDR3 FLASH: Up to 256MB, with optional SD socket
Ethernet:	1 Gigabit port via I/O board
Serial Ports:	1 RS-232 on board
USB:	USB 2.0 Host and Device ports via I/O board
I2C Devices:	EEPROM and Battery Backed Real Time Clock
Form Factor:	Fanless Credit Card size (3.5" x 2.2"), 1.0" high
Debug:	On board JTAG debug connector
Software:	U-Boot Bootloader, Linux, VxWorks and Green Hills INTEGRITY BSPs available
Notes:	Available as industrial temperature rated module

### Hardware Block Diagram



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- We can alleviate the headaches associated with volume production of embedded systems. Your product is delivered 100% tested from an ISO-9002 certified manufacturing facility.
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