

The EP2020xS and its companion I/O board create a compact, cost-effective, and powerful platform for developing high performance networked control platforms. At only 3.5" x 2.2" x 1.0", the board will fit into compact spaces, while 1 serial port, 2 USB ports, and 1 Gigabit Ethernet port plus the EBC connector provide the necessary control I/O.

The External Bus Connector (EBC) provides direct access to the P1020 interfaces. This high-speed compact interface will handle demanding applications.

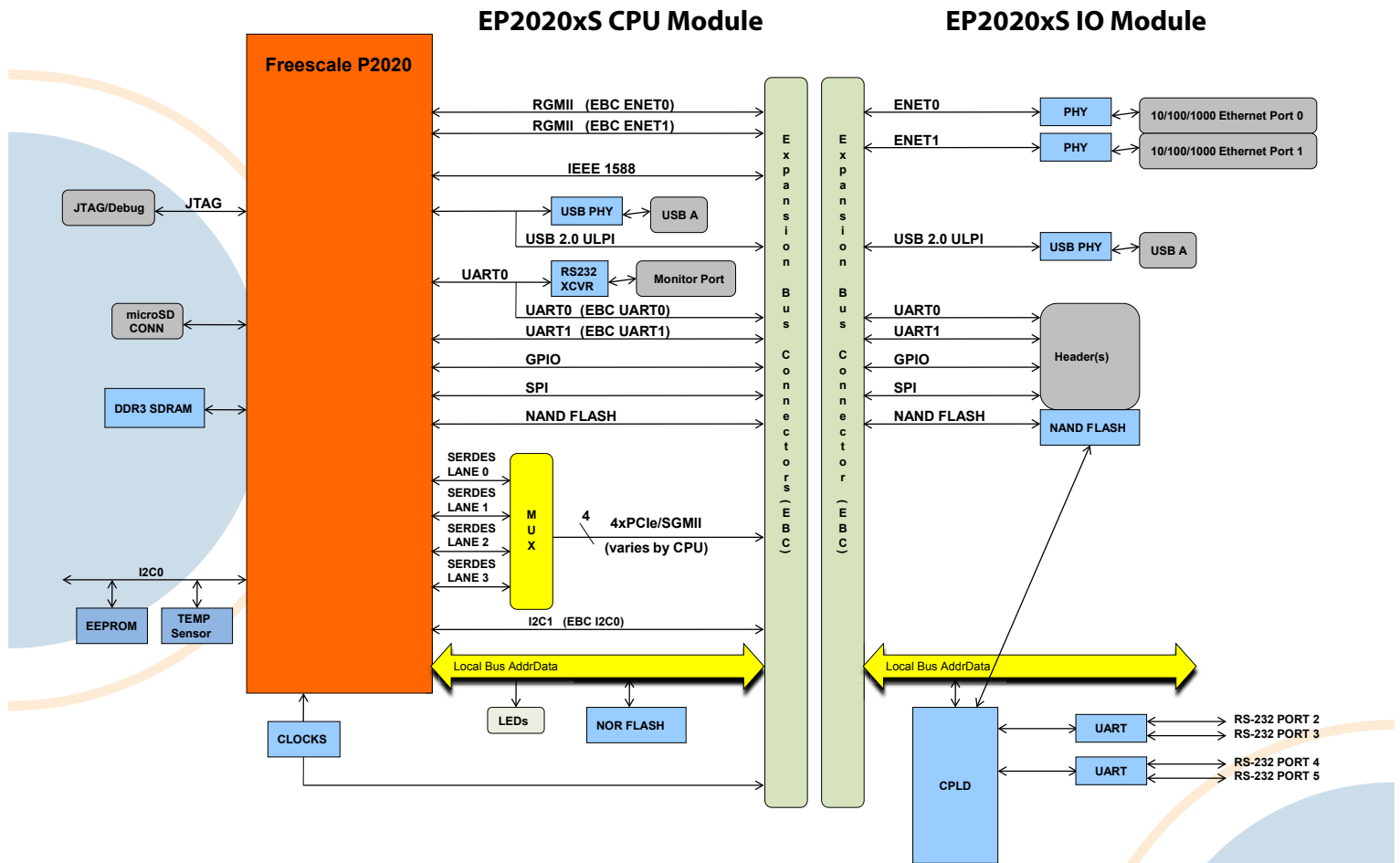
At the heart of the EP2020xS is the Freescale QorIQ P2020. The P2020 is a highly integrated SoC that includes two e500 cores at speeds up to 1.2GHz, dual floating point units, NAND flash controller, DDR2 RAM controller, USB 2.0 host and device controllers, and dual Gigabit Ethernet MACs. The high level of integration increases performance, while lowering system power consumption and cost.

The board stack is supported by the U-Boot bootloader and BSPs for Linux, VxWorks, and Green Hills INTEGRITY.

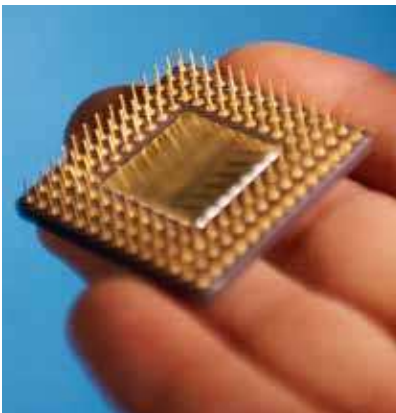
Feature Summary:

| Specification: | Description: |
|----------------|---|
| Processor: | Freescale P2020 at up to 1.2GHz |
| Memory: | RAM: Up to 256MB DDR3 FLASH: Up to 128MB, with additional 512MB NAND flash via I/O board |
| EBC Connector: | Direct access to SERDES, Clocks (SERDES, Ssystem, Ethernet), 1588, TSEC, USB, UARTs, GPIO, I2C, Flash and Power |
| Ethernet: | 1 Gigabit port (Additional via I/O board) |
| Serial Ports: | 1 RS-232 on board with additional ports available via optional I/O board |
| USB: | USB 2.0 Host and Device ports via I/O board |
| I2C Devices: | EEPROM and Battery Backed Real Time Clock |
| Form Factor: | Fanless Credit Card size (3.5" x 2.2"), 1.0" high |
| Debug: | On board JTAG debug connector |

Hardware Block Diagram



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