DS800SL Series

800 Watts

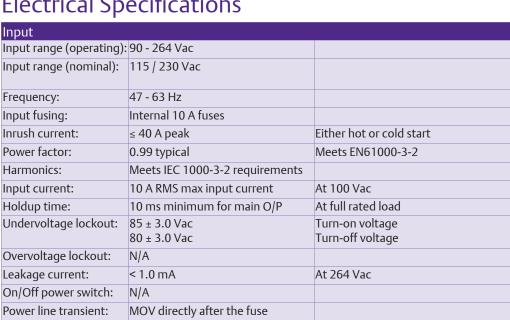
Bulk Front End

Total Output 800 Watts Power: +5.0 Vdc Stand-by

Special Features

- 800 W output power
- 19.05 W/cu-in
- 1U X 54.5 mm form factor (slimline)
- N + 1 redundant
- Hot-swap
- Internal OR'ing
- 5.0 V housekeeping
- High efficiency 92% @ 200 Vac, 50% load (Climate Savers Gold)
- Variable speed "smart fans"
- EMI Class B
- EN61000 Immunity
- Available in two airflow directions

Electrical Specifications



Ordering Information						
	Main Output	Standby Output	Airflow Direction			
DS800SL-3	12V	5VSB @ 4.0A	Forward (output to handle)			
DS800SL-3-001	12V	5VSB @ 4.0A	Reverse (handle to output) ¹			

¹Output power derating applies. See power derating curve.





Rev. 12.17.13 DS800SL Series 1 of 8

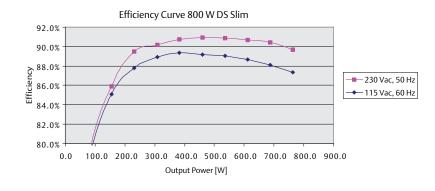
Safety

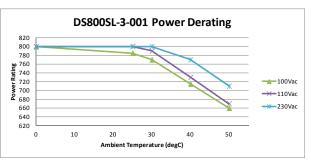
 UL/cUL 60950 -1 • CSA 60950-1 VDE 60950-1 China CCC

• CB Scheme Report/Cert



Rev. 12.17.13 DS800SL Series 2 of 8



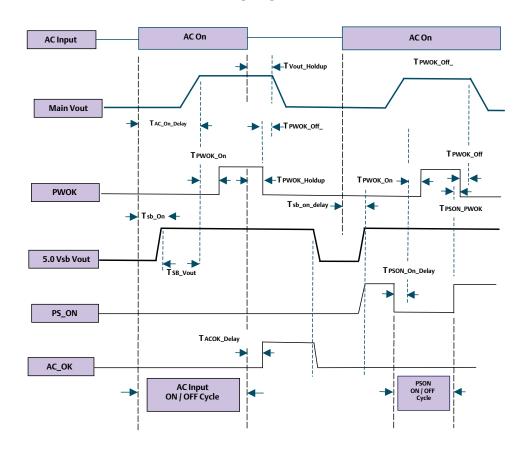


Output		
Output rating:	12 V @ 66.7 A; 800 W 5.0 Vsb @ 4 A; 20 W	90 - 264 Vac
Setpoint:	12.0 V	
Total regulation range:	12 V ± 1% 5.0 Vsb ± 3%	Line/load/transient when measured at output connector
Rated load:	800 W maximum	No derating over operating temp range for forward air
Minimum load:	12 V @ 0.5 A 5.0 Vsb @ 0.0 A	No load operation shall not damage the power supply
Output noise (PARD):	120 mV Max P-P 100 mV Max P-P	12.0 V output 5.0 Vsb output Measured with a 0.1 uF ceramic and 10 uF tantalum capacitor on any output; 20 Mhz
Output voltage overshoot:	600 mV; 12 V main 250 mV; 5.0 standby	1 A/uSec slew rate
Transient response:	+/- 5% of regulation range	50% load step @ 1 A/us Step load valid between 10% to 100% of output rating Recovery time to within 1% of set point at onset of transient
Max units in parallel:	Up to 6	
Short circuit protection:	>130% of rated output current	Output to return
Remote sense:	Compensation up to 100 mV	
Output isolation:	Standard per Safety Requirements	
Forced load sharing:	To within 10% of all shared outputs	Digital sharing control
Overload protection (OCP):	120% to 130% 120% to 170%	12 V output 5.0 Vsb output
Overvoltage protection (OVP):	110% to 120% 110% to 125%	12 V output 5.0 Vsb output
Overtemperature protection:	10 - 15 °C above safe operating area	Both PFC and output converter monitored

Outputs - All Models

Rev. 12.17.13 DS800SL Series 3 of 8

Timing Diagram



Turn On/Off Timinig				
ltem	Description	Min	Max	Units
Tvout_rise	+12 Output rise time	10	300	mSec
Tvout_rise	5.0 Vsb output rise time	1	50	mSec
Tsb_on_delay	Delay from AC being applied to 5.0 Vsb being within regulation.		1500	mSec
Tac_on_delay	Delay from AC being applied to all output voltages being within regulation.		3000	mSec
Tvout_holdup	Time all output voltages, including 5.0 Vsb, stay within regulation after loss of AC.	10		mSec
Tpwok_holdup	Delay from loss of AC to de-assertion of PWOK	5		mSec
Tpson_on_delay	Delay from PSON# active to output voltages within regulation limits.	50	2500	mSec
Tpson_pwok	Delay from PSON# de-active to PWOK being de-asserted.		100	mSec
Tacok_delay	Delay from loss of AC input to de-assertion of ACOK#.	10		mSec
Tpwok_on	Delay from output voltages within regulation limits to PWOK asserted at turn on.	100	1000	mSec
Tpwok_off	Delay from PWOK de-asserted to 12 Vdc or 5.0 Vsb dropping out of regulation limits.	1	1000	mSec
Tpwok_low	Duration of PWOK being in the de-asserted state during an off/on cycle using AC or the PSON# signal.	100		mSec
Tsb_vout	Delay from 5.0 Vsb being in regulation to 12 Vdc being in regulation at AC turn on.	50	1000	mSec

Rev. 12.17.13 DS800SL Series 4 of 8

Outputs - All Models

PSON#

The PSON# signal is required to remotely turn on/off the power supply. PSON# is an active low signal that turns on the +12 Vdc power rail. When this signal is not pulled low by the system, or left open, the +12 Vdc output turns off. The 5.0 Vsb output remains on. This signal is pulled to a standby voltage by a pull-up resistor internal to the power supply. The power supply fan(s) shall operate at the lowest speed.

Signal Type	Accepts an open collector/drain input from the system. Pulled-up to the 5.0 Vsb located in power supply.		
PSON# = Low		ON	
PSON# = Open		OFF	
	MIN	MAX	
Logic level low (power supply ON)	0 V	0.8 V	
Logic level high (power supply OFF)	2.0 V	5.2V	
Source current, Vpson = low		4 mA	
Power up delay: Tpson_on_delay	5 msec	200 msec	

PWOK# (Power Good)

PWOK is a power good signal and will assert HIGH when the outputs are within the regulation limits. PWOK will be pulled LOW by the power supply to indicate when either output falls below regulation limits or when AC power has been removed for a time sufficiently long so that power supply operation is no longer guaranteed. The start of the PWOK# delay time shall be inhibited as long as the +12 Vdc output is in current limit or the 5.0 Vsb output is below the regulation limit.

PWOK Signal Characteristics					
Signal Type		n output from power supply. sternal to the power supply.			
PWOK = High	F	Power Good			
PWOK = Low	Pov	wer Not Good			
	MIN	MAX			
To tLogic level low voltage, Ising = 4 mA	0 V	0.8 V			
Logic level high voltage, Isource = 200 μA	2.0 V	5.2 V			
Sink current, PWOK = low		4 mA			
Source current, PWOK = high		2 mA			
PWOK delay: Tpwok_on	100 ms	1000 ms			
PWOK rise and fall time		100 μsec			
Power down delay: Tpson_off	1 msec	1000 msec			

PSKILL

The +12 Vdc output only from the power supply shall be disabled if the PSKILL input is high and V Standby will continue to be provided, outputs may be enabled if this signal is low. The power supply includes a pull up to disable all outputs if this signal is open. PSKILL whall not be connected during a hot insertion before all of the other pins are connected.

Rev. 12.17.13 DS800SL Series 5 of 8

AC INPUT Present Indicator (ACOK/L)

The ACOK signal is used to indicate the presence of AC input to the power supply. This signal shall be connected to the standby output through a resistor on the hist system side. A logic "LOW" level on this signal shall indicate that the AC input is within operation range. A logic "HIGH" on this signal shall indicate the loss of AC input to the power supply.

ACOK/L Signal Characteristics					
Signal Type	Pull up to 5.0 Vsb the host system.	hrough a resistor in			
ACOK/L = High Not Present					
ACOK/L = Low Present					
	MIN	MAX			
Logic level low voltage, Isink = 4 mA	0 V	0.8 V			
Logic level high voltage, Isink = $50 \mu A$	2.0 V	5.2 V			
Sink current, ACOK/L = low		4 mA			
Sink current, ACOK/L = high		50 μsec			

Status Indications

See table below for Summary of Status signals, Ports and Indicators. The condition column assumes 2 or more power supplies present and ON and 5.0 Vsb shared for management interface. On the "Fan Blocked" condition, the assumption is that all outputs are within spec and not over temperature. This would be considered a "warning" condition. On the "Standby" condition, the system differentiates this state by knowing PS_ONL in negated (requesting Standby).

Status Indicators												
Condition	Status Signals		Status Register		Shutdown Register				LED's			
Condition	ACOK/L	PWOK/H	PSON	PWOK	Fan-Fail	AC-Loss	0-Temp	0-Current	Fail	AC	DC	Fail
Normal Operation	0	1	1	1	0	0	0	0	0	On	On	Off
V1 12 V Overcurrent	0	0	1	0	0	0	0	1	1	On	Off	On
AC Input Fail	1	0	1	0	0	1	0	0	1	Off	Off	Off
Fan Blocked or Running Under Speed. O/P's ok	0	1	1	1	0	0	0	0	0	On	On	Off
UV on V1 12 V and PS Has Latched Off	0	0	1	0	0	0	0	0	1	On	Off	On
UV on Vsb +5.0 and PS Has Turned Off	0	0	1	0	0	0	0	0	1	On	Off	On
OV on V1 12V or Vsb +5.0 & PS Has Latched Off	0	0	1	0	0	0	0	0	1	On	Off	On
Over Temp and PS Has Turned Off	0	0	1	0	0	0	1	0	1	On	Off	On
Fan Below Shutdown Limit	0	0	1	0	1	0	0	0	1	On	Off	On
No Problems But PS is in Standby Mode	0	0	0	0	0	0	0	0	0	On	Off	Off

Rev. 12.17.13 DS800SL Series 6 of 8

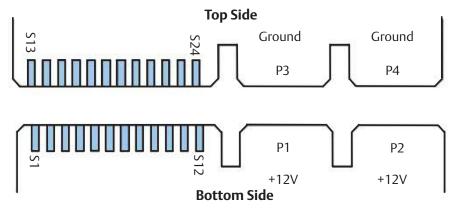
Pin Out Table

Pin	Signal Name		
P1	+12V		
P2	+12V		
P3	Ground		
P4	Ground		
S1	+12V Sense		
S2	+12V RTN Sense		
S3	+12V Current Share		
S4	SMB_ALERT/L		
S5	SDA		
S6	SCL*		
S7	PSKILL		
S8	PSON/L		
S9	PW_OK		
S10	PS_A1		

*Supports	12C standard	mode (1	100 kHz) only
SUDDOLLS	I-C Standard	modeti	LUU KHZI OHIV

Pin	Signal Name
S11	+5.0 V_STBY
S12	+5.0 V_STBY
S13	Reserved
S14	PRESENT/L
S15	PS_A0
S16	Reserved
S17	Reserved for factory use
S18	EEPROM_WP
S19	ACOK/L
S20	Not used
S21	PS_A2
S22	V_STBY Remote Sense
S23	+5.0 V_STBY
S24	+5.0 V_STBY

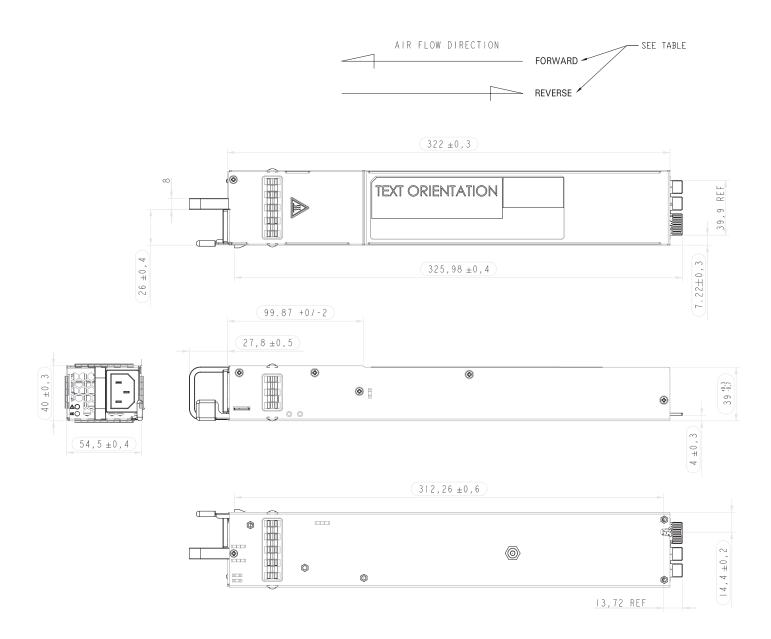
Output Connector (as seen from power supply side)



Note: Pin number assignment may not necessarily match connector vendor's pin number assignment. Please follow signal and number assignment as indicated in this datasheet.

Rev. 12.17.13 DS800SL Series 7 of 8

Mechanical Outline



Environmental Specifications

Operating Temperatue:	-10 to 50 °C
Operating Altitude:	up to 10,000 feet
Operating Relative Humidity:	5% to 95% non-condensing
Non-operating Temperature:	-40 to +85 °C
Non-operating Relative Humidity:	5% to 95% non-condensing
Non-operating Altitude:	up to 30,000 feet
ROHS Compliance:	Yes
MTBF:	>400,000 hours using Telcordia SR332 at 25 deg C ambient at full load.
Operating Life:	Minimum of 5 years
Operating Life:	5 years minimum

Burn-In

100% Burn-in at 45 °C, at 80 - 90% load. Duration of burn-in determined by Quality Assurance Procedures.

MTBF

The power supply has a minimum MTBF of >400K hours using Telcordia SR-332 at full load, 25 °C. With the power supply installed in a system in a 25 °C ambient environment and operating at full load, capacitor life shall be 5 years, minimum for ALL electrolytic capacitors contained within this power supply.

Quality Assurance

Full QAV testing shall be conducted in accordance with Emerson Network Power Standards with reports available upon request.

Warranty

Emerson Network Power shall warrant the power supply to be free of defects in materials and workmanship for a minimum period of two year from the date of shipment, when operated within specifications. The warranty shall be fully transferable to the end owner of the equipment powered by the supply.

Rev. 12.17.13 DS800SL Series Ficas 8 of 8

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