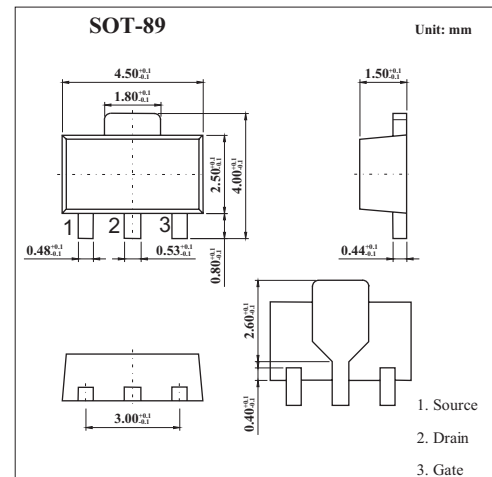


## 250V P-Channel Enhancement Mode Vertical MOSFET KVP4424Z

### ■ Features

- 240 Volt  $V_{DS}$
- $R_{DS(on)} = 8.8 \Omega$  typical at  $V_{GS} = -3.5V$
- Low threshold and Fast switching



### ■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	-240	V
Continuous Drain Current at $T_{amb}=25^\circ C$	$I_D$	-200	mA
Pulsed Drain Current	$I_{DM}$	-1	A
Gate Source Voltage	$V_{GS}$	$\pm 40$	V
Power Dissipation at $T_{amb} = 25^\circ C$	$P_{tot}$	1*	W
Operating and Storage Temperature Range	$T_j; T_{stg}$	-55 to +150	$^\circ C$

\* recommended  $P_{tot}$  calculated using FR4 measuring 15X15X0.6mm

**KVP4424Z**

## ■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Testconditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	B <sub>VDS</sub>	I <sub>D</sub> =-1mA, V <sub>GS</sub> =0V	-240			V
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	I <sub>D</sub> =-1mA, V <sub>DS</sub> = V <sub>GS</sub>	-0.7	-1.4	-2.0	V
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>GS</sub> =± 40V, V <sub>DS</sub> =0V			100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-240V, V <sub>GS</sub> =0V			-10	μ A
		V <sub>DS</sub> =-190V, V <sub>GS</sub> =0V, T=125°C			-100	μ A
On-State Drain Current	I <sub>D(on)</sub>	V <sub>DS</sub> =-10V, V <sub>GS</sub> =-10V	-0.75	-1.0		A
Static Drain-Source	R <sub>DS(on)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-200mA		7.1	9	Ω
On-State Resistance		V <sub>GS</sub> =-3.5V, I <sub>D</sub> =-100mA		8.8	11	Ω
Forward Transconductance *1,2	g <sub>fs</sub>	V <sub>DS</sub> =-10V, I <sub>D</sub> =-0.2A	125			mS
Input Capacitance *2	C <sub>iss</sub>	V <sub>DS</sub> =-25V, V <sub>GS</sub> =0V, f=1MHz		100	200	pF
Common Source Output Capacitance *2	C <sub>oss</sub>			18	25	pF
Reverse Transfer Capacitance*2	C <sub>rss</sub>			5	15	pF
Turn-On Delay Time *2,3	t <sub>d(on)</sub>			8	15	ns
Rise Time *2,3	t <sub>r</sub>	V <sub>DD</sub> ≈ -50V, I <sub>D</sub> = -0.25A, V <sub>GEN</sub> = -10V		8	15	ns
Turn-Off Delay Time *2,3	t <sub>d(off)</sub>			26	40	ns
Fall Time *2,3	t <sub>f</sub>			20	30	ns

\*1 Measured under pulsed conditions. Width=300 μ s. Duty cycle ≤2%

\*2 Sample test.

\*3 Switching times measured with 50 Ω source impedance and <5ns rise time on a pulse generator

Spice parameter data is available upon request for this device

## ■ Marking

Marking	24P
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