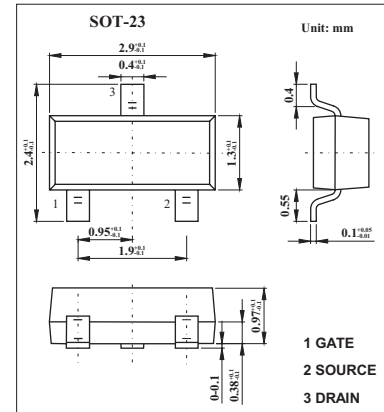


## MOS Field Effect Transistor

### 2SK1581

#### ■ Features

- Can be driven by ICs having a 3V single power supply.
- Not necessary to consider driving current because of its thgh input impedance.
- Possible to reduce the number of parts by omitting the bias resistor



#### ■ Absolute Maximum Ratings $T_a = 25^\circ\text{C}$

Parameter	Symbol	Rating	Unit
Drain to source voltage	$V_{DS}$	16	V
Gate to source voltage	$V_{GS}$	$\pm 16$	V
Drain current (DC)	$I_D$	$\pm 200$	mA
Drain current(pulse) *	$I_D$	$\pm 400$	mA
Power dissipation	$P_D$	200	mW
Channel temperature	$T_{ch}$	150	$^\circ\text{C}$
Storage temperature	$T_{stg}$	-55 to +150	$^\circ\text{C}$

\*  $PW \leq 10\text{ms}$ , duty cycle  $\leq 5\%$

#### ■ Electrical Characteristics $T_a = 25^\circ\text{C}$

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit
Drain cut-off current	$I_{DSS}$	$V_{DS}=16\text{V}, V_{GS}=0$			10	$\mu\text{A}$
Gate leakage current	$I_{GSS}$	$V_{GS}=\pm 3\text{V}, V_{DS}=0$			$\pm 10$	$\mu\text{A}$
Gate to source cutoff voltage	$V_{GS(off)}$	$V_{DS}=3.0\text{V}, I_D=10\mu\text{A}$	0.9	1.2	1.5	V
Forward transfer admittance	$ Y_{fs} $	$V_{DS}=3.0\text{V}, I_D=10\text{mA}$	20	70		ms
Drain to source on-state resistance	$R_{DS(on)}$	$V_{GS}=2.5\text{V}, I_D=1\text{mA}$		3.2	5.0	$\Omega$
		$V_{GS}=4.0\text{V}, I_D=1\text{mA}$		2.2	3.0	$\Omega$
Input capacitance	$C_{iss}$	$V_{DS}=3.0\text{V}, V_{GS}=0, f=1\text{MHz}$		27		pF
Output capacitance	$C_{oss}$			37		pF
Reverse transfer capacitance	$C_{rss}$			8		pF
Turn-on delay time	$t_{d(on)}$				100	
Rise time	$t_r$	$I_D=10\text{mA}, V_{GS(on)}=3.0\text{V}, R_L=300\Omega, V_{DD}=3.0\text{V}, R_G=10\Omega$		300		ns
Turn-off delay time	$t_{d(off)}$			210		ns
Fall time	$t_f$			240		ns

#### ■ Marking

Marking	G14
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