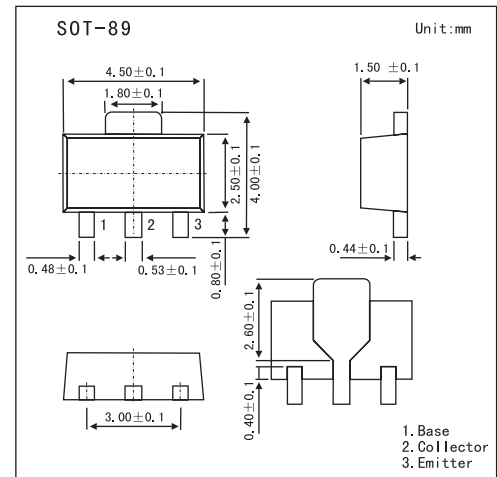


## PNP Epitaxial Planar Silicon Transistors

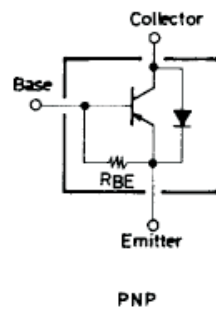
## 2SB1325

## ■ Features

- Low saturation voltage.
- Contains diode between collector and emitter.
- Contains bias resistance between base and emitter.
- Large current capacity.
- Small-sized package making it easy to provide highdensity, small-sized hybrid ICs.



## ■ Electrical Connection

■ Absolute Maximum Ratings  $T_a = 25^\circ\text{C}$ 

Parameter	Symbol	Rating	Unit
Collector-base voltage	$V_{CBO}$	-25	V
Collector-emitter voltage	$V_{CEO}$	-20	V
Emitter-base voltage	$V_{EBO}$	-6	V
Collector current	$I_C$	-4	A
Collector current (pulse)	$I_{CP}$	-6	A
Collector dissipation	$P_C$ *	1.5	W
Jumction temperature	$T_j$	150	$^\circ\text{C}$
Storage temperature	$T_{stg}$	-55 to +150	$^\circ\text{C}$

\* Mounted on ceramic board (250mm<sup>2</sup>X0.8mm)

## 2SB1325

## ■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Testconditions	Min	Typ	Max	Unit
Collector cutoff current	ICBO	V <sub>CB</sub> = 20V , I <sub>E</sub> = 0			-1	μA
DC current Gain	h <sub>FE</sub>	V <sub>CE</sub> = -2V , I <sub>C</sub> = -0.5A	70			
		V <sub>CE</sub> = -2V , I <sub>C</sub> = -3A	50			
Gain bandwidth product	f <sub>T</sub>	V <sub>CE</sub> = -2V , I <sub>C</sub> = -0.5A		300		MHz
Output capacitance	C <sub>ob</sub>	V <sub>CB</sub> = -10V , f = 1MHz		60		pF
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> = -3A , I <sub>B</sub> = -150mA		-0.25	-0.5	V
Base-emitter saturation voltage	V <sub>BE(sat)</sub>	V <sub>CE</sub> = -3V , I <sub>C</sub> = -150mA			-1.5	V
Collector-base breakdown voltage	V <sub>(BR)CBO</sub>	I <sub>C</sub> = -10μA , I <sub>E</sub> = 0	-25			V
Collector-emitter breakdown voltage	V <sub>(BR)CEO</sub>	I <sub>C</sub> = -10μA , R <sub>BE</sub> = ∞	-25			V
Base-emitter on state voltage	V <sub>(BR)CEO</sub>	I <sub>C</sub> = -10mA , R <sub>BE</sub> = ∞	-20			V
Diode forward voltage	V <sub>F</sub>	I <sub>F</sub> =0.5A			1.5	V
Base-emitter resistance	R <sub>BE</sub>			1.5		KΩ

## ■ Marking

Marking	BM
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