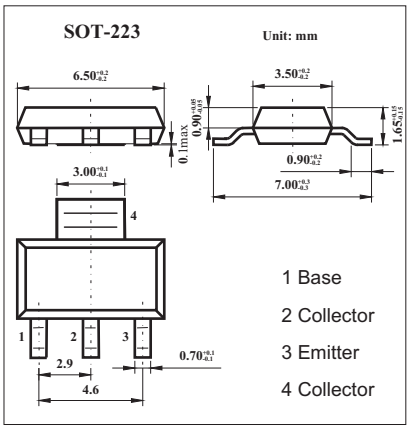
PNP Silicon Planar Medium Power Transistor

FZT589



Features

Power Collector dissipation: PC=2W

Continuous Collector Current: IC=-1A

Absolute Maximum Ratings Ta = 25℃

Parameter	Symbol	Rating	Unit
Collector-base voltage	V_{CBO}	-50	V
Collector-emitter voltage	V_{CEO}	-30	V
Emitter-base voltage	V_{EBO}	-5	V
Continuous Collector Current	I_C	-1	A
Peak collector current	I_{CM}	-2	A
Power Collector dissipation	P_C	2	W
Operating and storage temperature range	T_j,T_{stg}	-55 to +150	℃

Electrical Characteristics Ta = 25℃

Parameter	Symbol	Test conditons	Min	Typ	Max	Unit
Collector-base breakdown voltage	$V_{(BR)CBO}$	I_C=-100μA	-50			V
Collector-emitter breakdown voltage *	$V_{(BR)CEO}$	I_C=-1mA	-30			V
Emitter-base breakdown voltage	$V_{(BR)EBO}$	I_E=-100μA	-5			V
Collector cutoff current	I_{CBO}	V_{CB}=-30V, I_E=0			-100	nA
Emitter cut-off current	I_{EBO}	V_{EB}=-4V, I_C=0			-100	nA
Collector-emitter saturation voltage *	$V_{CE(sat)}$	I_C=-1A,I_B=-100mA			-0.35	V
Base-emitter saturation voltage *	$V_{BE(sat)}$	I_C=-1A,I_B=-100mA			-1.2	V
Base-emitter voltage *	$V_{BE(ON)}$	I_C=-1A,V_{CE}=-2V			-1.1	V
DC current gain	h_{FE}	I_C=-1mA, V_{CE}=-2V*	70			
		I_C=-500mA,V_{CE}=-2V	100		300	
		I_C=-1A, V_{CE}=-2V*	80			
		I_C=-2A, V_{CE}=-2V*	40			
Transition frequecy	f_T	I_C=-100mA,V_{CE}=-5V,f=100MHz	100			MHz
Output capacitance	C_{ob}	V_{CB}=-10V,f=1MHz			15	pF

* Pulse test: tp 300 μs; d 0.02.

Marking

Marking	589