



DESCRIPTION

PT6305 is a 48-Bit High Voltage Display Driver utilizing CMOS technology specially designed for VFD display panels. It provides a 48-bit shift register, a 48-bit latch and a high-voltage CMOS driver. The logic circuit operates on a 5 volt power supply (CMOS level input) making it possible for the PT6305 to be used in conjunction with a microcomputer. The driver block consists of an 80 volt, 50mA (max.) high voltage output buffer. Pin assignments and application circuits are optimized for easy PCB layout and cost saving benefits.

FEATURES

- CMOS technology
- Low power consumption
- 48-bit shift registers
- Data controlled via external transfer clock and latch
- High speed data transfer
- Wide operating temperature range: -40 to +85