

NHD-320240WG-ATMI-VZ#

Graphic Liquid Crystal Display Module

NHD-	Newhaven Display
320240-	320 x 240 pixels
WG-	Display Type: Graphic
A-	Model
T-	White LED Backlight
M-	STN-Blue (-)
I-	Transmissive, 6:00 Optimal View, Wide Temperature
VZ#-	Built-in Negative Voltage
	RoHS Compliant

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Document Revision History

Revision	Date	Description	Changed by
0	6/7/2007	Initial Release	-
1	4/7/2010	User guide reformat	MC
2	3/11/2011	Driver link updated	AK
3	4/12/2011	Pin description updated	AK
4	12/31/2013	Electrical characteristics updated	AK

Functions and Features

- 320 x 240 pixels
- Built-in Negative Voltage
- No Controller
- NT7086 Driver
- +5.0V power supply
- RoHS Compliant

Technical drawing of the NHD-320240WG-ATMI-VZ# display module, showing dimensions, pinout, and contact details.

Dimensions:

- Overall width: 160.0 ± 0.5
- Overall height: 109.0 ± 0.5
- Display area: 320×240 Dots
- Pin 1 to Pin 3 distance: 190.0 ± 10.0 (B/L Cable Length)
- Supporting tape length: 10.0
- Terminal thickness: 0.30 ± 0.05
- PCB thickness: 1.0
- Dot size: 0.36×0.34 (SCALE 10/1)
- Contact side: $1.25 \times 13 = 16.25$ (SCALE 2/1)

Pinout Table:

PIN NO.	SYMBOL
1	DB0
2	DB1
3	DB2
4	DB3
5	DISPOFF
6	FLM
7	M
8	LP
9	CP
10	VDD
11	VSS
12	VEE
13	V0
14	FGND

Contact Side Details:

- 4- $\phi 3.5$ PTH
- 4- $\phi 6.0$ PAD
- W=0.8
- P=1.25
- 1.25
- 18.75
- 5.0 $\pm$ 1.0

Other Labels:

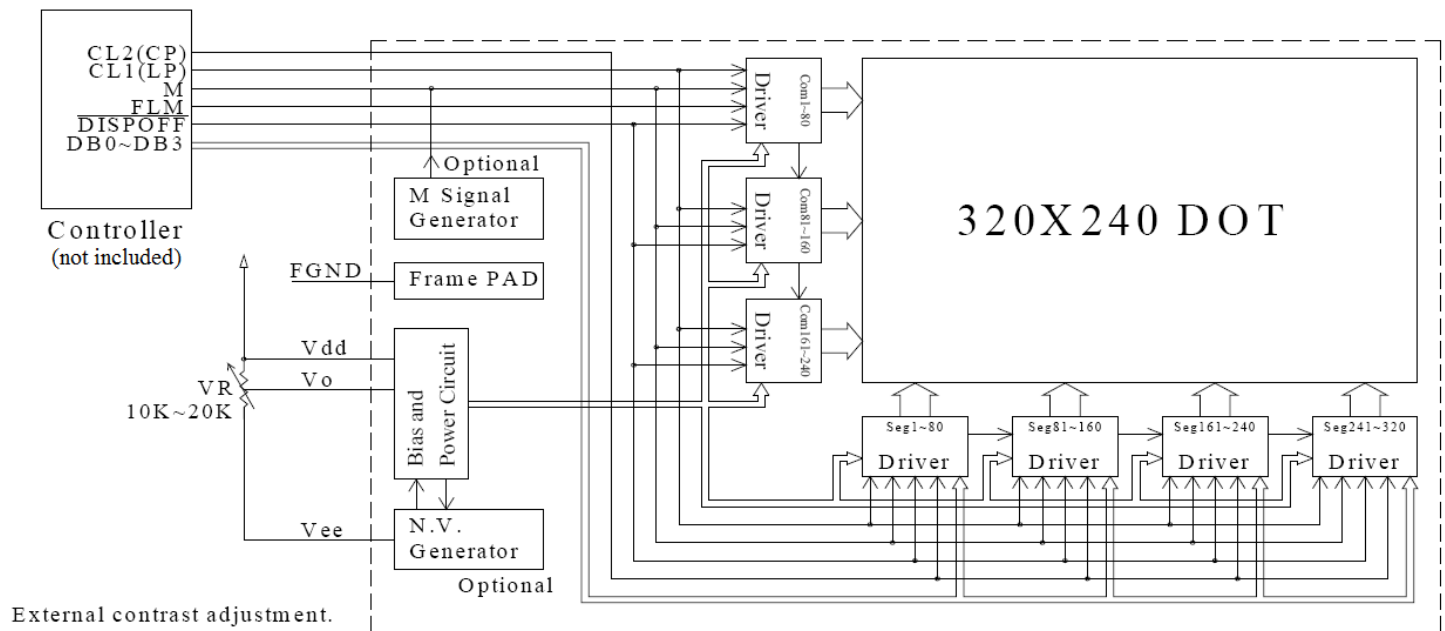
- JST XHP-3
- PIN 1
- PIN 3
- Max 13.0
- 6.5
- 2.15
- 4.0
- 8.5
- 11.31
- 25.0
- 28.41
- 122.0(VA)
- 115.18(AA)
- 152.0
- 141.0
- 11.0
- 4.0
- 104.7
- 101.0
- 92.0(VA)
- 86.38(AA)
- 206.0 $\pm$ 3.0
- 37.9
- 0.36
- 0.34
- 0.36
- 0.34
- DOT SIZE SCALE 10/1
- 1
- 14
- 1
- 14
- 1
- 14

Pin Description and Wiring Diagram

Pin No.	Symbol	External Connection	Function Description
1-4	DB0-DB3	MPU	Signal data bus
5	DISP OFF	MPU	Display On/Off
6	FLM	MPU	Scan Start-up signal
7	M	-	No Connect
8	LP	MPU	Data latch pulse
9	CP	MPU	Data shift pulse
10	VDD	Power Supply	Power supply for logic (+5.0V)
11	VSS	Power Supply	Ground
12	VEE	Power Supply	Negative voltage output (-25V)
13	VO	Adj. Power Supply	Power supply for contrast (approx. -19.0V)
14	FGND	-	No Connect

Recommended LCD connector: 1.25mm pitch, 14-pos FFC connector **Mates with:** Molex p/n 0039532144

Backlight connector: JST p/n: XHP-3 **Mates with:** JST p/n: B 3B-XH-A



Electrical Characteristics

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Operating Temperature Range	Top	Absolute Max	-20	-	+70	°C
Storage Temperature Range	Tst	Absolute Max	-30	-	+80	°C
Supply Voltage	VDD		2.7	5.0	5.5	V
Supply Current	IDD	Ta=25°C, VDD=5.0V	60	75	80	mA
Supply for LCD (contrast)	VDD-V0	Ta=25°C	23	23.6	24.2	V
"H" Level input	VIH		0.8*VDD	-	VDD	V
"L" Level input	VIL	-	0	-	0.2*VDD	V
"H" Level output	VOH	-	VDD-0.4	-	VDD	V
"L" Level output	VOL	-	-	-	0.4	V
Backlight Supply Voltage	VLED		3.4	3.5	3.6	V
Backlight Supply Current	ILED	VLED=3.5V	-	128	160	mA

Optical Characteristics

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Viewing Angle – Top		Cr ≥ 2	-	20	-	°
Viewing Angle – Bottom			-	40	-	°
Viewing Angle – Left			-	30	-	°
Viewing Angle – Right			-	30	-	°
Contrast Ratio	Cr		-	3	-	
Response Time (rise)	Tr	-	-	150	200	ms
Response Time (fall)	Tf	-	-	150	200	ms

Driver Information

Built-in NT7086 driver.

Please download specification at http://www.newhavendisplay.com/app_notes/NT7086.pdf

Timing Characteristics

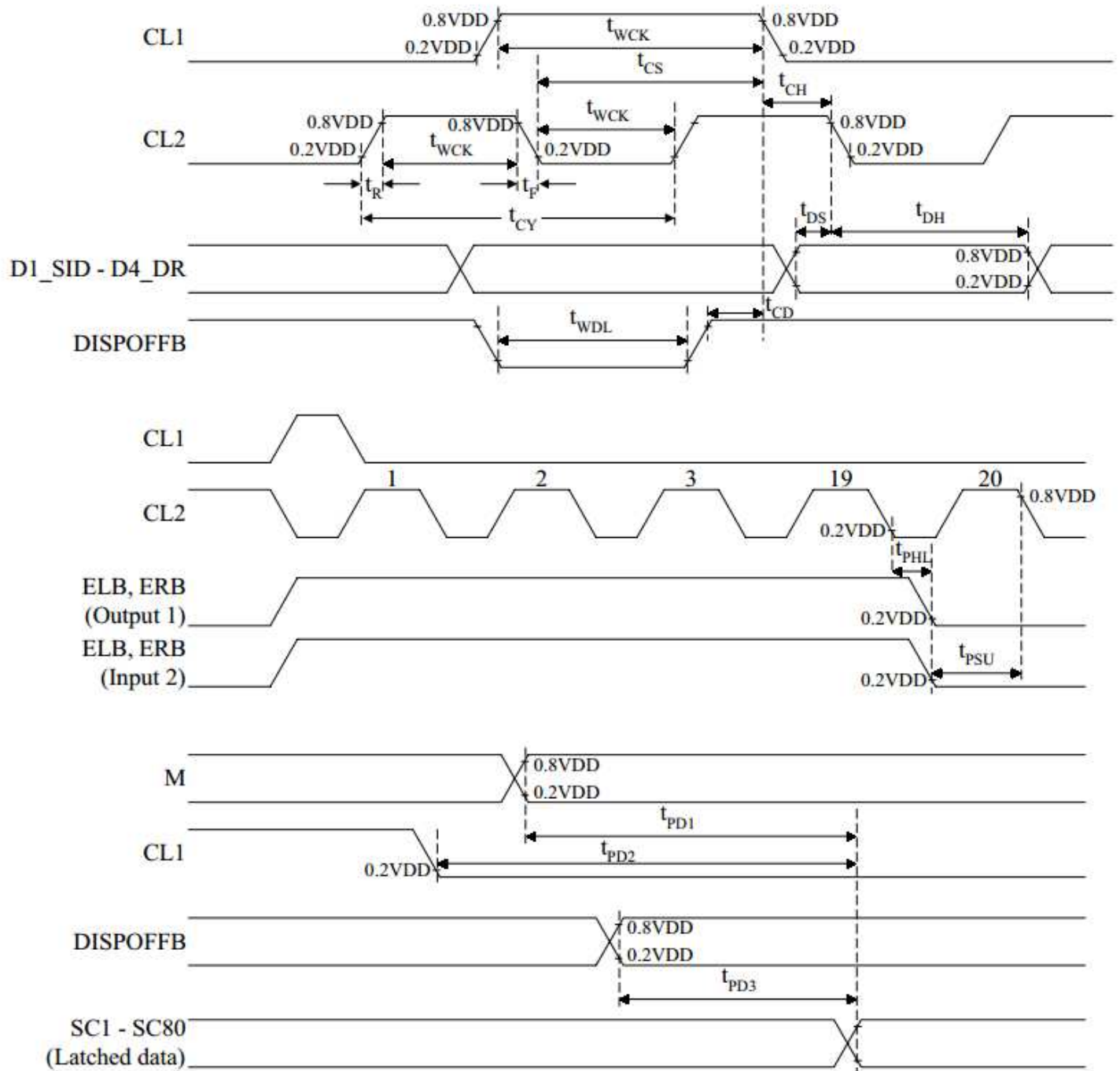
(1) Segment Driver Application

Characteristic	Symbol	Test condition	(1) VDD=5V±10%			(2) VDD=3V±10%			Unit
			Min.	Typ.	Max.	Min.	Typ.	Max.	
Clock cycle time	t _{CY}	Duty=50%	125	-	-	250	-	-	ns
Clock pulse width	t _{WCK}	-	45	-	-	95	-	-	
Clock rise/ fall time	t _R / t _F	-	-	-	-	-	-	30	
Data set-up time	t _{DS}	-	30	-	-	65	-	-	
Data hold time	t _{DH}	-	30	-	-	65	-	-	
Clock set-up time	t _{CS}	-	80	-	-	120	-	-	
Clock hold time	t _{CH}	-	80	-	-	120	-	-	
Propagation delay time	t _{PHL}	ELB output	-	-	60	-	-	125	
		ERB output	-	-	60	-	-	125	
ELB,ERB set-up time	t _{PSU}	ELB input	30	-	-	65	-	-	μ s
		ERB input	30	-	-	65	-	-	
DISPOFFB low pulse width	t _{WDL}	-	1.2	-	-	1.2	-	-	
DISPOFFB clear time	t _{CD}	-	100	-	-	100	-	-	
M – OUT propagation delay time	t _{PD1}	C _L =15pF	-	-	1.0	-	-	1.2	
CL1 – OUT propagation delay time	t _{PD2}		-	-	1.0	-	-	1.2	
DISPOFFB – OUT propagation delay time	t _{PD3}		-	-	1.0	-	-	-	

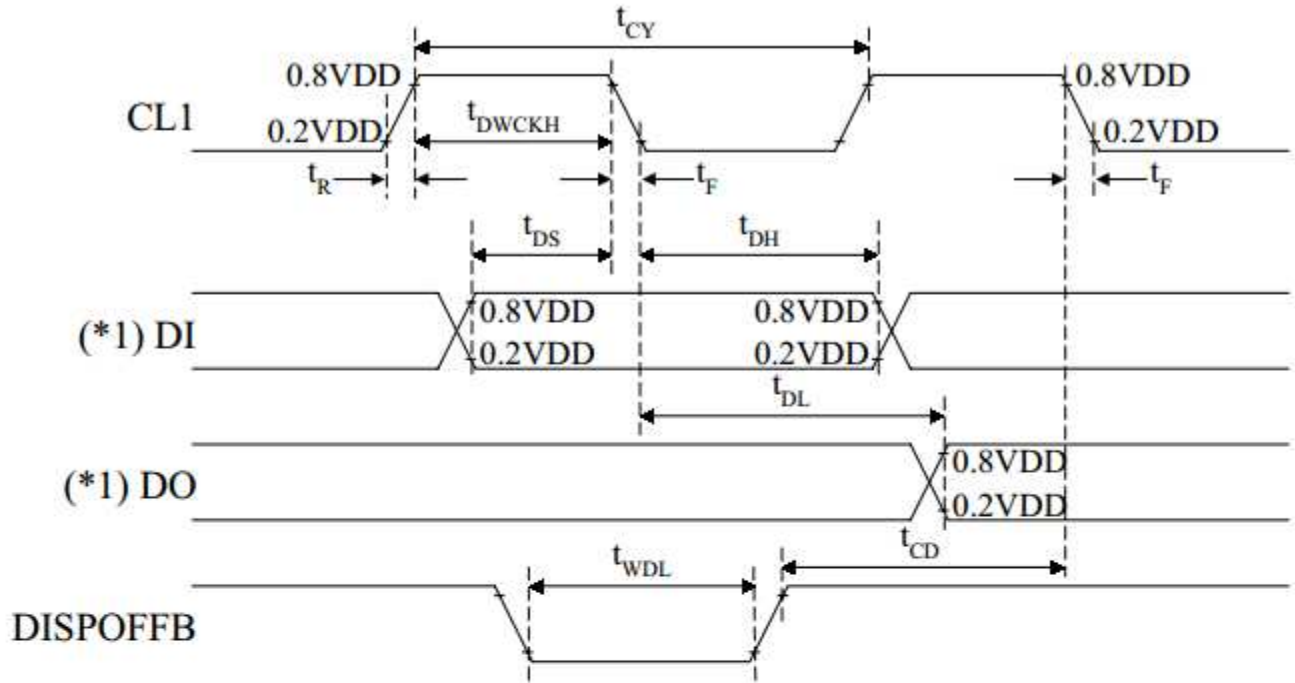
(2) Common Driver Application

Characteristic	Symbol	Test condition	(1) VDD=5V±10%			(2) VDD=3V±10%			Unit
			Min.	Typ.	Max.	Min.	Typ.	Max.	
Clock cycle time	t _{CY}	Duty=50%	250	-	-	500	-	-	ns
Clock pulse width	t _{WCK}	-	45	-	-	95	-	-	
Clock rise/ fall time	t _R / t _F	-	-	-	50	-	-	50	
Data set-up time	t _{DS}	-	30	-	-	65	-	-	
Data hold time	T _{DH}	-	30	-	-	65	-	-	
DISPOFFB low pulse width	t _{WDL}	-	1.2	-	-	1.2	-	-	μ s
DISPOFFB clear time	t _{CD}	-	100	-	-	100	-	-	ns
Output delay time	t _{DL}	C _L =15pF	-	-	200	-	-	250	
M – OUT propagation delay time	t _{PD1}		-	-	1.0	-	-	1.2	μ s
CL1 – OUT propagation delay time	t _{PD2}		-	-	1.0	-	-	1.2	
DISPOFFB – OUT propagation delay time	t _{PD3}		-	-	1.0	-	-	1.2	

(3) Segment Driver Application Timing



(4) Common Driver Application Timing



(*1) When in single-type interface mode

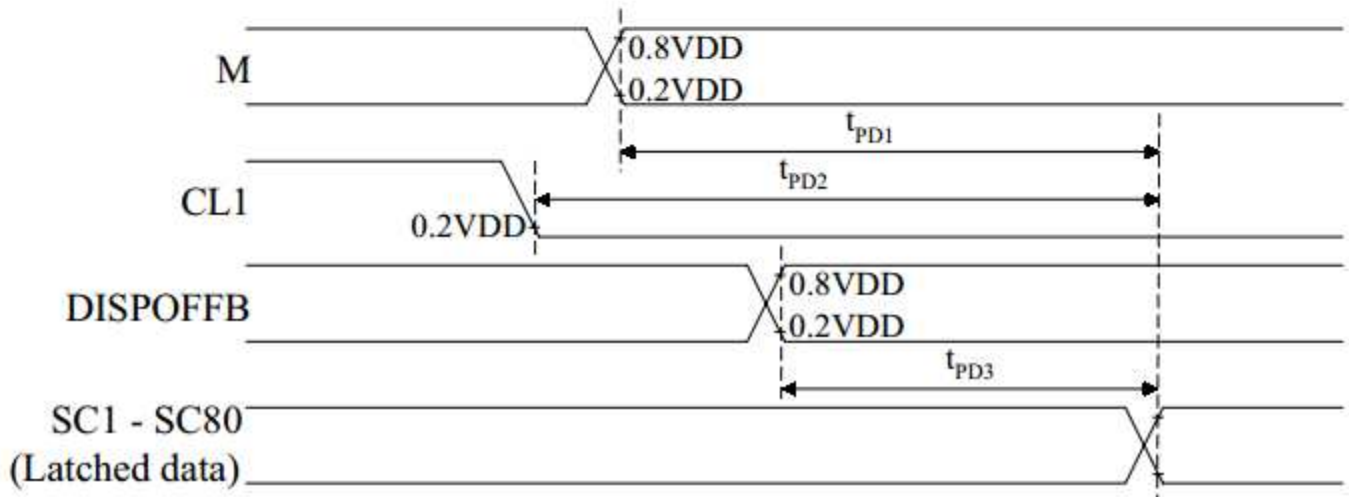
DI=>DDL(SHL=L), D4_DR(SHL=H)

DO=>D4_DR(SHL=L), D2_DL(SHL=H)

When in dual-type interface mode

DI=>D2_DL and D3_DM(SHL=L), D4_DR and D3_DM(SHL=H)

DO=>D4_DR(SHL=L), D2_DL(SHL=H)



Quality Information

Test Item	Content of Test	Test Condition	Note
High Temperature storage	Endurance test applying the high storage temperature for a long time.	+80°C , 200hrs	2
Low Temperature storage	Endurance test applying the low storage temperature for a long time.	-30°C , 200hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (voltage & current) and the high thermal stress for a long time.	+70°C 200hrs	2
Low Temperature Operation	Endurance test applying the electric stress (voltage & current) and the low thermal stress for a long time.	-20°C , 200hrs	1,2
High Temperature / Humidity Operation	Endurance test applying the electric stress (voltage & current) and the high thermal with high humidity stress for a long time.	+60°C , 90% RH , 96hrs	1,2
Thermal Shock resistance	Endurance test applying the electric stress (voltage & current) during a cycle of low and high thermal stress.	-20°C,30min -> 25°C,5min -> 70°C,30min = 1 cycle 10 cycles	
Vibration test	Endurance test applying vibration to simulate transportation and use.	10-55Hz , 15mm amplitude. 60 sec in each of 3 directions X,Y,Z For 15 minutes	3
Static electricity test	Endurance test applying electric static discharge.	VS=800V, RS=1.5kΩ, CS=100pF One time	

Note 1: No condensation to be observed.

Note 2: Conducted after 4 hours of storage at 25°C, 0%RH.

Note 3: Test performed on product itself, not inside a container.

Precautions for using LCDs/LCMs

See Precautions at www.newhavendisplay.com/specs/precautions.pdf

Warranty Information and Terms & Conditions

http://www.newhavendisplay.com/index.php?main_page=terms