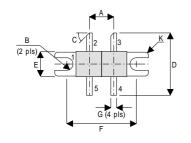
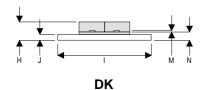


## **D2016UK**

### ROHS COMPLIANT METAL GATE RF SILICON FET

#### **MECHANICAL DATA**





PIN 1 SOURCE (COMMON) PIN 2 DRAIN 1

DRAIN 2 PIN 4 PIN<sub>3</sub> GATE 2

PIN 5 GATE 1

DIM	mm	Tol.	Inches	Tol.	
Α	6.45	0.13	0.254	0.005	
В	1.65R	0.13	0.065R	0.005	
С	45°	5°	45°	5°	
D	16.51	0.76	0.650	0.03	
Е	6.47	0.13	0.255	0.005	
F	18.41	0.13	0.725	0.005	
G	1.52	0.13	0.060	0.005	
Н	4.82	0.25	0.190	0.010	
- 1	24.76	0.13	0.975	0.005	
J	1.52	0.13	0.060	0.005	
K	0.81R	0.13	0.032R	0.005	
М	0.13	0.02	0.005	0.001	
N	2.16	0.13	0.085	0.005	

# **GOLD METALLISED MULTI-PURPOSE SILICON DMOS RF FET** 30W - 28V - 1GHz**PUSH-PULL**

### **FEATURES**

- SIMPLIFIED AMPLIFIER DESIGN
- SUITABLE FOR BROAD BAND APPLICATIONS
- VERY LOW C<sub>rss</sub>
- SIMPLE BIAS CIRCUITS
- LOW NOISE
- HIGH GAIN 10 dB MINIMUM

### **APPLICATIONS**

 HF/VHF/UHF COMMUNICATIONS from 1MHz to 2 GHz

# **ABSOLUTE MAXIMUM RATINGS** (T<sub>case</sub> = 25°C unless otherwise stated)

$\overline{P_D}$	Power Dissipation	117W
$BV_DSS$	Drain – Source Breakdown Voltage *	65V
$BV_GSS$	Gate – Source Breakdown Voltage *	±20V
I <sub>D(sat)</sub>	Drain Current *	6A
T <sub>stg</sub>	Storage Temperature	–65 to 150℃
T <sub>j</sub>	Maximum Operating Junction Temperature	200℃

Per Side

Semelab PIc reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by Semelab is believed to be both accurate and reliable at the time of going to press. However Semelab assumes no responsibility for any errors or omissions discovered in its use. Semelab encourages customers to verify that datasheets are current before placing orders.



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### **ELECTRICAL CHARACTERISTICS** (T<sub>case</sub> = 25°C unless otherwise stated)

Parameter		Test Conditions		Min.	Тур.	Max.	Unit
	PER SIDE						
B\/¬ o o	Drain-Source	V <sub>GS</sub> = 0	I <sub>D</sub> = 10mA	65			V
BV <sub>DSS</sub>	Breakdown Voltage	v <sub>GS</sub> = 0	ID = IOIIIA	03			
	Zero Gate Voltage	V <sub>DS</sub> = 28V	V <sub>GS</sub> = 0			6	m A
	Drain Current					0	mA
I <sub>GSS</sub>	Gate Leakage Current	V <sub>GS</sub> = 20V	V <sub>DS</sub> = 0			1	μΑ
V <sub>GS(th)</sub>	Gate Threshold Voltage *	I <sub>D</sub> = 10mA	$V_{DS} = V_{GS}$	1		7	V
9 <sub>fs</sub>	Forward Transconductance *	V <sub>DS</sub> = 10V	I <sub>D</sub> = 1.2A	1.08			S
	TOTAL DEVICE						
G <sub>PS</sub>	Common Source Power Gain	P <sub>O</sub> = 30W		10			dB
η	Drain Efficiency	V <sub>DS</sub> = 28V	I <sub>DQ</sub> = 1.2A	40			%
VSWR	Load Mismatch Tolerance	f = 1GHz		20:1			_
PER SIDE							
C <sub>iss</sub>	Input Capacitance	$V_{DS} = 0$	$V_{GS} = -5V f = 1MHz$			72	pF
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> = 28V	$V_{GS} = 0$ f = 1MHz			36	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	V <sub>DS</sub> = 28V	$V_{GS} = 0$ $f = 1MHz$			3	pF

<sup>\*</sup> Pulse Test: Pulse Duration = 300  $\mu s$  , Duty Cycle  $\leq$  2%

### **HAZARDOUS MATERIAL WARNING**

The ceramic portion of the device between leads and metal flange is beryllium oxide. Beryllium oxide dust is highly toxic and care must be taken during handling and mounting to avoid damage to this area.

#### THESE DEVICES MUST NEVER BE THROWN AWAY WITH GENERAL INDUSTRIAL OR DOMESTIC WASTE.

#### THERMAL DATA

R <sub>THj-case</sub>	Thermal Resistance Junction – Case	Max. 1.5°C / W
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Issue 1

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