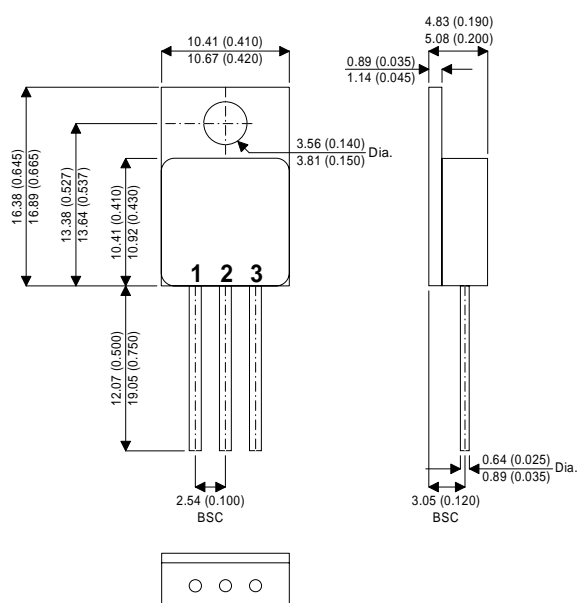


MECHANICAL DATA

Dimensions in mm (inches)


TO-257AA – Metal Package

Pad 1 – Gate Pad 2 – Drain Pad 3 – Source

**P-CHANNEL
POWER MOSFET
FOR HI-REL
APPLICATIONS**
 V_{DSS} – 200V
 $I_{D(cont)}$ – 6.5A
 $R_{DS(on)}$ 0.80 Ω
FEATURES

- HERMETICALLY SEALED TO-257AA METAL PACKAGE
- SIMPLE DRIVE REQUIREMENTS
- LIGHTWEIGHT
- SCREENING OPTIONS AVAILABLE
- ALL LEADS ISOLATED FROM CASE

ABSOLUTE MAXIMUM RATINGS ($T_{case} = 25^{\circ}C$ unless otherwise stated)

V_{GS}	Gate – Source Voltage	$\pm 20V$
I_D	Continuous Drain Current @ $T_{case} = 25^{\circ}C$	- 6.5A
I_D	Continuous Drain Current @ $T_{case} = 100^{\circ}C$	- 4.0A
I_{DM}	Pulsed Drain Current	- 28A
P_D	Power Dissipation @ $T_{case} = 25^{\circ}C$	75W
	Linear Derating Factor	0.6W/ $^{\circ}C$
T_J, T_{stg}	Operating and Storage Temperature Range	- 55 to 150 $^{\circ}C$
$R_{\theta JC}$	Thermal Resistance Junction to Case	1.67 $^{\circ}C/W$ max.

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ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$ unless otherwise stated)

Parameter		Test Conditions		Min.	Typ.	Max.	Unit
STATIC ELECTRICAL RATINGS							
BV _{DSS}	Drain – Source Breakdown Voltage	V _{GS} = 0	I _D = 1mA	- 200			V
ΔBV _{DSS}	Temperature Coefficient of	Reference to 25°C			- 0.2		V/°C
ΔT _J	Breakdown Voltage	I _D = 1mA					
R _{DS(on)}	Static Drain – Source On–State Resistance	V _{GS} = -10V	I _D = - 4.0A			0.80	Ω
		V _{GS} = -10V	I _D = - 6.5A			0.92	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS}	I _D = - 250μA	- 2		- 4	V
g _{fs}	Forward Transconductance	V _{DS} ≥ -15V	I _{DS} = - 4A	2			S(Ω)
I _{DSS}	Zero Gate Voltage Drain Current	V _{GS} = 0	V _{DS} = - 0.8BV _{DSS}			- 25	μA
			T _J = 125°C			- 250	
I _{GSS}	Forward Gate – Source Leakage	V _{GS} = - 20V				- 100	nA
I _{GSS}	Reverse Gate – Source Leakage	V _{GS} = - 20V				100	
DYNAMIC CHARACTERISTICS							
C _{iss}	Input Capacitance	V _{GS} = 0			700		pF
C _{oss}	Output Capacitance	V _{DS} = 25V			200		
C _{rss}	Reverse Transfer Capacitance	f = 1MHz			40		
Q _g	Total Gate Charge	V _{GS} = - 10V I _D = - 6.5A V _{DS} = - 0.5BV _{DSS}				31	nC
Q _{gs}	Gate – Source Charge	I _D = - 6.5A				7	nC
Q _{gd}	Gate – Drain (“Miller”) Charge	V _{DS} = - 0.5BV _{DSS}				17	
t _{d(on)}	Turn–On Delay Time	V _{DD} = - 100V				50	ns
t _r	Rise Time	I _D = - 6.5A				100	
t _{d(off)}	Turn–Off Delay Time	R _G = 7.5Ω				100	
t _f	Fall Time					80	
SOURCE – DRAIN DIODE CHARACTERISTICS							
I _S	Continuous Source Current					- 6.5	A
I _{SM}	Pulse Source Current					- 28	
V _{SD}	Diode Forward Voltage	I _S = - 6.5A T _J = 25°C V _{GS} = 0				- 6	V
t _{rr}	Reverse Recovery Time	I _S = - 6.5A T _J = 25°C				400	ns
Q _{rr}	Reverse Recovery Charge	d _i / d _t ≤ 100A/μs V _{DD} ≤ - 50V				4	μC
PACKAGE CHARACTERISTICS							
L _D	Internal Drain Inductance	(from 6mm down drain lead pad to centre of die)			8.7		nH
L _S	Internal Source Inductance	(from 6mm down source lead to centre of source bond pad)			8.7		

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