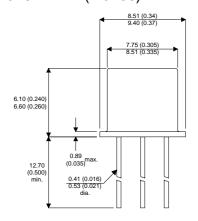
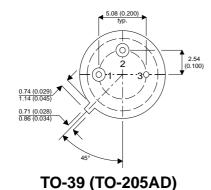


#### **MECHANICAL DATA**

Dimensions in mm (inches)





Pin 1 - Emitter

Pin 2 - Base

Pin 3 - Collector

# **PNP SILICON TRANSISTORS**

### **DESCRIPTION**

The 2N5679 and 2N5680 are silicon epitaxial planar PNP transistors in jedec TO-39 metal case intended for use as drivers for high power transistors in general purpose, amplifier and switching circuit

The complementary NPN types are the 2N5681 and 2N5682 respectively

## **ABSOLUTE MAXIMUM RATINGS**

T <sub>CASE</sub> = 25°c unless otherwise stated		2N5679	2N5680	
$V_{CBO}$	Collector – Base Voltage	-100V	-120V	
$V_{CEO}$	Collector – Emitter Voltage (I <sub>B</sub> = 0)	-100V	-120V	
$V_{EBO}$	Emitter – Base Voltage (I <sub>C</sub> = 0)	-4V		
$I_{\mathbb{C}}$	Continuous Collector Current	-1A		
I <sub>B</sub>	Base Current -0.5A		5A	
$P_{tot}$	Total Dissipation at T <sub>case</sub> ≤ 25°C	10W		
	T <sub>amb</sub> ≤ 25°C	11	W	
$T_{stg}$	Operating and Storage Temperature Range	−65 to +200°C		
Tj	Junction temperature 200°C		)°C	

Semelab PIc reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by Semelab is believed to be both accurate and reliable at the time of going to press. However Semelab assumes no responsibility for any errors or omissions discovered in its use. Semelab encourages customers to verify that datasheets are current before placing orders.

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### THERMAL DATA

R <sub>thj-case</sub>	Thermal Resistance Junction-case	Max	17.5	°C/W
R <sub>thj-amb</sub>	Thermal Resistance Junction-ambient	Max	175	°C/W

# **ELECTRICAL CHARACTERISTICS** (T<sub>case</sub> = 25°C unless otherwise stated)

Parameter		Test Conditions		Min.	Тур.	Max.	Unit	
		I <sub>E</sub> = 0						
I <sub>CBO</sub>	Collector Cut Off Current	for 2N5679	$V_{CB} = -100V$			-1	μΑ	
		for 2N5680	$V_{CB} = -120V$			-1		
1	Collector Cut Off Current	V <sub>BE</sub> = 1.5					μA	
I <sub>CEV</sub>		for 2N5679	$V_{CE} = -100V$			-1		
		for 2N5680	$V_{CE} = -120V$			-1		
		T <sub>case</sub> = 150℃	;					
		for 2N5679	$V_{CE} = -100V$			-1	mA	
		for 2n5680	$V_{CE} = -120V$			-1		
I <sub>CEO</sub>		$I_B = 0$						
	Collector Cut Off Current	for 2N5679	$V_{CE} = -70V$			-10	μA	
		for 2N5680	$V_{CE} = -80V$			-10		
I <sub>EBO</sub>	Emitter Cut Off Current	I <sub>C</sub> = 0	V <sub>EB</sub> = -4V			-1		
	Collector Emitter Sustaining Voltage	$I_B = 0$	I <sub>C</sub> = -10mA					
V <sub>CEO(sus)*</sub>		for 2N5679		-100				
3 – 3 (3 3 3 )		for 2N5680		-120				
	Collector Emitter Saturation Voltage	$I_{C} = -250 \text{mA}$	$I_B = -25 \text{mA}$			-0.6	V	
V <sub>CE(sat)*</sub>		$I_{C} = -500 \text{mA}$	$I_B = -50 \text{mA}$			-1		
, ,		I <sub>C</sub> = -1A	$I_{B} = -200 \text{mA}$			-2		
V <sub>BE*</sub>	Base Emitter Voltage	$I_{C} = -250 \text{mA}$	V <sub>CE</sub> = -2V			-1	1	
L	DC Current Gain	$I_{C} = -250 \text{mA}$	V <sub>CE</sub> = -2V	40		150		
h <sub>FE*</sub>		I <sub>C</sub> = -1A	V <sub>CE</sub> = -2V	5			1	
f <sub>T</sub>	Transistion Frequency	$I_{C} = -100 \text{mA}$	V <sub>CE</sub> = -10V	20			MHz	
		f = 10MHz		30				
C <sub>CBO</sub>	Collector Base Capacitance	I <sub>E</sub> = 0	V <sub>CB</sub> = -20V			50	nE	
		f = 1MHz				50	pF	
h <sub>fe</sub>	Concil Circuit Coin	$I_{C} = -0.2A$	$V_{CE} = -1.5V$	40				
	Small Signal Current Gain	f = 1KHz						

<sup>\*</sup> Pulse test  $t_p = 300\mu s$  ,  $\delta < 2\%$ 

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Document Number 3076 Issue 1