

SILICON EPITAXIAL

NPN TRANSISTOR

General purpose power transistor for switching and linear applications in a

hermetic TO-39 package.



MECHANICAL DATA

Dimensions in mm (inches)

8.51 (0.34) 9.40 (0.37) 7.75 (0.305) 8.51 (0.335) 6.10 (0.240) 6.60 (0.260) 0.89 12.70 (0.500)

0.74 (0.029)

TO-39 (TO-205AD) PACKAGE

PIN 1 - Emitter

PIN 2 - Base

PIN 3 - Collector

ABSOLUTE MAXIMUM RATINGS (T_A = 25°C unless otherwise stated)

V_{CBO}	Collector – Base Voltage	500V	
V_{CER}	Collector – Emitter Voltage $R = 10\Omega$	500V	
V_{EBO}	Emitter – Base Reverse Voltage	5V	
I _C	Continuous Collector Current	0.5A	
P_TOT	Total Device Dissipation $T_C = 25^{\circ}C$	2W	
$T_{J_{I}}T_{STG}$	Maximum Storage and Junction Temperature Range	200°C	
$R_{\theta JC}$	Thermal Impedance Junction To Case	50°C/W	

FEATURES

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise stated)

	Parameter	Test Conditions	s	Min.	Тур.	Max.	Unit
I _{CBO}	Collector Base Leakage Current	V _{CB} = 500V				0.006	mA
h _{FE}	D.C Current Gain	V _{CE} = 10V	$I_C = 0.025A$	30		180	_
f _t	Transition Frequency				20		MHz

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