



End of Life Notification

August 17th, 2012

To: To whom it may concern:

Re: PCN for EOL of DL1L5*K delay line series.

All DL1L5*K parts, except those in current inventory, will no longer be available to order after December 31, 2012. TFT is eliminating the manufacturing of its DL1L5*K delay line series. These products are not planning to be replaced by a new part number.

The delay line families affected by this change include the products starting with the following prefixes :

- DL1L5*K Where the * = can be one of the following letters W , X, Y, Z, O or numbers 3, 4, 5.

All last time orders for these parts must be placed before the December 31, 2012. All parts ordered must be delivered by March 31, 2013.

All products currently in stock will be available until the stock is gone. The MOQ of 100 pcs will be enforced unless the remaining stock is less than 100 pcs.

If you have any further questions regarding this issue please contact your TFT Sales Person or Customer Service Representative.

Best Regards,

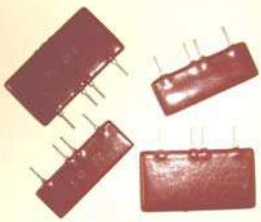
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Product Family: [4-pin Through Hole Delay Line](#)

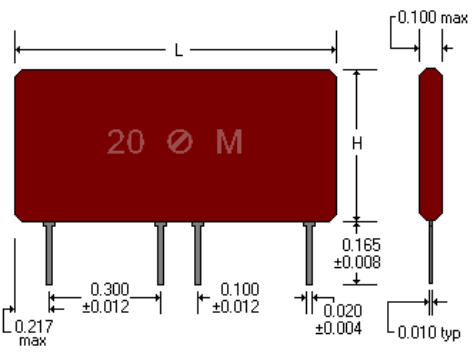
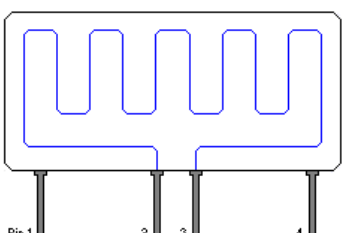
Part Number Series: [DL1L5, 4-pin Series](#)

	<p>Construction:</p> <ul style="list-style-type: none"> • High Purity Alumina or Zirconium Substrate • 4-pin, through hole configuration • Available in RoHS and non-RoHS versions. RoHS version utilizes exemption 7a. 	<p>Features:</p> <ul style="list-style-type: none"> • Single-ended (1 delay element) • Time delays of 0.1ns to 10.0ns • Tolerances as tight as ± 0.05ns • 50Ω impedance • High volume production suitable for commercial and special applications
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Description:

These 4-pin delay lines offer excellent performance and small size. The micro-strip construction is ideal for high frequency and tight tolerance timing and deskew applications. The performance of these equally distributed capacitance delay lines is significantly better than lumped element delay lines and the physical size is much smaller than delays obtained by using cable.

Product Dimensions, Schematic and Marking (All dimensions are shown in inches):

 <p>Dimensions shown include: L (length), H (height), 0.100 max (height), 0.217 max (pin height), 0.300 ±0.012 (pin spacing), 0.100 ±0.012 (pin width), 0.020 ±0.004 (pin diameter), 0.010 typ (pin diameter), 0.165 ±0.008 (microstrip width), and 0.100 max (microstrip thickness).</p>	<table border="1"> <thead> <tr> <th>Time Delay</th> <th>"H" dimension</th> <th>"L" dimension</th> </tr> </thead> <tbody> <tr> <td>0.1~1.3ns</td> <td>0.295 max.</td> <td>1.04 max.</td> </tr> <tr> <td>1.4~2.5ns</td> <td>0.400 max.</td> <td>1.04 max.</td> </tr> <tr> <td>2.6~3.4ns</td> <td>0.500 max.</td> <td>1.10 max.</td> </tr> <tr> <td>3.5~10.0ns</td> <td>0.610 max.</td> <td>1.10 max.</td> </tr> </tbody> </table> <p>Product marking will be composed of the following:</p> <ul style="list-style-type: none"> • 2-digit time delay code representing full and tenths of nanoseconds (ie 20=2.0ns) • TFT "Don't Stop" logo • 1-digit manufacturing code 	Time Delay	"H" dimension	"L" dimension	0.1~1.3ns	0.295 max.	1.04 max.	1.4~2.5ns	0.400 max.	1.04 max.	2.6~3.4ns	0.500 max.	1.10 max.	3.5~10.0ns	0.610 max.	1.10 max.	<p>Schematic:</p>  <p>Pin 1, 2, 3, 4</p> <ul style="list-style-type: none"> • Part is symmetrical (orientation can be reversed) • Pins 2 & 3 are Input/Output • Pins 1 & 4 should be connected to ground
Time Delay	"H" dimension	"L" dimension															
0.1~1.3ns	0.295 max.	1.04 max.															
1.4~2.5ns	0.400 max.	1.04 max.															
2.6~3.4ns	0.500 max.	1.10 max.															
3.5~10.0ns	0.610 max.	1.10 max.															

Part Numbering: Ex: DL1L54K200S-C

Product Designator	Number of Elements	Impedance Code	Package Code Height/Length	Time Delay	Delay Type	RoHS Indicator
DL	1L = 1	5 = 50 Ω	3K = 0.295"/1.04" 4K = 0.400"/1.04" 5K = 0.500"/1.10" OK = 0.610"/1.10"	Three numeric characters representing nanoseconds x 100 (i.e. 200=2.00ns, A00=10.0ns)	S = Single	-C = RoHS (leave blank for leaded)

Examples:

Part Number	Product Height	Product Length	Time Delay	Package Qty	RoHS
DL1L53K050S-C	0.295"	1.04"	0.5ns	100 pcs/bag	Yes*
DL1L54K200S	0.400"	1.04"	2.0ns	100 pcs/bag	No
DL1L55K300S	0.500"	1.10"	3.0ns	100 pcs/bag	No
DL1L50KA00S-C	0.610"	1.10"	10.0ns	100 pcs/bag	Yes*

* RoHS version utilizes exemption 7a.

Electrical Specifications:

Product Designator -->	DL1L53K		DL1L54K	DL1L55K		DL1L50K		
Product Height (inches)	0.295		0.400	0.500		0.610		
Time Delay	0.1 ~ 1.0ns	1.1 ~ 1.3ns	1.4 ~ 2.5ns	2.6ns	2.8 ~ 3.4ns	3.6 ~ 4.8ns	5.0ns	5.5 ~ 10.0ns
Time delay increments	0.1ns steps			0.2ns steps				0.5ns steps
Time delay tolerance	±0.05ns			-0.05/+0.1ns	±0.1ns		-0.1/+0.25ns	±0.25ns
Impedance	50Ω ±10%							
DC Resistance	0.8 Ω max.	0.8 Ω/ns max.						
Rise Time	1.0ns max.			2.0ns max.				
Rated Current	100 mA							
Temp. coef. of time delay	150 ppm/°C							
Insulation Resistance	100 Vdc (1 minute minimum)							
Isolation Resistance (Sgn-Gnd)	100MΩ minimum @ 50 Vdc							
Operating Temp. Range	-40°C ~ +85°C							
Storage Temp. Range	-55°C ~ +125°C							
Packaging	100 pcs/bag (bag contains tag board to protect leads from bending)					50 pcs/bag (bag contains tag board to protect leads from bending)		