

500 WATT ULTRA LOW CAPACITANCE STEERING DIODE/TVS ARRAY



DESCRIPTION

The PSRDAxx-6 Series are low capacitance multi-line steering diode/transient voltage suppressor arrays that provides board level protection for standard TTL and CMOS bus line applications against the damaging effects of ESD, tertiary lightning and switching transients.

The PSRDAxx-6 Series has a peak pulse power rating of 500 Watts for an $8/20\mu s$ waveshape. This device series meets the IEC 61000-4-2, IEC 61000-4-4 and IEC 61000-4-5 requirements.

FEATURES

- Compatible with IEC 61000-4-2 (ESD): Air 15kV, Contact 8kV
- Compatible with IEC 61000-4-4 (EFT): 40A 5/50ns
- Compatible with IEC 61000-4-5 (Surge): 24A, 8/20μs Level 2(Line-Gnd) & Level 3(Line-Line)
- 500 Watts Peak Pulse Power per Line (tp = 8/20µs)
- Bidirectional Configuration
- Available in 3.3V and 5V
- Protects Up to Six Lines
- Ultra Low Capacitance: 5pF
- RoHS Compliant
- REACH Compliant

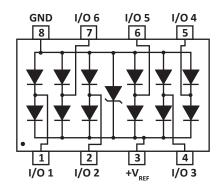
MECHANICAL CHARACTERISTICS

- Molded JEDEC SO-8 Package
- Approximate Weight: 70 milligrams
- Lead-Free Pure-Tin Plating (Annealed)
- Solder Reflow Temperature:
 - Pure-Tin Sn, 100: 260-270°C
- 12mm Tape and Reel Per EIA Standard 481
- Flammability Rating UL 94V-0

APPLICATIONS

- Computer Interface Protection
- Ethernet 10/100/1000 Base T
- Set-Top Box Protection
- Industrial Controls

PIN CONFIGURATION





TYPICAL DEVICE CHARACTERISTICS

MAXIMUM RATINGS @ 25°C Unless Otherwise Specified							
PARAMETER	SYMBOL	VALUE	UNITS				
Peak Pulse Power (tp = $8/20\mu s$) - See Figure 1	P _{pp}	500	Watts				
Operating Temperature	T _L	-55 to 150	°C				
Storage Temperature	T _{stg}	-55 to 150	°C				
Continuous Power Dissipation	P _{PC}	1000	mW				
Maximum Forward Voltage @ 10mA (Note 1)	V _F	1.1	Volts				
NOTES	•	•	•				

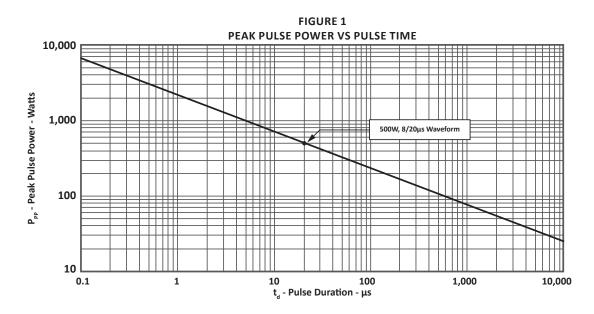
NOTES

1. Measured between pins 8 to 1, 2, 3, 4, 5, 6 or 7.

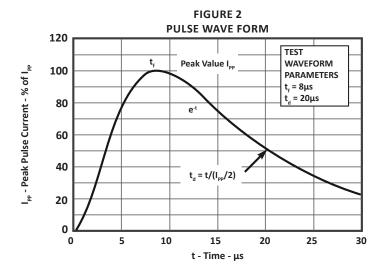
ELECTRICAL CHARACTERISTICS PER LINE @ 25°C Unless Otherwise Specified									
PART NUMBER	DEVICE MARKING	RATED STAND-OFF VOLTAGE	MINIMUM BREAKDOWN VOLTAGE @1mA	MAXIMUM CLAMPING VOLTAGE (Fig. 2) @I _p = 1A	MAXIMUM LEAKAGE CURRENT @V _{wm}	MAXIMUM CAPACITANCE (Note 1) (Fig. 5) @0V, 1MHz			
		V _{wм} VOLTS	V _(BR) VOLTS	V _c VOLTS	Ι _D μΑ	C _{J(SD)} pF			
PSRDA3.3-6	SGG	3.3	4.0	6.5	125	5			
PSRDA05-6	SGH	5.0	6.0	9.8	20	5			

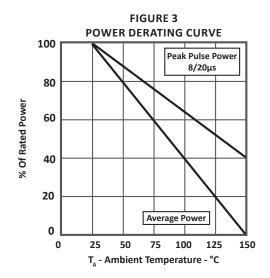
NOTES

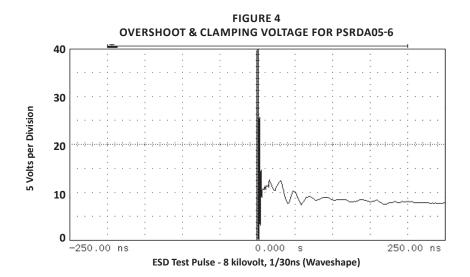
1. Capacitance measured at $V_{WM} = V_{CC}$ connected between I/O pins to pin 8 (Gnd). $V_R = V_{WM}$ @ 1MHz. As shown in Figure 6, REF1 is connected to ground, REF2 is connected to + $V_{CC'}$ and input applies to $V_{CC} = 5V$, $V_{SIGN} = 30$ mV, F = 1MHz



TYPICAL DEVICE CHARACTERISTICS







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TYPICAL DEVICE CHARACTERISTICS

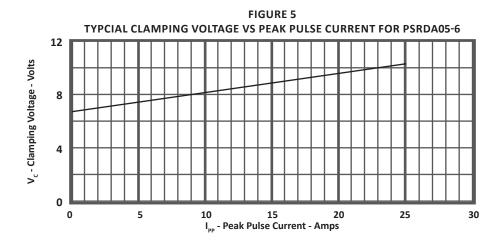


FIGURE 6
INPUT CAPACITANCE CIRCUIT

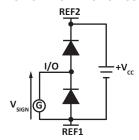
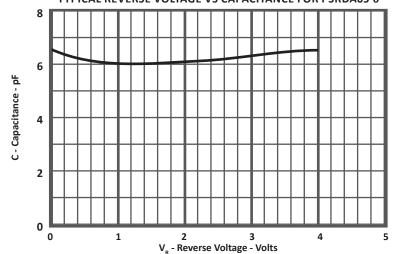


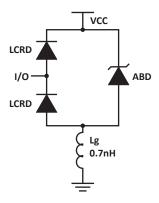
FIGURE 7
TYPICAL REVERSE VOLTAGE VS CAPACITANCE FOR PSRDA05-6



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SPICE MODEL

FIGURE 1 SPICE MODEL



ABD - Avalanche Breakdown Diode (TVS) LCRD: Low Capacitance Rectifier Diode Lg - Lead Inductance

TABLE 1 - SPICE PARAMETERS							
PARAMETER	UNIT	ABD(TVS)	LCRD				
BV	V	See Table 2	200				
IBV	μΑ	1	0.01				
C _{jo}	pF	See Table 2	5				
I _s	А	See Table 2	1E-13				
Vj	V	0.6	0.6				
М	-	0.33	0.33				
N	-	1	1				
R _s	Ohms	See Table 2	0.31				
TT	S	1E-8	1E-9				
EG	eV	1.11	1.11				

TABLE 2 - ABD SPECIFIC SPICE PARAMETERS							
PART NUMBER B _V (VOLTS) C _{io} (pF) I _s (AMPS) Rs(OHMS)							
PSRDA3.3-6	4.0	438	1E-11	0.21			
PSRDA05-6	6.0	284	1E-11	0.14			

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APPLICATION INFORMATION

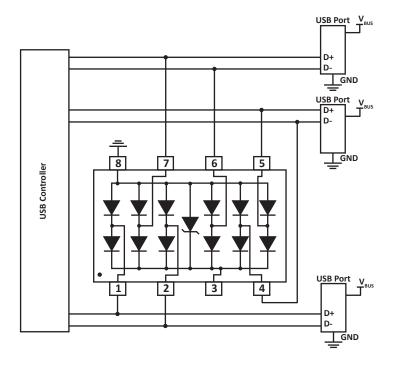


FIGURE 1 - DIFFERENTIAL MODE USB PROTECTION

Circuit connectivity is as follows:

- Pins 1, 2, 4, 5, 6 and 7 connected to the datalines.
- Pin 8 connected to ground.
- Pin 3 connected to the databus.

CIRCUIT BOARD RECOMMENDATIONS

Circuit board layout is critical for electromagnetic compatibility protection. The following guidelines are recommended:

- The protection device should be placed near the input terminals or connectors, the device will divert the transient current immediately before it can be coupled into the nearby traces.
- The path length between the TVS device and the protected line should be minimized.
- All conductive loops including power and ground loops should be minimized.
- The transient current return path to ground should be kept as short as possible to reduce parasitic inductance.
- Ground planes should be used whenever possible. For multilayer PCBs, use ground vias.

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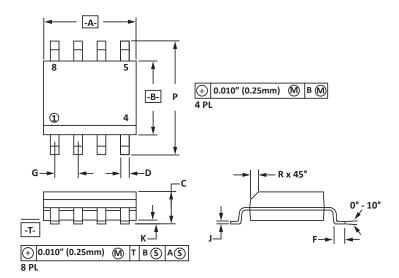


SO-8 PACKAGE INFORMATION

OUTLINE DIMENSIONS								
DIM	MILLIN	METERS	INCHES					
DIIVI	MIN	MAX	MIN	MAX				
Α	4.80	5.00	0.189	0.196				
В	3.80	4.00	0.150	0.157				
С	1.35	1.75	0.054	0.068				
D	0.35	0.49	0.014	0.019				
F	0.40 1.25 0.016		0.016	0.049				
G	1.27	BSC	0.05	BSC				
J	0.18	0.25	0.007	0.009				
К	0.10	0.25	0.004	0.008				
Р	5.80	6.20	0.229	0.244				
R	0.25	0.50	0.010	0.019				

NOTES

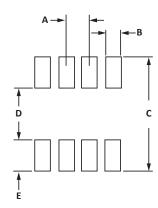
- 1. -T- = Seating plane and datum surface.
- 2. Dimensions "A" and "B" are datum.
- 3. Dimensions "A" and "B" do not include mold protrusion.
- 4. Maximum mold protrusion is 0.015" (0.380mm) per side.
- 5. Dimensioning and tolerances per ANSI Y14.5M, 1982.
- 6. Dimensions are exclusive of mold flash and metal burrs.



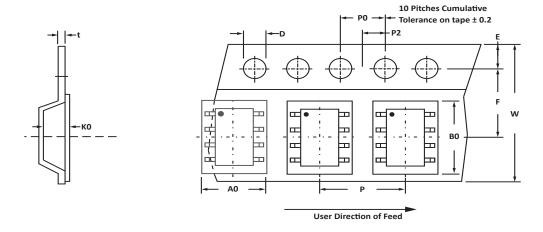
PAD LAYOUT DIMENSIONS								
DIM	MILLIN	IETERS	INCHES					
	MIN	MAX	MIN	MAX				
Α	1.14	1.40	0.045	0.055				
В	0.64	0.89	0.025	0.035				
С	6.22	-	0.245	-				
D	3.94	4.17	0.155	0.165				
Е	1.02	1.27	0.040	0.050				

NOTES

1. Controlling dimension: inches.



TAPE AND REEL



SPECIFICATIONS												
REEL DIA.	TAPE WIDTH	A0	В0	ко	D	E	F	w	P0	P2	Р	tmax
178mm (7")	12mm	6.50 ± 0.10	5.40 ± 0.10	2.00 ± 0.10	1.50 ± 0.10	1.75 ± 0.10	5.50 ± 0.05	12.00 ± 0.30	4.00 ± 0.12	2.00 ± 0.10	4.00 ± 0.10	0.25

NOTES

- 1. Dimensions are in millimeters.
- 2. Surface mount product is taped and reeled in accordance with EIA-481.
- 3. Suffix T7 = 7" Reel 1,000 pieces per 12mm tape.
- 4. Suffix T13 = 13" Reel 2,500 pieces per 12mm tape.
- 5. Bulk product shipped in tubes of 98 pieces per tube.
- 6. Marking on Part marking code (see page 2), date code, logo and pin one defined by dot on top of package.

Package outline, pad layout and tape specifications per document number 06009.R3 9/10.

ORDERING INFORMATION									
BASE PART NUMBER (xx = Voltage)	LEADFREE SUFFIX	TAPE SUFFIX	QTY/REEL	REEL SIZE	TUBE QTY				
PSRDAxx-6	-LF	-T7	1,000	7"	98				
PSRDAxx-6 -LF -T13 2,500 13" 98									
This device is only available in a Lead-Free configuration.									

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COMPANY INFORMATION

COMPANY PROFILE

In business more than 20 years, ProTek Devices™ is a privately-held company located in Tempe, Arizona, that offers a product line of transient voltage suppressors (TVS); avalanche breakdown diodes; steering diode TVS arrays and other surge suppressor component products. These TVS devices protect electronic systems from the effects of lightning, electrostatic discharge (ESD), nuclear electromagnetic pulses (NEMP), inductive switching and EMI / RFI. ProTek Devices also offers high performance interface and linear products that include analog switches; multiplexers; LED drivers; audio control ICs; RF and related high frequency products. The analog devices work in a host of consumer; industrial; automotive and other applications.

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