



## SM537-1 ADSL Line Transformers



## Features

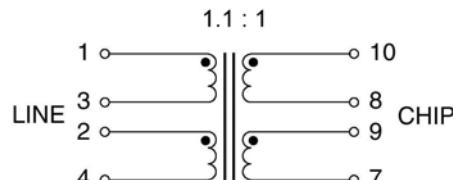
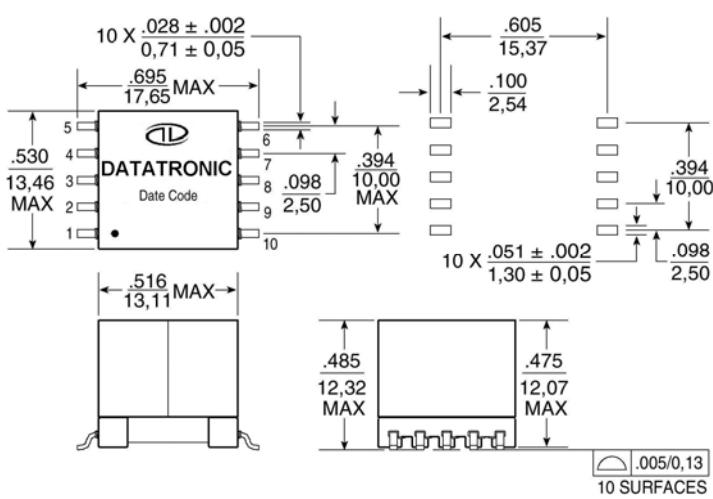
- For use with Analog Devices AD20msp930 ADSL Chipset (Central Office)
- Excellent Total Harmonic Distortion (THD) Performance
- SMT and Small size package
- Designed to meet UL1950 and EN60950 supplementary insulation requirements for voltages up to 250 Vrms maximum

## ELECTRICAL SPECIFICATIONS AT 25°C:

**OPERATING TEMP: -40°C to +85°C**

URNS RATIO (10-7):(1-4)	1 : 1.1±2%
OCL @10kHz, 0.1V	
(1-4) WITH (2-3) SHORTED	2.03mH MIN
(10-7) WITH (8-9) SHORTED	1.75mH MIN
LEAKAGE INDUCTANCE @100kHz 0.1V	
(10-7) WITH (2-3), (1-4), (8-9) SHORTED	7.5µH MAX
LONGITUDINAL BALANCE	
10kHz TO 1.1MHz	50dB MIN
INSERTION LOSS @300kHz	<0.5dB
ISOLATION VOLTAGE (MAX)	1500 Vrms
LOAD RESISTANCE	100Ω
DC RESISTANCE	
(1-4) WITH (2-3) SHORTED	1.30Ω MAX
(10-7) WITH (8-9) SHORTED	1.50Ω MAX
THD LINEARITY @30kHz	-80dB TYP
APPLICATION	CO

## OUTLINE DIMENSIONS



## SCHEMATIC

**NOTES:**

1. Dimensions are shown in inches/millimeters  
Unless otherwise specified, tolerance  
= Inches:  $\pm 0.010$   
= Millimeters:  $\pm 0.25$
2. For non-RoHS parts replace SM prefix with 42-  
(e.g. SM537-1 becomes 42-537-1)
3. Terminal finish is compliant to RoHS  
requirements. Solder in accordance with  
J-STD-020C.

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All specifications are subject to change without notice.