

## SM531-1 and PT532-1 ADSL Line Transformers



### Features

- For use with Analog Devices AD20msp918 ADSL Chipset
- Excellent Total Harmonic Distortion (THD) performance in a small size
- Both SMD and Thru-Hole Package Version
- Designed to meet UL1950 and EN60950 Supplementary Insulation requirements for voltages up to 250Vrms maximum

### ELECTRICAL SPECIFICATIONS AT 25°C:

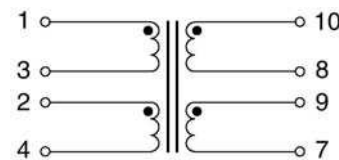
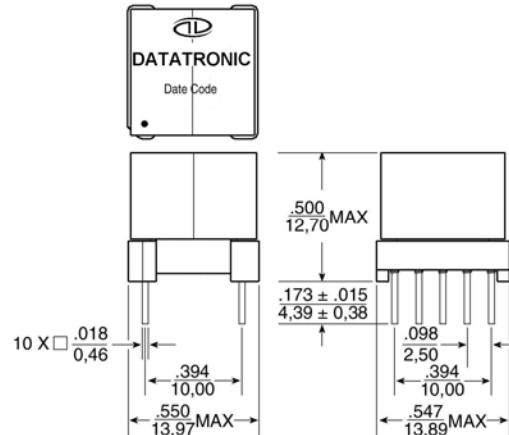
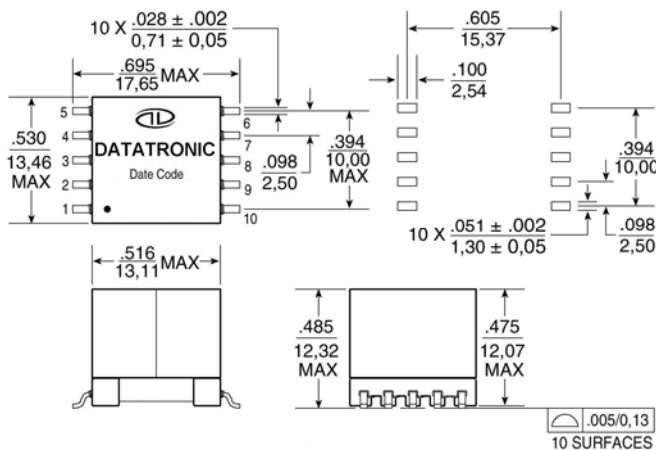
### OPERATING TEMP: 0°C TO 70°C

|   |             |
|---|-------------|
| URNS RATIO                              | 1 : 1.27±2% |
| OCL @10kHz, 0.1V                        |             |
| (1-4) WITH (2-3) SHORTED                | 1.80mH MIN  |
| (10-7) WITH (8-9) SHORTED               | 2.90mH MIN  |
| LEAKAGE INDUCTANCE @100kHz 0.1V         |             |
| (1-4) WITH (2-3), (8-9), (10-7) SHORTED | 7.5µH MAX   |
| LONGITUDINAL BALANCE 30kHz – 1.1MHz     | 50dB MIN    |
| INSERTION LOSS @100kHz                  | <0.5dB      |
| ISOLATION VOLTAGE (MAX)                 | 1500 Vrms   |
| LOAD RESISTANCE                         | 100Ω        |
| DC RESISTANCE (1-4), (10-7)             | 2Ω MAX      |
| THD LINEARITY @30kHz                    | -80dB TYP   |
| PACKAGE                                 | SMT         |

### OUTLINE DIMENSIONS

#### SMT

#### THT



### SCHEMATIC

#### NOTES:

1. Dimensions are shown in inches/millimeters  
Unless otherwise specified, tolerance = Inches: ±0.010  
= Millimeters: ±0,25
2. For non-RoHS parts replace SM prefix with 42- (e.g. SM531-1 becomes 42-531-1)
3. Terminal finish is compliant to RoHS requirements. Solder in accordance with J-STD-020C.