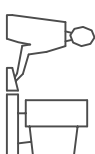




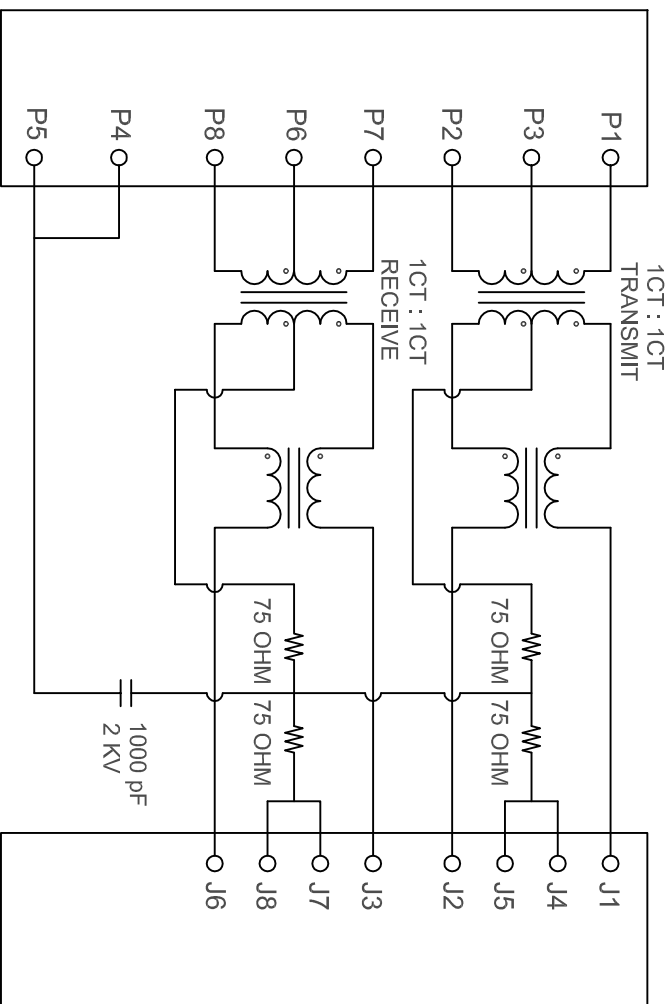
**ELECTRICAL CIRCUIT:**

THIS IS A C.A.D. GENERATED DRAWING  
DO NOT MAKE MANUAL REVISIONS TO MASTER.



CONNECTOR SOLDER SIDE

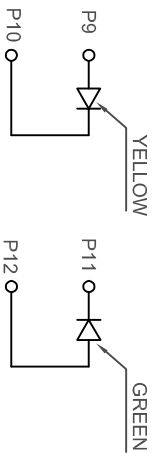
RJ45 CONTACT SIDE



TEST NOTES:(25±5°C)

1. TURN RATIO:(100KHZ,0.1V):  
PINS:(P1-P2):(J1-J2)=1:1±3%  
PINS:(P7-P8):(J3-J6)=1:1±3%
- 2.LX:(100KHZ,100mV,8mA, DC Bias)  
PINS:(P1,P2),(P7,P8)=350uH MINIMUM
- 3.DCR: PINS:(J1-J2),(J3-J6)= 1.2 OHMS MAXIMUM
- 4.DIELECTRIC WITHSTAND:  
PINS:(P1-P2):(J1-J2)=1500VAC OR 2250VDC  
PINS:(P7-P8):(J3-J6)=1500VAC OR 2250VDC
- 5.INSERTION LOSS:  
-1.2dB MAXIMUM AT 0.1MHZ TO 100MHZ
- 6.RETURN LOSS:  
-18dB MINIMUM AT 1MHZ TO 30MHZ;  
-(18-20log(f/30))dB MINIMUM AT 30MHZ TO 60MHZ  
-12dB MINIMUM AT 60MHZ TO 80MHZ
- 7.GROSS TALK:  
-35dB TYPICAL AT 1MHZ TO 100MHZ
- 8.COMMON TO COMMON MODE REJECTION:  
-30dB TYPICAL AT 30MHZ TO 100MHZ
9. LEAKAGE INDUCTANCE:  
PINS:(P1-P2) WITH J1 AND J2 SHORT=0.35uH MAX AT 1MHZ  
PINS:(P7-P8) WITH J3 AND J6 SHORT=0.35uH MAX AT 1MHZ
10. INTERWINDING CAPACITANCE  
PINS:(P1-P2):(J1-J2)=35pF MAXIMUM AT 1MHZ  
PINS:(P7-P8):(J3-J6)=35pF MAXIMUM AT 1MHZ

**LED CIRCUIT:**



LED SPECIFICATIONS (WITH FORWARD CURRENT OF 20 mA)			
STANDARD LED	WAVELENGTH	FORWARD V (MAX)	TYP
GREEN	565 nm	2.4 V	2.2 V
YELLOW	590 nm	2.5 V	2.1 V

**RJ45 MAGNETIC JACK WITH LED, 8P, 8C  
SHIELDED, 10/100 Mbps FILTER**

ACAD REFERENCE NO.: A63-113-231N435

DRAWN: R.STA.MONICA DATE: NOV.19/2010

CHECKED: DATE:

PART NUMBER

SEE NOTE

SHEET 2 OF 2

DRAWING NUMBER

A63-112-231N435

ISSUE

2



THIS SERIES FULLY CONFORMS TO THE  
EUROPEAN UNION DIRECTIVES 2002/95/EC  
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TORONTO, ONTARIO  
CANADA

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