

PA900-44(Z) Data Sheet

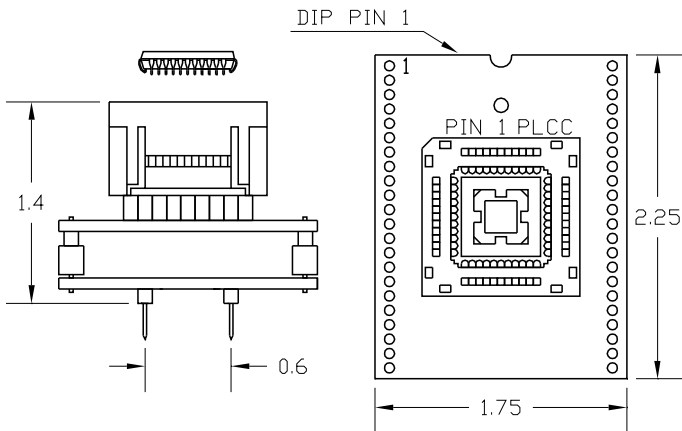
44 pin PLCC socket/40 pin DIP 0.6" plug

Supported Device/Footprints

Using this adapter, the Altera 900 & 910 in PLCC and CLCC package can be programmed on general purpose 40 pin DIP programmers.

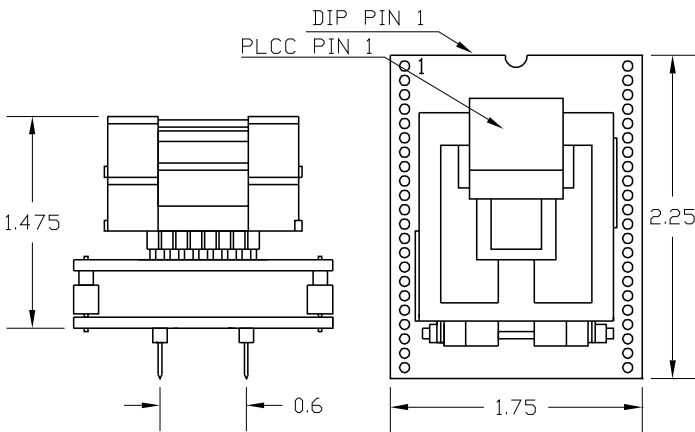
| Mfgr | Device | | Footprint | |
|--------|---------------|------------|---------------|------------|
| | Device | Package | Device | Plug |
| Altera | AL900 & AL910 | PLCC, CLCC | AL900 & AL910 | 40 pin DIP |

Adapter Dimensions



Press rim to open socket, Press device to close

PA900-44



PA900-44Z

Adapter Parts & Part Numbers

The following chart shows the various socket and board part numbers that make up these adapters.

| Adapter | Socket | Top Board | Bottom Board |
|-----------|------------------|--------------------|--------------|
| PA900-44 | 44-106 or 44-306 | 44PL2-1 or 44PL2-3 | AL900 |
| PA900-44Z | 44-400 | 44PL2-Z | AL900 |

Adapter Construction

The adapter is made up of 3 sub-assemblies. They assemble via connectors making the adapter modular. This way the sub-assemblies can be replaced when they wear out.

When disassembling the adapter take care not to bend the pins. When reassembling the adapter note the pin 1 indicators to align the parts correctly.

Test Socket

PLCC Auto-Eject test socket:

Yamaichi Part #: IC120-0444-106 LSC #: 44-106

Yamaichi Part #: IC120-0444-306 LSC #: 44-306

ZIF Lidded socket:

Yamaichi Part #: IC51-0444-400 LSC #: 44-400

44PL2-1, -3, -Z

Accepts the test socket and connects to the bottom board.

AL900

Performs the wiring shown in the Adapter Wiring section

Adapter Wiring

The following chart shows the connections from the PLCC device to the adapter's DIP plug.

| DEVICE | SIGNAL | PLUG | PLUG | SIGNAL | DEVICE |
|--------|--------|------|------|--------|--------|
| 1 | Vcc | 40 | 40 | Vcc | 44 |
| 2 | CLK1 | 1 | 39 | Input | 43 |
| 3 | Input | 2 | 38 | Input | 42 |
| 4 | Input | 3 | 37 | Input | 41 |
| 5 | Input | 4 | 36 | I/O | 40 |
| 6 | I/O | 5 | - | NC | 39 |
| 7 | I/O | 6 | 35 | I/O | 38 |
| 8 | I/O | 7 | 34 | I/O | 37 |
| 9 | I/O | 8 | 33 | I/O | 36 |
| 10 | I/O | 9 | 32 | I/O | 35 |
| 11 | I/O | 10 | 31 | I/O | 34 |
| 12 | I/O | 11 | 30 | I/O | 33 |
| 13 | I/O | 12 | 29 | I/O | 32 |
| 14 | I/O | 13 | 28 | I/O | 31 |
| 15 | I/O | 14 | 27 | I/O | 30 |
| 16 | I/O | 15 | 26 | I/O | 29 |
| 17 | NC | - | 25 | I/O | 28 |
| 18 | I/O | 16 | 24 | Input | 27 |
| 19 | Input | 17 | 23 | Input | 26 |
| 20 | Input | 18 | 22 | Input | 25 |
| 21 | Input | 19 | 21 | CLK2 | 24 |
| 22 | Gnd | 20 | 20 | Gnd | 23 |