

# PA51CC3-PDZ Data Sheet

52 pin PLCC socket/40 pin DIP 0.6" plug

## Supported Device/Footprints

Using one of these adapters, the Atmel AT89C51CC03 in its PLCC package can be programmed using the 40 pin DIP footprint specified by Atmel.

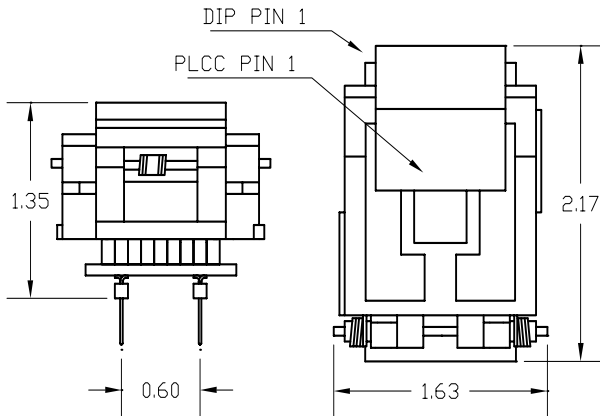
For this adapter to be useful, a programmer must offer specific support for this device and adapter combination.

**Atmel:** AT89C51CC03 PLCC52

**Footprint:** Atmel specified 40 DIP 0.6"

Device			Footprint	
Mfgr	Device	Package	Device	Plug
Atmel	AT89C51CC03	PLCC	AT89C51CC02	40 DIP

## Adapter Dimensions



PA51CC3-PDZ

## Adapter Construction

These adapters are made up of 2 sub-assemblies. They assemble via connectors making the adapter modular. This way the sub-assemblies can be replaced easily.

When disassembling the adapter take care not to bend the pins. When reassembling the adapter note the pin 1 indicators to align the parts correctly.

Adapter	Test Socket	Circuit Board
PA-51CC3-PDZ	52-411	CC03-52Z, Ver C

## Test Socket

LSC Socket	Style	Mfgr/Pn
52-411	Lidded ZIF	Yamaichi IC51-0524-411

## Adapter Wiring

The following chart shows the connections from the PLCC device to the adapter's DIP plug. This is Ver C wiring.

DEVICE	SIGNAL	PLUG	PLUG	SIGNAL	DEVICE
1	RST	9	20	VSS	52
2	VAGND	20	20	VSS	51
3	VAREF	-	40	VDD	50
4	P1.0	1	40	VDD	49
5	P1.1	2	19	XTAL1	48
6	P1.2	3	18	XTAL2	47
7	P1.3	4	30	ALE	46
8	P1.4	5	29	PSEN	45
9	P1.5	6	32	P0.7	44
10	P1.6	7	33	P0.6	43
11	P1.7	8	-	N/C	42
12	EA	31	34	P0.5	41
13	N/C	-	35	P0.4	40
14	P3.0	10	36	P0.3	39
15	P4.3	TP*	37	P0.2	38
16	P3.1	11	38	P0.1	37
17	P3.2	12	TP*	P4.4	36
18	P3.3	13	39	P0.0	35
19	P3.4	14	21	P2.0	34
20	P3.5	15	TP*	P4.2	33
21	P3.6	16	22	P2.1	32
22	P3.7	17	23	P2.2	31
23	P4.0	TP*	24	P2.3	30
24	P4.1	TP*	25	P2.4	29
25	P2.7	28	26	P2.5	28
26	P2.6	27	-	N/C	27

\* TP = Test Point provided on adapter board for Port 4 lines 0 through 4, P4[0:4]. There are no DIP plug connections to Port 4.