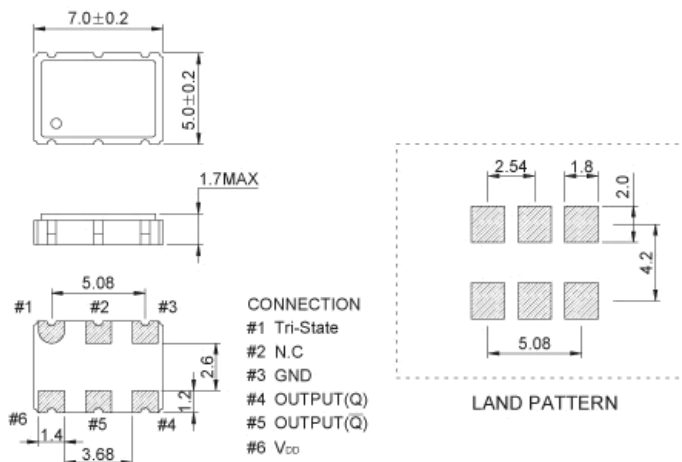


CF57L Series Clock Oscillator

5x7x1.6mm Ceramic
SMD 6 Pad
RoHS Compliant
LVDS Output
2.5 or 3.3VDC
9.500MHz to
200.000MHz
Tristate enabled

Mechanical Dimensions

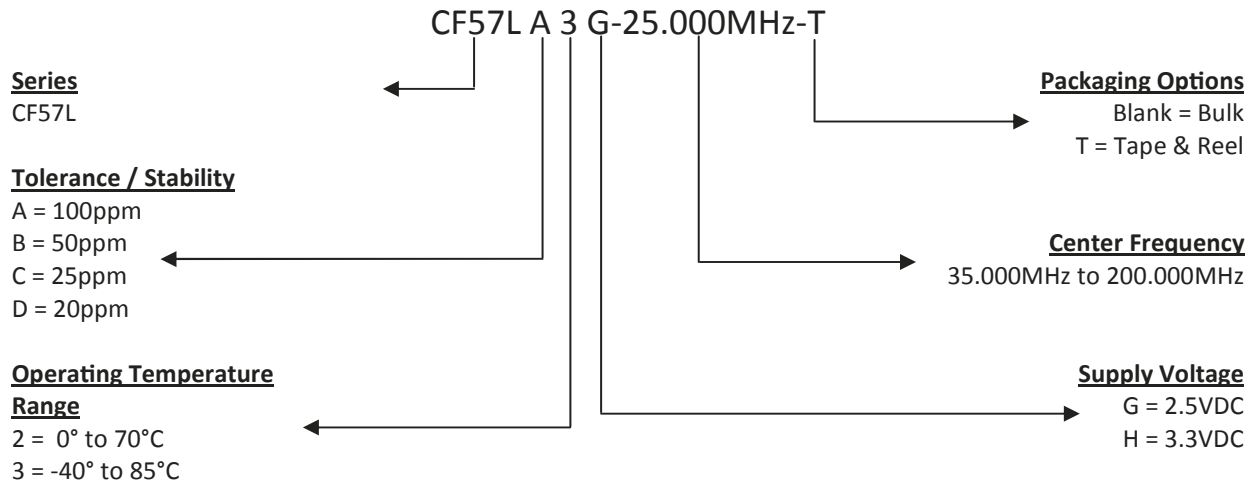
Dimensions are in millimeters. Dot indicates pin one location.



Electrical Specifications

Frequency Range		9.500MHz to 200.000MHz
Operating Temperature Range		0° to 70°C or -40° to 85°C
Storage Temperature Range		-55°C to 125°C
Supply Voltage	VDC ±5%	2.5 or 3.3V
Input Current	VDC = 2.5 & 3.3	45mA Typical, 55mA Max
Output Voltage	Logic High V _{DD} Logic Low V _{DD}	1.4V Min 1.1V Max
Output Differential Voltage		247 ~ 454mV, 350mV Typical
Offset Voltage		1.125 ~ 1.375V, 1.2V Typical
Output Enable High		0.7V _{DD} Min
Output Enable Low		0.3V _{DD} Min
Frequency Tolerance / Stability		20ppm, 25ppm, 50ppm, or 100ppm
Duty Cycle	50% of Waveform	50 ± 5%
Rise Fall Time		700pS Typical 1.5nS Max (20 ~ 80% of Waveform)
Phase Jitter	12KHz to 20MHz Offset Freq.	1pSecond Max
Startup Time		10mSeconds Max
Aging (At 25°C)		±3ppm per year
Pin 1 Tristate Input Voltage	No Connection VIH : ≥ 0.7VDD VIL : ≤ 0.3VDD	Enable Output Enable Output Disable Output: High Impedance

Part Numbering Guide



Part Marking Guide

Line #1	CFP CF57L
Line #2	XX.XXX M XX.XXX = Frequency (5 Digits Max + Decimal) M = Frequency Unit Of Measure (MHz)
Line #3	XXYYZZ XX = Crescent Manufacturing Identifier YY = Last Two Digits of Year ZZ = Week of Year

Solder Reflow

