



UT4101

Power MOSFET

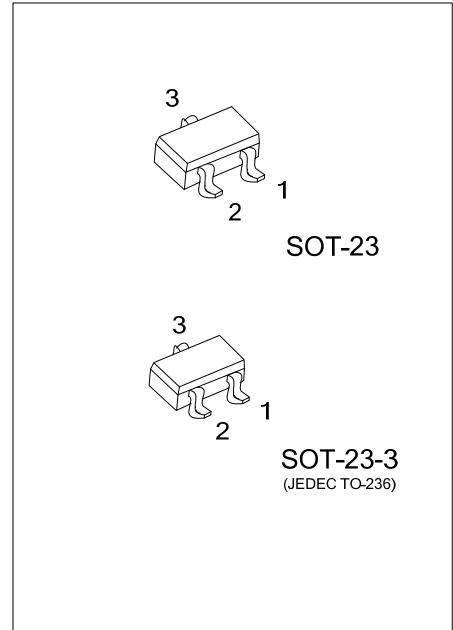
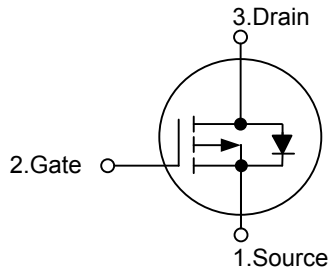
P-CHANNEL ENHANCEMENT MODE

DESCRIPTION

The UTC **UT4101** is P-channel enhancement mode Power MOSFET, designed with high density cell, with fast switching speed, low on-resistance, excellent thermal and electrical capabilities and operation with low gate voltages.

This device is suitable for use as a load switch or in PWM applications.

SYMBOL

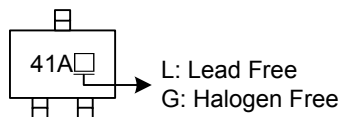


ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UT4101L-AE2-R	UT4101G-AE2-R	SOT-23-3	S	G	D	Tape Reel
UT4101L-AE3-R	UT4101G-AE3-R	SOT-23	S	G	D	Tape Reel

UT4101L-AE3-R	(1)Packing Type	(1) R: Tape Reel
	(2)Package Type	(2) AE2: SOT-23-3, AE3: SOT-23
	(3)Lead Free	(3) G: Halogen Free, L: Lead Free

MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_J = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	RATING	UNITS
Drain-Source Voltage	V_{DS}	-20	V
Gate-Source Voltage	V_{GS}	± 8.0	V
Continuous Drain Current (Note 3)	I_D	-2.4	A
Pulsed Drain Current (Note 1, 2)	I_{DM}	-7.5	A
Power Dissipation	P_D	1.25	W
Junction Temperature	T_J	+150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55 ~ +150	$^\circ\text{C}$

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.
Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

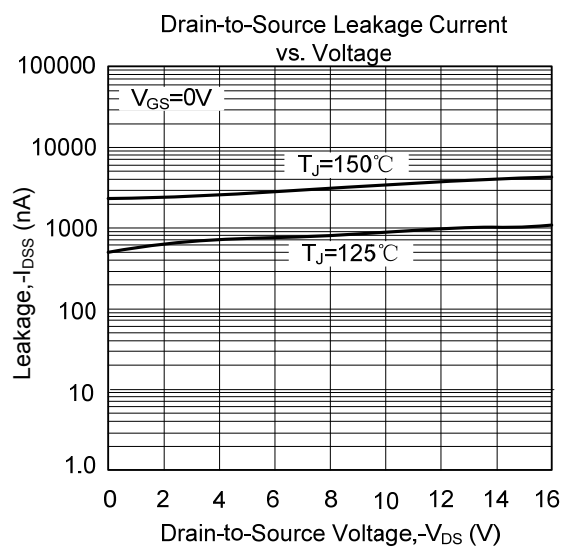
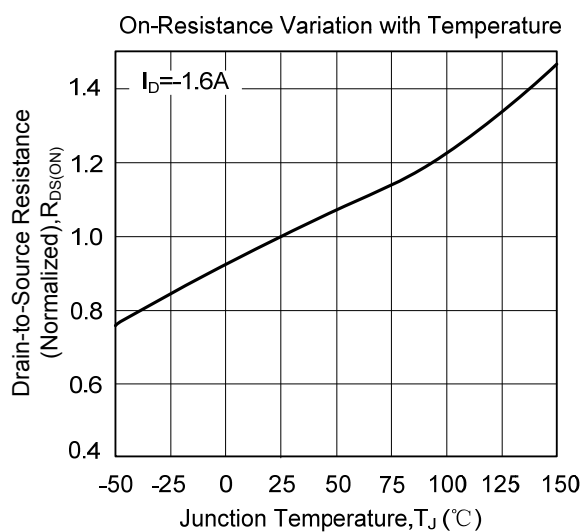
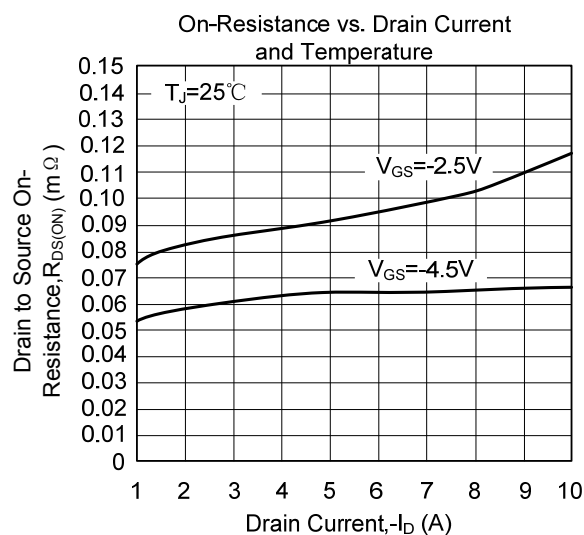
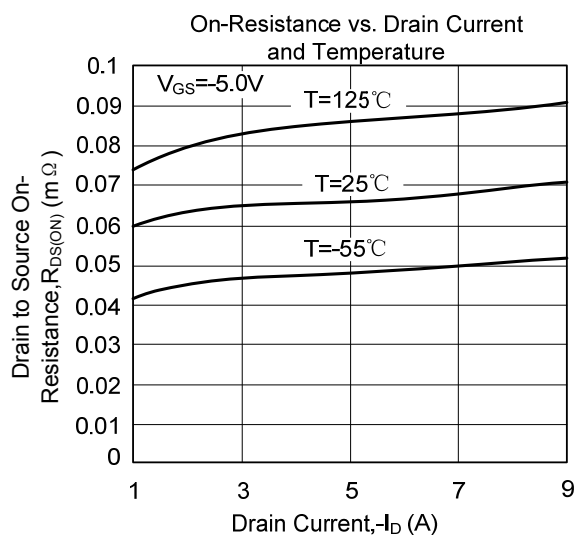
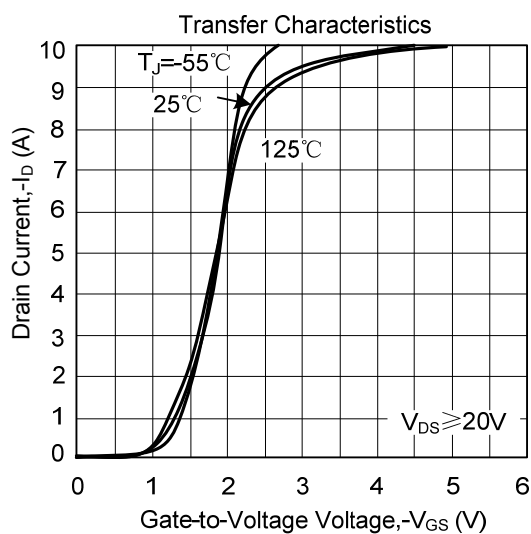
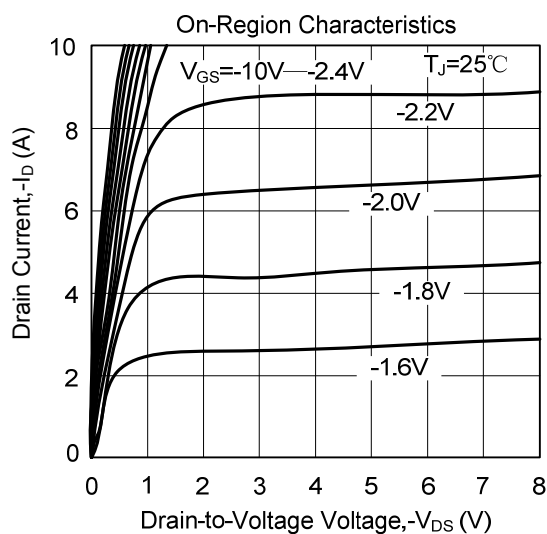
PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Junction-to-Ambient				100	$^\circ\text{C}/\text{W}$

■ ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, unless otherwise specified)

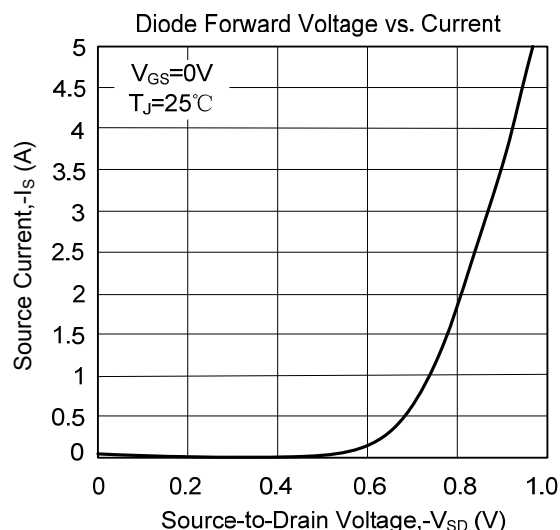
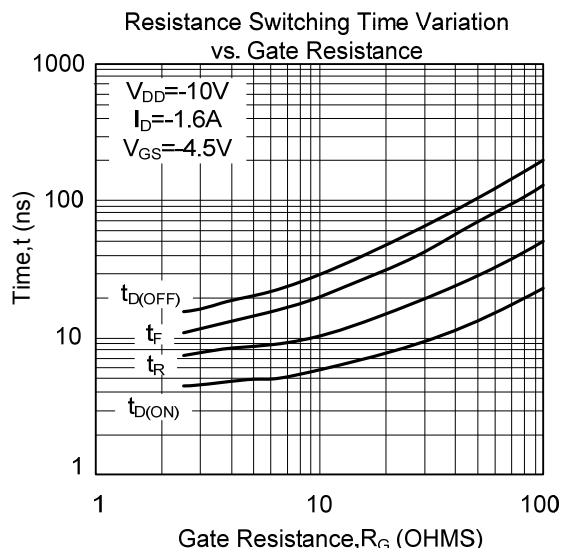
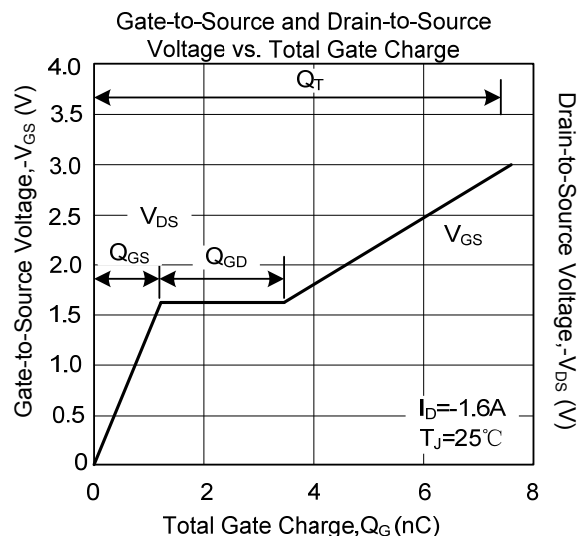
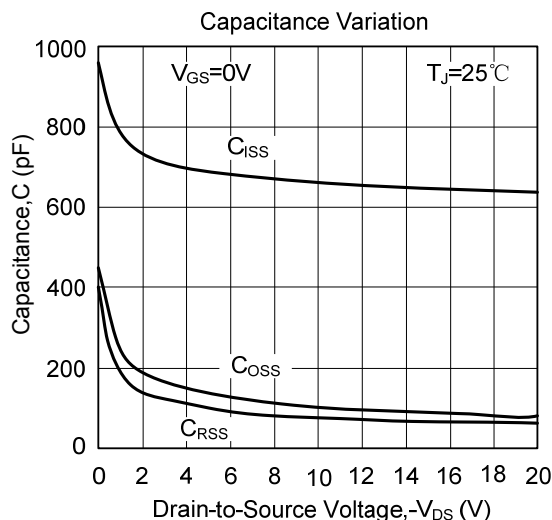
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0 V, I _D =-250μA	-20			V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =-16 V, V _{GS} =0 V			-1.0	μA
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0 V, V _{GS} = ±8.0 V			±100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _D =-250 μA	-0.40	-0.72	-1.5	V
Drain-Source On-State Resistance (Note 2)	R _{DS(ON)}	V _{GS} =-4.5 V, I _D =-1.6 A		70	85	mΩ
		V _{GS} =-2.5 V, I _D =-1.3 A		90	120	
		V _{GS} =-1.8 V, I _D =-0.9 A		112	210	
DYNAMIC PARAMETERS						
Input Capacitance	C _{ISS}	V _{DS} =-10 V, V _{GS} =0V, f=1MHz		675		pF
Output Capacitance	C _{OSS}			100		
Reverse Transfer Capacitance	C _{RSS}			75		
SWITCHING PARAMETERS						
Turn-ON Delay Time	t _{D(ON)}	V _{GS} =-4.5V,V _{DS} =-10V,R _G =6.0 Ω, I _D =-1.6A		7.5		ns
Turn-ON Rise Time	t _R			12.6		
Turn-OFF Delay Time	t _{D(OFF)}			30.2		
Turn-OFF Fall-Time	t _F			21.0		
Gate Charge	Q _G	V _{DS} =-10 V, V _{GS} =-4.5 V, I _D =-1.6A		7.5	8.5	nC
Gate Source Charge	Q _{GS}	V _{DS} =-10 V, I _D =-1.6A		1.2		
Gate Drain Charge	Q _{GD}			2.2		
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Drain-Source Diode Forward Voltage(Note2)	V _{SD}	V _{GS} =0V, I _S =-2.4 A		-0.82	-1.2	V
Maximum Continuous Drain-Source Diode Forward Current	I _S				-2.4	A
Reverse Recovery Time	t _{RR}	V _{GS} =0V, dI _{SD} /dt=100A/μs, I _S =-1.6A		12.8	15	ns
Reverse Recovery Charge	Q _{RR}			1008		nC

Note: 1. Pulse width limited by $T_{J(MAX)}$
2. Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
3. Surface mounted on 1 in² copper pad of FR4 board

■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS(Cont.)



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