



# 2SB798

## PN EPITAXIAL SILICON TRANSISTOR

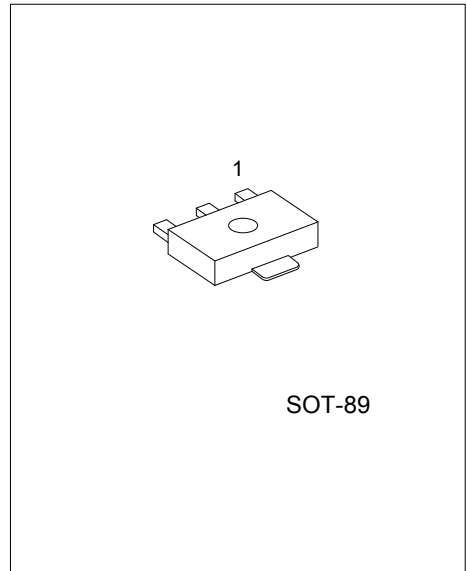
### POWER TRANSISTOR

#### DESCRIPTION

The UTC **2SB798** is designed for audio frequency power amplifier applications, especially in Hybrid Integrated Circuits.

#### FEATURES

- \* Low Collector Saturation Voltage:  
 $V_{CE(sat)} < -0.4V$  ( $I_C = -1.0A, I_B = -100mA$ )
- \* Excellent DC Current Gain Linearity :  
 $h_{FE} = 100$  Typ. ( $V_{CE} = -1.0V, I_C = -1.0A$ )



#### ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
2SB798L-X-AB3-R	2SB798G-X-AB3-R	SOT-89	B	C	E	Tape Reel

<p>2SB798L-X-AB3-R</p> <ul style="list-style-type: none"> <li>(1) Packing Type</li> <li>(2) Package Type</li> <li>(3) Rank</li> <li>(4) Lead Free</li> </ul>	<ul style="list-style-type: none"> <li>(1) R: Tape Reel</li> <li>(2) AB3: SOT-89</li> <li>(3) x: refer to Classification of <math>h_{FE1}</math></li> <li>(4) Halogen Free, L: Lead Free</li> </ul>
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### ■ ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C)

PARAMETER		SYMBOL	RATINGS	UNIT
Collector-Base Voltage		V <sub>CBO</sub>	-30	V
Collector-Emitter Voltage		V <sub>CEO</sub>	-25	V
Emitter-Base Voltage		V <sub>EBO</sub>	-5.0	V
Collector Current	DC	I <sub>C</sub>	-1.0	A
	Pulse(Note 1)		-1.5	A
Collector Dissipation (Note 2)		P <sub>C</sub>	2	W
Junction Temperature		T <sub>J</sub>	150	°C
Storage Temperature		T <sub>STG</sub>	-55 ~ +150	°C

Notes: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

1. PW ≤ 10ms, Duty Cycle ≤ 50%
2. When mounted on a ceramic substrate of 16cm<sup>2</sup>×0.7 mm.

### ■ ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C, unless otherwise specified)

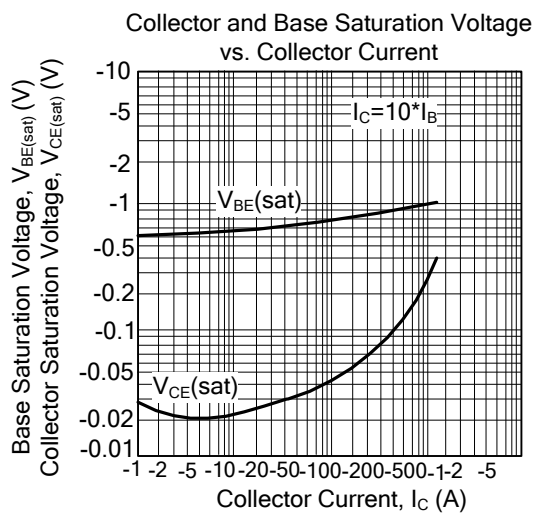
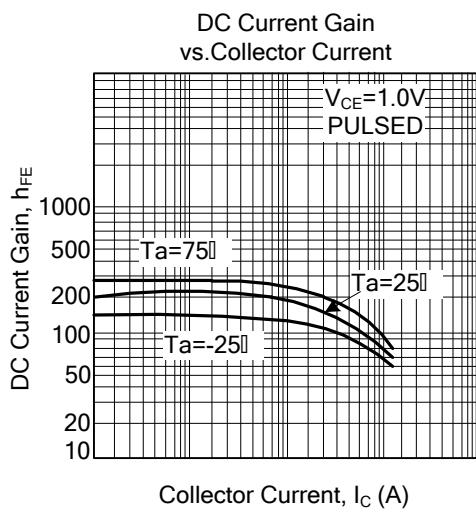
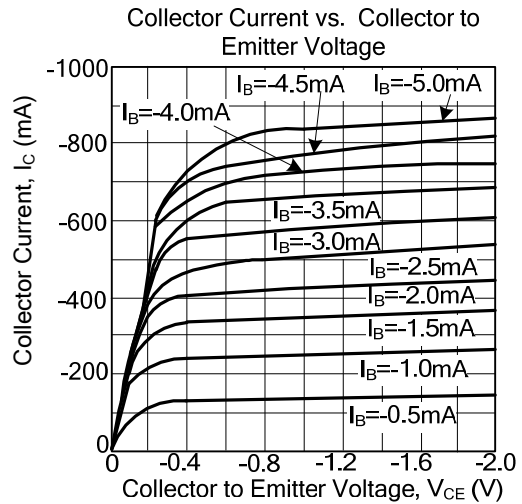
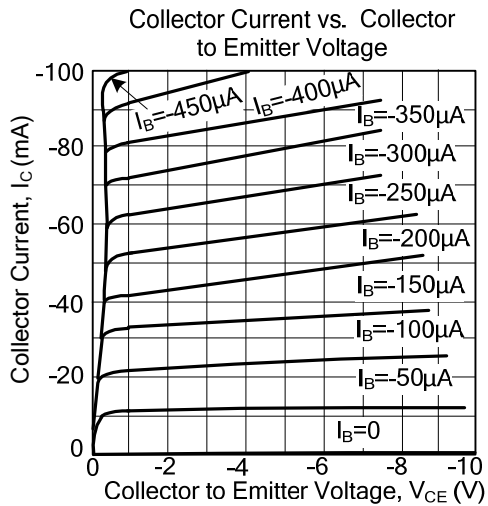
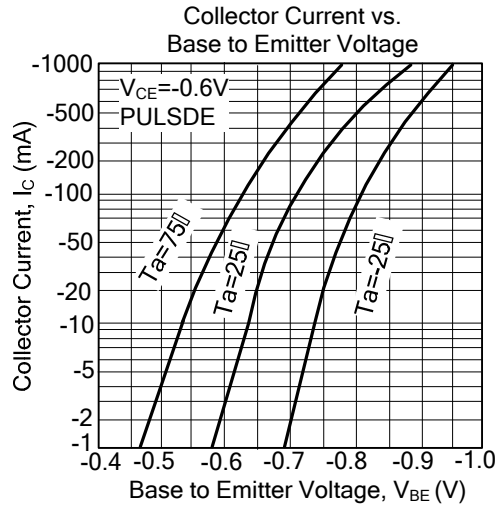
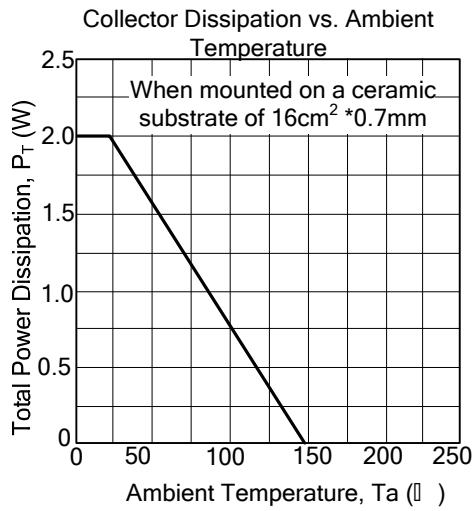
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector Cut-Off Current	I <sub>CBO</sub>	V <sub>CB</sub> = -30V, I <sub>E</sub> = 0			-100	nA
Emitter Cut-Off Current	I <sub>EBO</sub>	V <sub>EB</sub> = -5.0V, I <sub>C</sub> = 0			-100	nA
DC Current Gain	h <sub>FE1</sub>	V <sub>CE</sub> = -1.0V, I <sub>C</sub> = -100mA	90	200	400	
DC Current Gain	h <sub>FE2</sub>	V <sub>CE</sub> = -1.0V, I <sub>C</sub> = -1.0A	50	100		
Base to Emitter Voltage	V <sub>BE</sub>	V <sub>CE</sub> = -6.0V, I <sub>C</sub> = -10mA	-600	-640	-700	mV
Collector-Emitter Saturation Voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> = -1.0A, I <sub>B</sub> = -0.10A		-0.25	-0.40	V
Base-Emitter Saturation Voltage	V <sub>BE(sat)</sub>	I <sub>C</sub> = -1.0A, I <sub>B</sub> = -0.10A		-1.0	-1.2	V
Gain Bandwidth Product	f <sub>T</sub>	V <sub>CE</sub> = -6.0V, I <sub>E</sub> = 10mA		110		MHz
Output Capacitance	C <sub>ob</sub>	V <sub>CB</sub> = -6.0V, I <sub>E</sub> = 0, f=1MHz		36		pF

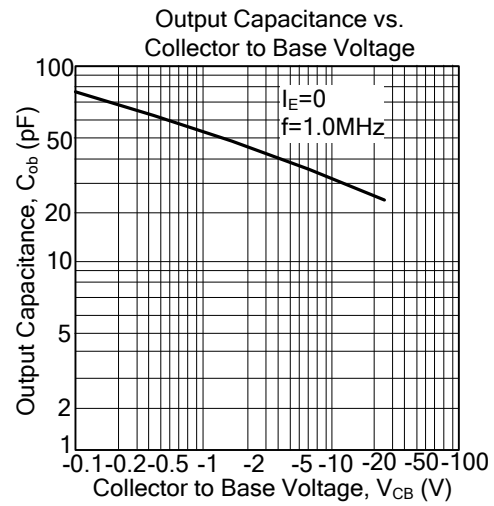
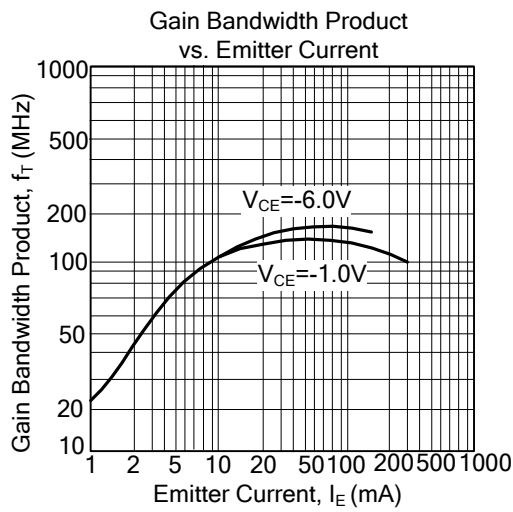
Note: 3. PW ≤ 350μs, Duty Cycle ≤ 2%

### ■ CLASSIFICATION OF h<sub>FE1</sub>

MARKING	DM	DL	DK
h <sub>FE1</sub>	90-180	135-270	200-400

## TYPICAL CHARACTERISTICS





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