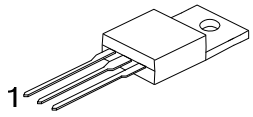
US104S/N

SCRS

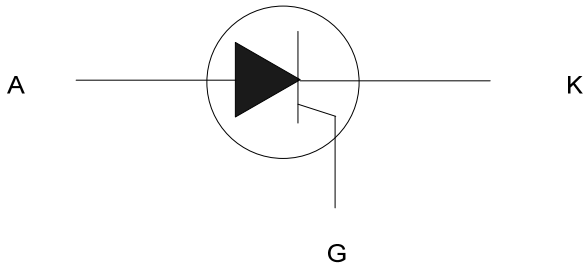


TO-220

■ DESCRIPTION

Thanks to highly sensitive triggering levels, the UTC **US104S** is suitable for all applications where the available gate current is limited, such as motor control for hand tools, kitchen aids, overvoltage crowbar protection for low power supplies, Available in through-hole or surface-mount packages, they provide an optimized performance in a limited space area.

■ SYMBOL



■ ORDERING INFORMATION

Order Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
US104SL-4-TA3-T	US104SG-4-TA3-T	TO-220	K	A	G	Tube
US104SL-6-TA3-T	US104SG-6-TA3-T	TO-220	K	A	G	Tube
US104SL-8-TA3-T	US104SG-8-TA3-T	TO-220	K	A	G	Tube
US104NL-4-TA3-T	US104NG-4-TA3-T	TO-220	K	A	G	Tube
US104NL-6-TA3-T	US104NG-6-TA3-T	TO-220	K	A	G	Tube
US104NL-8-TA3-T	US104NG-8-TA3-T	TO-220	K	A	G	Tube

Note: Pin Assignment: K: Cathode G: Gate A: Anode

US104SL-4-TA3-T (1)Packing Type (2)Package Type (3)Lead Free	(1) T: Tube (2) TA3: TO-220 (3) G: Halogen Free, L: Lead Free

■ ABSOLUTE MAXIMUM RATING

PARAMETER		SYMBOL	RATINGS	UNIT
Repetitive Peak Off-State Voltages And Repetitive Peak Reverse Voltage	US104S/N-4	V_{DRM}, V_{RRM}	400	V
	US104S/N-6		600	
	US104S/N-8		800	
RMS On-State Current (180° Conduction Angle) (T_C = 115℃)		$I_{T(RMS)}$	4	A
Average On-State Current (180° Conduction Angle) (T_C = 115℃)		$I_{T(AV)}$	2.5	A
Non Repetitive Surge Peak On-State Current (T_J= 25℃)	t_P=8.3ms	I_{TSM}	33	A
	t_P=10ms		30	
I^2t Value For Fusing (t_P = 10 ms, T_J = 25℃)		I^2t	4.5	A^2S
Critical Rate Of Rise Of On-State Current (I_G = 2 x I_{GT} , tr ≤ 100 n s, F= 60 Hz ,T_J = 125℃)		dI/dt	50	A/μs
Peak Gate Current (t_P=20μs, T_J = 125℃)		I_{GM}	1.2	A
Average Gate Power Dissipation (T_J = 125℃)		$PG_{(AV)}$	0.2	W
Storage Temperature		T_{STG}	-40 ~ +150	℃
Junction Temperature		T_J	-40 ~ +125	℃

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. The device is guaranteed to meet performance specification within 0℃~70℃ operating temperature range and assured by design from –20℃~85℃.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Case	θ_{JA}	60	K/W
Junction to Ambient	θ_{JC}	3.0	K/W

■ ELECTRICAL CHARACTERISTICS (T_J=25℃, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Gate Trigger Current	I_{GT}	V_D = 12 V, R_L =33Ω			200	μA
Gate Trigger Voltage	V_{GT}	V_D = 12 V, R_L=33Ω			0.8	V
Gate Non-Trigger Voltage	V_{GD}	V_D = V_{DRM}, R_L = 3.3kΩ R_{GK} = 220 T_J = 125℃	0.1			V
Reverse Gate Voltage	V_{RG}	I_{RG} = 10 μA	8			V
Holding Current	I_H	I_T = 50mA, R_{GK} = 1kΩ			5	mA
Latching Current	I_L	I_G = 1mA , R_{GK} = 1kΩ			6	mA
Circuit Rate Of Change Of off-State Voltage	dV/dt	V_D=67% V_{DRM} ,R_{GK} = 220Ω, T_J= 125℃	5			V/μs
On-State Voltage	V_{TM}	I_{TM} = 8 A, t_P = 380 μs, T_J = 25℃			1.6	V
Threshold Voltage	V_{t0}	T_J = 125℃			0.85	V
Dynamic Resistance	R_d	T_J = 125℃			90	mΩ
Off-State Leakage Current	I_{DRM}	V_{DRM} = V_{RRM}, R_{GK} = 220Ω, T_J= 25℃			5	μA
	I_{RRM}	V_{DRM} = V_{RRM}, R_{GK} = 220Ω, T_J = 125℃			1	mA

■ ELECTRICAL CHARACTERISTICS(Cont.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Gate Trigger Current	I_{GT}	V_D = 12 V, R_L =33Ω	2		15	mA
Gate Trigger Voltage	V_{GT}	V_D = 12 V, R_L=33Ω			1.3	V
Gate Non-Trigger Voltage	V_{GD}	V_D = V_{DRM}, R_L = 3.3 kΩ, T_J = 125℃	0.2			V
Holding Current	I_H	I_T = 100 mA Gate open			30	mA
Latching Current	I_L	I_G = 1.2 I_{GT}			60	mA
Circuit Rate Of Change Of off-State Voltage	dV/dt	V_D = 67 % V_{DRM} Gate open, T_J= 125℃	100			V/μs
On-State Voltage	V_{TM}	I_{TM} = 8A, t_P = 380 μs, T_J = 25℃			1.6	V
Threshold Voltage	V_{t0}	T_J = 125℃			0.85	V
Dynamic Resistance	R_d	T_J = 125℃			62	mΩ
Off-State Leakage Current	I_{DRM}	V_{DRM} = V_{RRM}, T_J= 25℃			5	μA
	I_{RRM}	V_{DRM} = V_{RRM}, T_J = 125℃			2	mA