

The Wiggler is a low cost interface used in the design, debug, and programming of microprocessor and microcontroller-based embedded systems. One side of the Wiggler interfaces to the parallel port of a host PC and the other side connects to an OCD (On-Chip Debug) port on the target system. As with all Macraigor interface devices, the Wiggler can simultaneously debug up to 255 devices on a single scan chain.

The Wiggler is the least expensive of Macraigor's line of OCD connection devices, yet it provides all the same functionality as our more expensive products. The Wiggler supports configurable JTAG clock rates between about 60 KHz and 380 KHz. It will perform best with those ports that are EPP (enhanced parallel port) compatible but will also work with most other parallel port modes. The Wiggler is compatible with Windows hosts.

List Price: \$150

Macraigor Macraigor Macraigor Macraigor Macraigor Systems Complete JTAG Debug Support

Supported Debuggers and Tools

The Wiggler is fully compatible with all of Macraigor's software tools, including our free OCD Commander debugger, free GNU toolkit distributions, OCD Flash Programmer application, and our production line test tools.

In addition, several commercial third-party debuggers interface to the Wiggler. A partial list of our debugger partners includes:

- · Accelerated Technology/ Mentor Graphics
- Allant (ARM)
- Arc (Metaware)
- Altium
- DDC-I
- · Green Hills Software
- IAR Systems
- Metrowerks
- Microcross
- Microsoft
- · Paradigm Systems
- Red Hat
- Viosoft

For a complete list of compatible debuggers, please visit our web site.

Theory of Operation

The Wiggler is a very simple device that primarily acts as a signal buffer between parallel port signals and the target JTAG/BDM signals. Depending on the target processor, power may be supplied by an external five volt power supply or directly from the parallel port and/or partially from the target itself.

Supported Processors

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AMCC:	PPC440
AMD:	SC520, Athlon, Duron, AU1000, AU1100, AU15x0
ARM:	7TDMI, 710T, 720T, 740T, 9TDMI, 920T, 922T, 940T, 946T, 1136EJ-S
Broadcom:	BCM1250, BCM7115
Freescale:	MC9328MX1, 56300, 56600, 56800, StarCore, 683xx,MPC603e, MPC8xx, MPC5xx, MPC5554, MPC740,MPC745, MPC750,MPC755, MPC8240, MPC8245, MPC8247/8248, MPC8250, MPC8255, MPC8260, MPC8264/65/66, MPC8270/71/72, MPC8280, MPC8540, MPC8541, MPC8555
IBM:	PPC603e, PPC740, PPC750
IDT:	RC323xx
Intel XScale® Technology:	PXA21x, PXA25x, PXA26x, PXA27x, IOP3xx, IXC1100, IXP42x, IXP46x, IXP24xx, IXP28xx, 80200, 80219, 8032x, 8033x
MIPS:	4Kc/p/m/e, 5Kc
NEC:	VR5432, VR5500
Net Silicon:	NetARM+10, NetARM+40, NetARM+50, NS7520, NS9750
PLX:	IOP480
Phillips:	PR1900
Toshiba:	TX49
Triscend:	E5, A7

For a more complete and up to date list of supported processors, please visit our web site.

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Macraigor Systems is a leading supplier of BDM/JTAG connection solutions for on-chip debugging of 32 and 64-bit embedded microprocessors. Macraigor Systems' solutions are designed for price-sensitive customers. These solutions include a suite of software tools that supports Windows 9x, NT, ME, XP, Linux and Solaris host systems. Macraigor Systems supports all major embedded microprocessor architectures, including AMD, ARM, ColdFire, CPU32 Series, PowerPC, MIPS and the XScale microarchitecture.