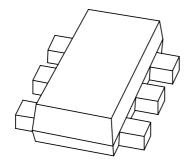
DISCRETE SEMICONDUCTORS

DATA SHEET



BAT74VSchottky barrier double diode

Product data sheet 2002 Sep 02



Schottky barrier double diode

BAT74V

FEATURES

- Low forward voltage
- Low capacitance
- Ultra small SMD plastic package
- Flat leads: excellent coplanarity and improved thermal behaviour.

APPLICATIONS

- · Ultra high-speed switching
- Voltage clamping
- · Line termination
- Inverse polarity protection.

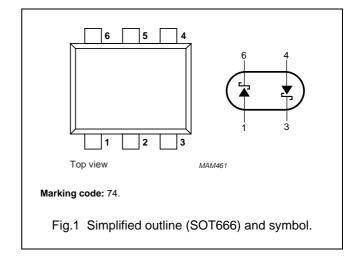
DESCRIPTION

Planar Schottky barrier double diode with an integrated guard ring for stress protection.

Two separate dies encapsulated in a SOT666 ultra small SMD plastic package.

PINNING

PIN	DESCRIPTION		
1	anode 1		
2	not connected		
3	cathode 2		
4	anode 2		
5	not connected		
6	cathode 1		



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_R	continuous reverse voltage		_	30	V
I _F	continuous forward current		_	200	mA
I _{FRM}	repetitive peak forward current	$t_p \le 1 \text{ s}; \ \delta \le 0.5$	_	300	mA
I _{FSM}	non-repetitive peak forward current	t _p < 10 ms		600	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	_	230	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	125	°C
T _{amb}	operating ambient temperature		-65	+125	°C

2002 Sep 02 2

Schottky barrier double diode

BAT74V

CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MAX.	UNIT
V _F	continuous forward voltage	I _F = 0.1 mA	240	mV
		I _F = 1 mA	320	mV
		I _F = 10 mA	400	mV
		I _F = 30 mA	500	mV
		I _F = 100 mA; note 1; see Fig.2	800	mV
I _R	reverse current	V _R = 25 V; note 1; see Fig.3	2	μΑ
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; see Fig.4	10	pF

Note

1. Pulse test: t_p = 300 μ s; δ = 0.02.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	note 1	416	K/W

Note

1. Refer to SOT666 standard mounting conditions.

Soldering

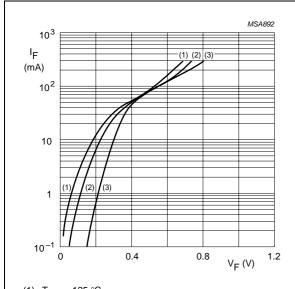
The only recommended soldering method is reflow soldering.

2002 Sep 02 3

Schottky barrier double diode

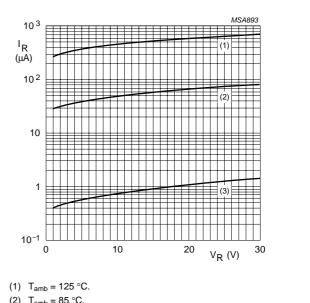
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GRAPHICAL DATA



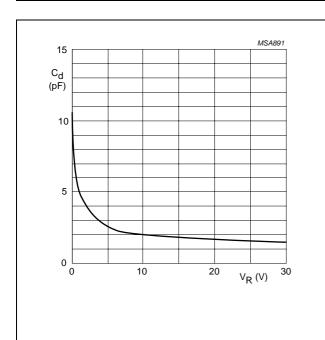
- (1) $T_{amb} = 125 \, ^{\circ}C$.
- (2) $T_{amb} = 85 \, ^{\circ}C$.
- (3) $T_{amb} = 25 \, ^{\circ}C$.

Fig.2 Forward current as a function of forward voltage; typical values.



- (2) $T_{amb} = 85 \, ^{\circ}C$.
- (3) $T_{amb} = 25 \, ^{\circ}C$.

Fig.3 Reverse current as a function of reverse voltage; typical values.



f = 1 MHz; $T_{amb} = 25 \,^{\circ}\text{C}$.

Fig.4 Diode capacitance as a function of reverse voltage; typical values.

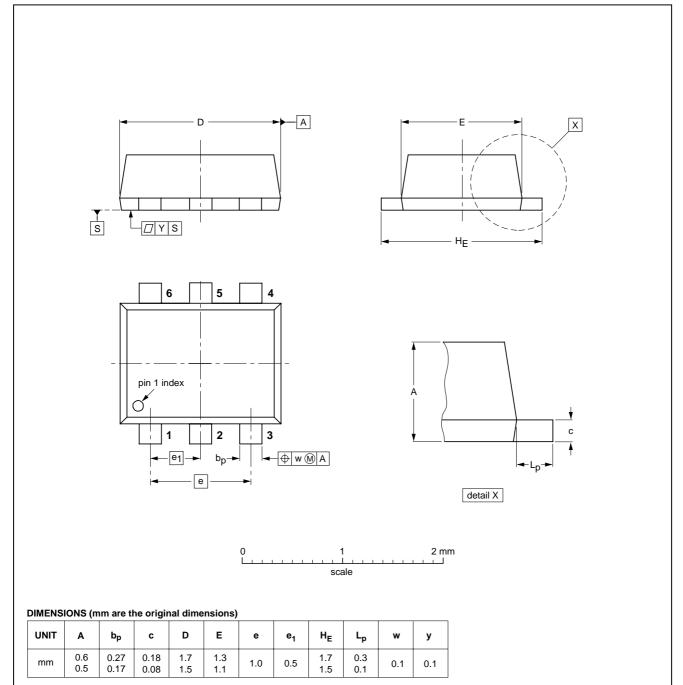
Schottky barrier double diode

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PACKAGE OUTLINE

Plastic surface mounted package; 6 leads

SOT666



OUTLINE		REFER	FERENCES EUROPEAN ISSUE DATE				ISSUE DATE	
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE		
SOT666						01-01-04 01-08-27		
				1	-			

2002 Sep 02 5

Schottky barrier double diode

BAT74V

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

- 1. Please consult the most recently issued document before initiating or completing a design.
- The product status of device(s) described in this document may have changed since this document was published
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