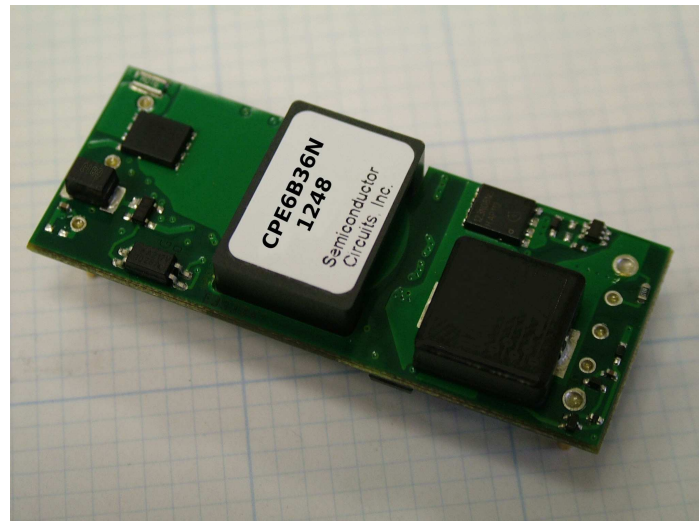


COOL POWER TECHNOLOGIES

Eighth-Brick Isolated DC/DC Converter

Features

- Ultra-wide input voltage range: 18 – 75Vin
- Output: 12V at 6 A, 72W max.
- ROHS Directive 2002/95/EC Compliant
- No minimum load required
- Low height - 0.500" (12.7mm) max.
- Basic Insulation
- Withstands 100 V input transients
- Fixed-frequency operation
- Industry standard 1/8th brick footprint
- Fully protection (OTP, OCP, OVP, UVLO – auto-restart)
- Remote ON/OFF - positive or negative enable logic options
- Remote sense
- Output voltage trim range: +10%/-20% (industry-standard trim equations)
- Weight: 0.79 oz [22.4 g]
- On-board input differential LC-filter
- Meets UL94, V-0 flammability rating
- Designed to meet UL/CSA60950-1, TUV per IEC/EN60950-1, 2nd edition
- Designed to meet Class B conducted emissions per FCC and EN55022 when used with external filter (see EMC Compliance section below.)



Description

The CPE6B36 “Cool Power Technologies” DC-DC converter is an open frame eighth-brick DC-DC converter that conforms to industry standard specifications. The converter operates over an input voltage range of 18 to 75 VDC, and provides a tightly regulated output voltage with an output current rating of 6 A. The output is fully isolated from the input and the converter meets Basic Insulation requirements. The standard feature set includes remote On/Off (positive or negative enable), input undervoltage lockout, output overvoltage protection, overcurrent and short circuit protections, output voltage trim, remote sense and overtemperature shutdown with hysteresis. The high efficiency of the CPE6B36 allows operation over a wide ambient temperature range with minimal derating (full rated power @ 60°C & 200LFM (1m/s) airflow from 22 – 65 Vin.)

ELECTRICAL SPECIFICATIONS

18–75Vin, 12V/6Aout

Conditions: $T_A = 25\text{ }^\circ\text{C}$, Airflow = 300 LFM, $V_{in} = 48\text{ VDC}$, $C_{in} = 33\text{ }\mu\text{F}$, unless otherwise specified.

Input Characteristics						
Parameter	Conditions	Min	Typ	Max	Unit	
Operating Input Voltage Range		18	48	75	VDC	
Input Under-Voltage Lock-out		Turn-on Threshold	17.2	17.6	18.0	VDC
		Turn-off Threshold	15.8	16.2	16.6	
Input Voltage Transient	100ms			100	VDC	
Maximum Input Current	$V_{IN} = 18\text{VDC}; I_{out} = 6\text{A}$			4.7	A	
Input Standby Current	Converter Disabled		2	5	mA	
Input No-Load Current	Converter Enabled		70	120	mA	
Short Circuit Input Current	RMS		30		mA	
Input Reflected Ripple Current	5Hz to 50MHz See Fig 1 for setup		10	20	$\text{mA}_{\text{PK-PK}}$	
Input Voltage Ripple Rejection	120Hz		50		dB	
Inrush Current	All	-	-	0.1	A^2/s	
Output Characteristics						
Parameter	Conditions	Min	Typ	Max	Unit	
Output Voltage Set point	Sense pins connected to output pins	11.82	12.0	12.18	VDC	
Output Current		0		6	A	
Output Current Limit Inception		7	8	11	A	
Peak Short-Circuit Current	10mΩ Short			14	A	
RMS Short-Circuit Current	10mΩ Short			1.4	A_{RMS}	
External Load Capacitance				2200	μF	
Output Ripple and Noise	20MHz Bandwidth 1 μF Ceramic + 10 μF Tantalum See Fig 2 for setup		50	100	$\text{mV}_{\text{PK-PK}}$	
Output Regulation						
Line:			± 10	± 20	mV	
Load:			± 10	± 20	mV	
Overall Output Regulation:	Over line, load & temp.	11.64		12.36	V	

ELECTRICAL SPECIFICATIONS (continued)

18–75Vin, 12V/6Aout

Conditions: $T_A = 25\text{ }^\circ\text{C}$, Airflow = 300 LFM, $V_{in} = 48\text{ VDC}$, $C_{in} = 33\text{ }\mu\text{F}$, unless otherwise specified.

Efficiency					
Parameter	Conditions	Min	Typ	Max	Unit
Full Load	$V_{in} = 24\text{V}_{in}$	91	92		%
	$V_{in} = 48\text{V}_{in}$	90.5	92		%
50% Load	$V_{in} = 24\text{V}_{in}$	91	92.5		%
	$V_{in} = 48\text{V}_{in}$	89	91		%
Dynamic Response					
Parameter	Conditions	Min	Typ	Max	Unit
Load Change 50%-75% or 25% to 50% of $I_{out\text{ Max}}$, $di/dt = 0.1\text{ A}/\mu\text{s}$			100	180	mV
Settling Time to 1% of V_{out}	$C_o = 1\text{ }\mu\text{F}$ ceramic + $10\text{ }\mu\text{F}$ tantalum		50		μs
Load Change 25%-75% of $I_{out\text{ Max}}$, $di/dt = 0.2\text{ A}/\mu\text{s}$	$C_o = 1\text{ }\mu\text{F}$ ceramic + $2000\text{ }\mu\text{F}$ Oscon		50	100	mV
Settling Time to 1% of V_{out}			100		μs
Isolation Specifications					
Isolation Capacitance			1000		pF
Isolation Resistance		10			$\text{M}\Omega$
Isolation Voltage – Input to Output				2250	V_{DC}
Reliability					
Per Telcordia SR-332, Issue 2: Method I, Case 3 ($I_o = 80\%$ of I_{o_max} , $T_A = 40^\circ\text{C}$, airflow = 200 lfm, 90% confidence)	MTFB	3,381,037			Hours
	FITs (failures in 10^9 hours)	296			$/10^9$ Hours



ELECTRICAL SPECIFICATIONS (continued)

18–75Vin, 12V/6Aout

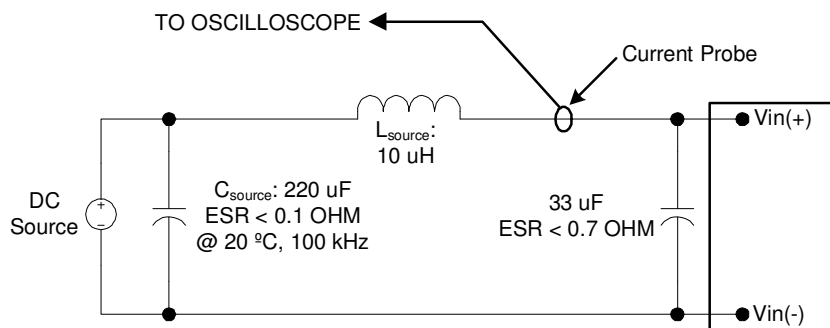
Conditions: Ta = 25 °C, Airflow = 300 LFM, Vin = 48 VDC, Cin=33 μF, unless otherwise specified.

Absolute Maximum Ratings					
Parameter	Conditions	Min	Typ	Max	Unit
Input Voltage	Continuous Operation	0		75	VDC
Operating Ambient Temperature		-40		+85	°C
Storage Temperature		-55		+125	°C
Feature Characteristics					
Parameter	Conditions	Min	Typ	Max	Unit
Switching Frequency			410		kHz
Output Voltage Trim Range*		-20		+10	%
Remote Sense Compensation*				+10	%
Output Over-voltage Protection	Non-latching	115	120	130	%
Over-temperature Protection	Avg. PCB temp, non-latching		125		°C
Peak Backdrive Output Current during startup into prebiased output	Sinking current from external voltage source equal to V _{OUT} – 0.6V and connected to the output via 1Ω resistor. C _{OUT} =220μF, Aluminum		400	500	mA
Backdrive Output Current in OFF state	Converter disabled		0	5	mA
Enable to Output Turn-ON Time	V _{OUT} = 0.9*V _{OUT_NOM}		20		ms
Output Enable ON/OFF					
Negative Enable					
Converter ON		-0.5		0.8	VDC
Converter OFF		2.4		20	VDC
Positive Enable					
Converter ON		2.4		20	VDC
Converter OFF		-0.5		0.8	VDC
Output Voltage Overshoot @ Startup			0	2	%Vo
Auto-Restart Period	(all protection features)		100		ms

* Note: 10% trim-up/remote sense limited to 20 - 72 Vin range



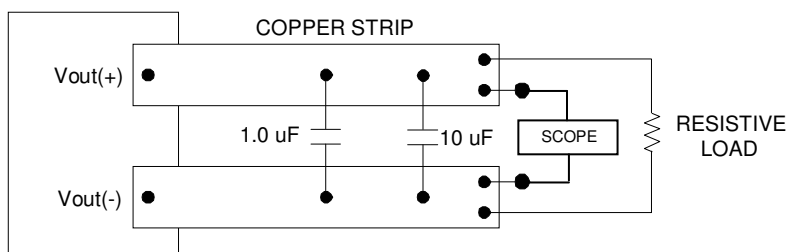
INPUT REFLECTED RIPPLE TEST SETUP:



Note: Measure input reflected-ripple current with a simulated source inductance (L_{test}) of 10 μ H. Capacitor C_s offsets possible source impedance.

Figure 1. Input Reflected-ripple Current Test Setup.

OUTPUT RIPPLE TEST SETUP:



Note: Use a 1 μ F X7R ceramic capacitor and a 10 μ F tantalum capacitor. Scope measurement should be made using a BNC socket. Position the load 3 in. [76mm] from module.

Figure 2. Peak-to-Peak Output Noise Measurement Test Setup.

CHARACTERISTIC CURVES:

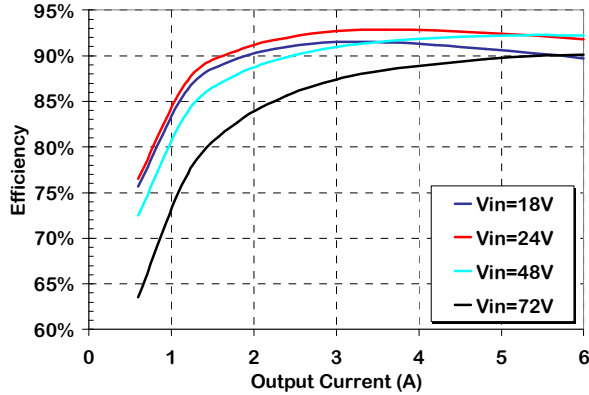


Figure 3. Efficiency vs Output Current, 300lfm airflow, 25°C ambient.

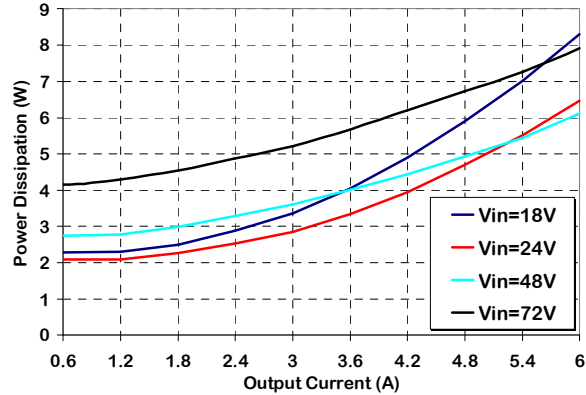


Figure 4. Power Dissipation vs. Load Current, 300lfm airflow, 25°C ambient.

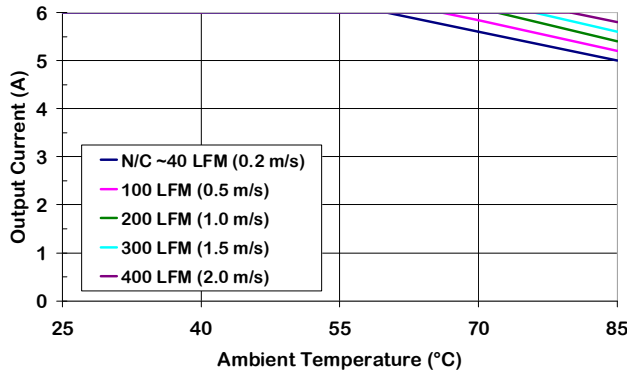


Figure 5. Output Current Derating vs Ambient Temperature & Airflow (converter mounted vertically with air flowing from pin 3 to pin 1, Vin = 48 V.)

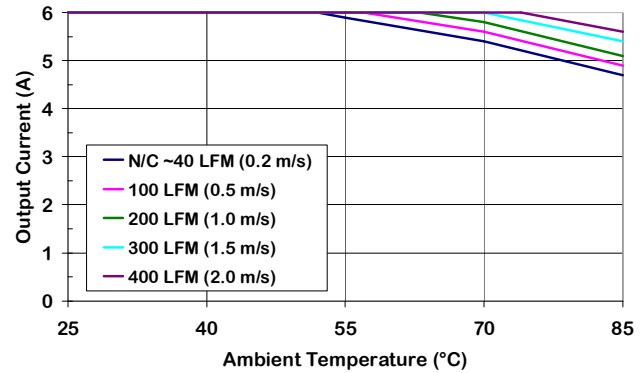


Figure 6. Output Current Derating vs Ambient Temperature & Airflow (converter mounted vertically with air flowing from pin 3 to pin 1, Vin = 24 V.)

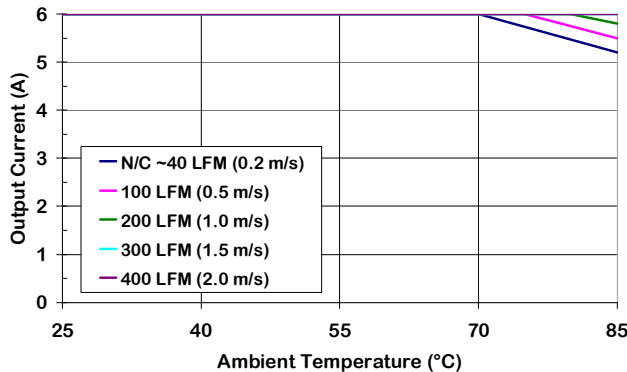


Figure 7. Output Current Derating vs Ambient Temperature & Airflow (converter mounted vertically with air flowing from pin 3 to pin 1, Vin = 36 V.)



Figure 8. Thermal Image of CPE6B36 (6A output, 70C Ambient, 200lfm airflow, Vin = 48V, airflow from pin 3 to pin 1, T_{max} = 118°C)

CHARACTERISTIC WAVEFORMS:

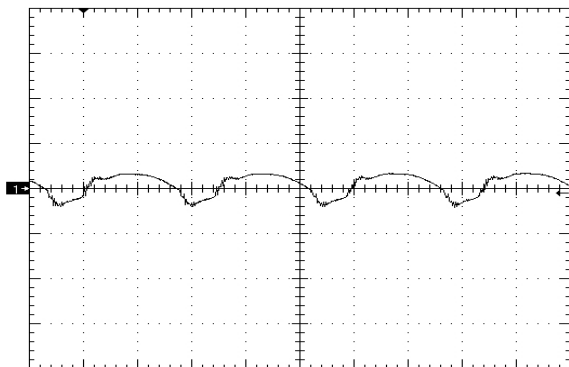


Figure 9. Output Voltage Ripple (50mV/div), time scale – 1uS/div. Vin=Vin_nom, full resistive

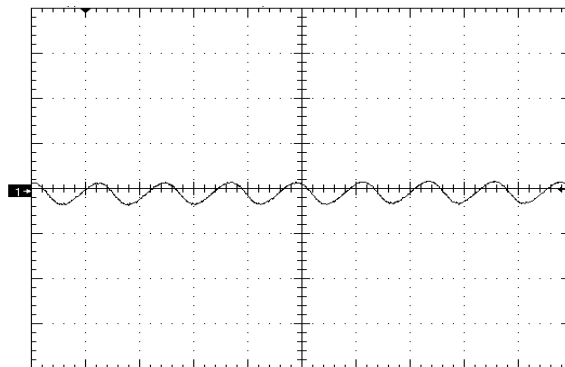


Figure 10. Input Reflected Ripple Current (10mA/div) time scale - 2uS/div. Vin=Vin_nom, full resistive

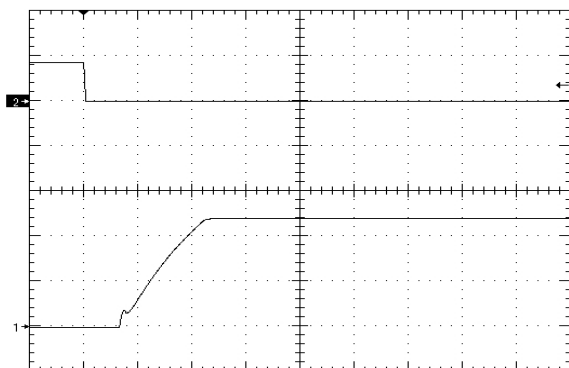


Figure 11. Startup Waveform via Enable Pin, time scale 10mS/div. Vin=Vin_nom, full resistive load (negative enable.)

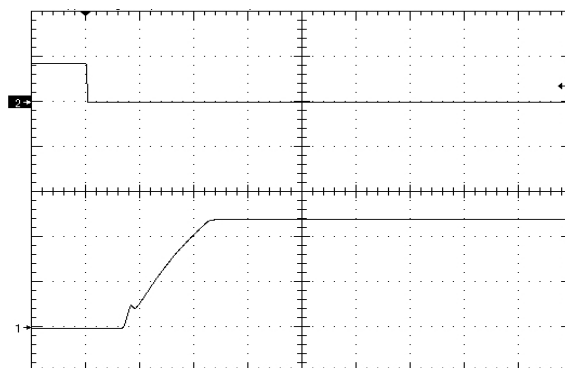


Figure 12. Startup Waveform via Enable Pin, time scale 4mS/div. Vin=Vin_nom, full resistive load + 2200uF (negative enable.)

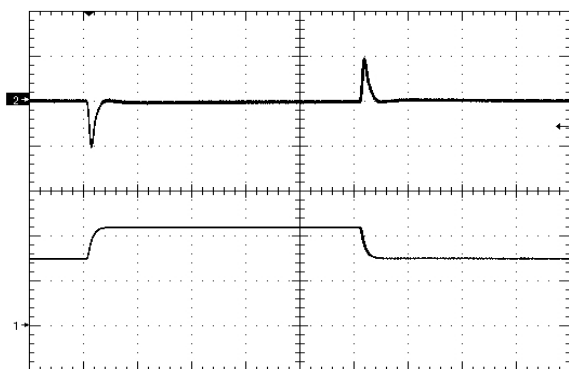


Figure 13. Load Transient Response (100mV/div), di/dt=0.1A/uS, 50% - 75% - 50% of full load, time scale: 200uS/div.

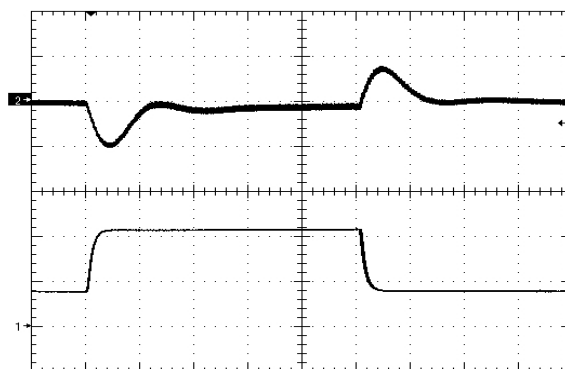


Figure 14. Load Transient Response (50mV/div), di/dt=0.2 A/uS, 25% - 75% - 25% of full load, 2000uF low ESR Oscon, time scale: 200uS/div.

OUTPUT VOLTAGE TRIM

Output voltage adjustment is accomplished by connecting an external resistor between the Trim Pin and either the +Vout (or +Sense) or -Vout (or -Sense) Pins.

TRIM UP EQUATION:

$$R_{trim_up} = \left[\frac{5.1 \times V_{o_nom} \times (100 + \Delta\%)}{1.225 \times \Delta\%} - \frac{510}{\Delta\%} - 10.2 \right] \times k\Omega$$

Where R_{trim_up} is the resistance value in k-ohms and $\Delta\%$ is the percent change in the output voltage.

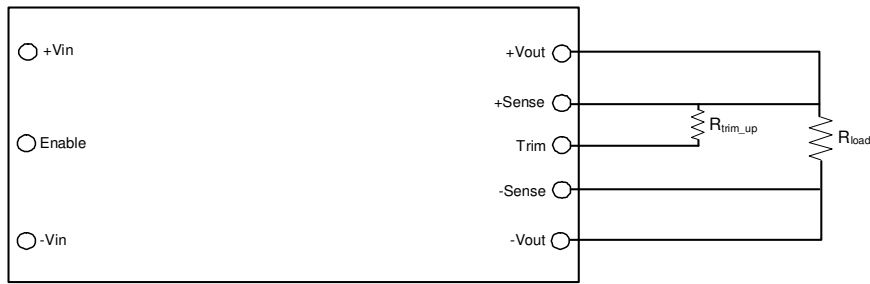


Figure 15. Trim UP circuit configuration

TRIM-DOWN EQUATION:

$$R_{trim_down} = \left(\frac{510}{\Delta\%} - 10.2 \right) \times k\Omega$$

Where R_{trim_down} is the resistance value in k ohms and $\Delta\%$ is the percent change in the output voltage.

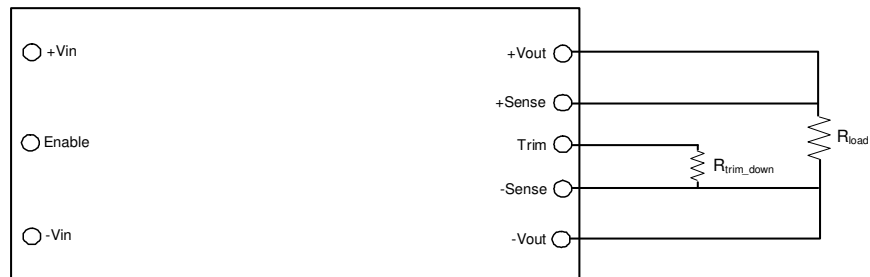


Figure 16. Trim DOWN circuit configuration



EMC COMPLIANCE:

To meet Class B compliance for EN55022 (CISPR 22) or FCC part 15 sub part j, the following input filter is required:

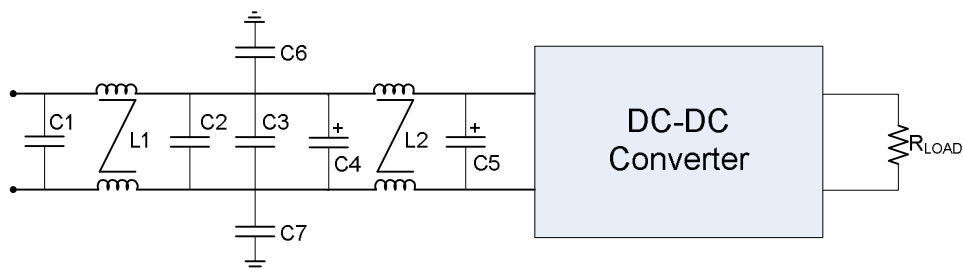


Figure 17. EMI Filter

L1, L2 =	0.77 mH Common Mode Inductor (Pulse P0422)
C1, C2, C3 =	2.2uF ceramic
C4 =	Not used
C5 =	100uF electrolytic
C6, C7 =	10nF (@2kV if output is ref. to gnd.)

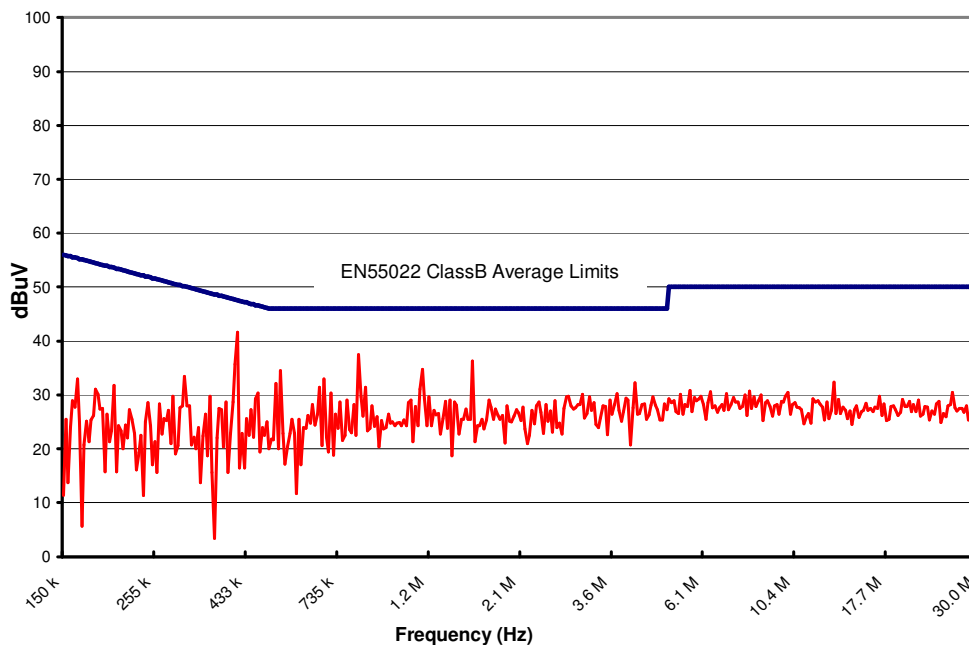
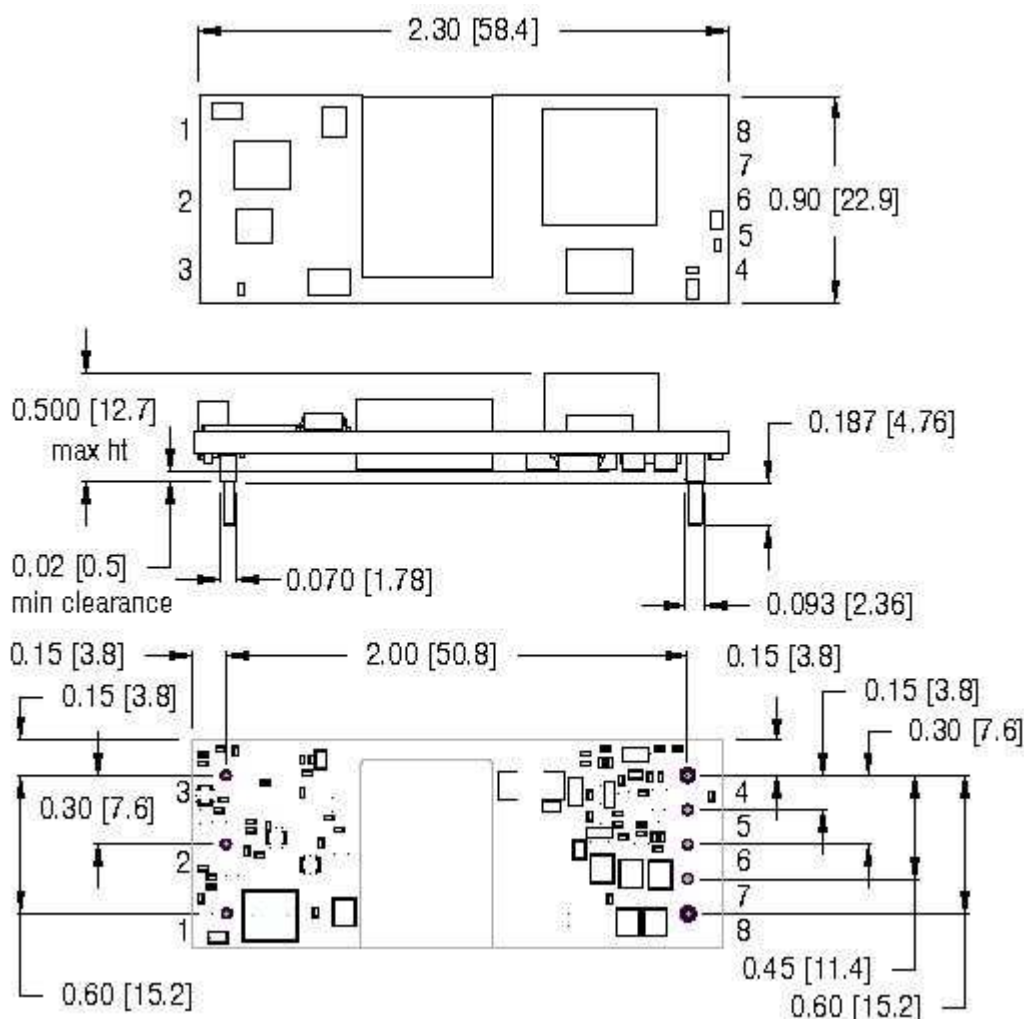


Figure 18. CPE6B36 Conducted Emissions using above specified input filter.
 Vin = 48V, Full Resistive Load

MODULE PIN ASSIGNMENT

PIN #	DESIGNATION	NOTES
1	V _{IN} (+)	1) All dimensions in inches [mm] Tolerances: .xx ± 0.02 [.x ± .5] .xxx ± 0.010 [.xx ± .25]
2	On/Off	
3	V _{IN} (-)	2) Input, on/off control and sense/trim pins are Ø 0.040" [1.02] with Ø 0.070" [1.77] standoff shoulders.
4	V _{OUT} (-)	
5	Sense (-)	3) Output pins are Ø 1.57 mm (0.062") with Ø 0.093" [2.36] shoulders (note, shoulder sits .008" above mounting surface)
6	Trim	
7	Sense (+)	4) All pins are gold plated with nickel under plating. 5) Weight: 22.4 g (0.79 oz.) 6) Workmanship: Meet or exceeds IPC-A-610 Class II
8	V _{OUT} (+)	

MECHANICAL OUTLINE



Ordering Information:					
Product Identifier	Output Current	Output Voltage	Input Voltage	Enable logic option	Additional features
CPE	6	B	36	N or P	- XX
“Cool Power Eighth”	6A	12	18 – 75V	N = Negative P = Positive	TBD

Rev 1.0, 18-Dec-12

