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FKP253

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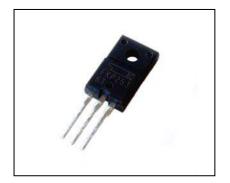
■Features

- •Low on-resistance
- •Low input capacitance
- Avalanche energy capability guaranteed

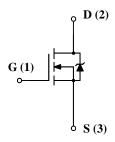
■ Applications

- •PDP driving
- •High speed switching

■Package---FM20 (TO220 Full Mold)



■Equivalent circuit



■ Absolute maximum ratings

(Ta=25°C)

		u 23 C)
Symbol	Rating	Unit
VDSS	250	V
VGSS	±30	V
ID	±20A	A
ID(pulse) *1	±80A	A
PD	40 (Tc=25°C)	W
EAS *2	160	mJ
IAS	20	A
Tch	150	°C
Tstg	-55 to 150	°C
	VDSS VGSS ID ID(pulse) *1 PD EAS *2 IAS Tch	Symbol Rating VDSS 250 VGSS ±30 ID ±20A ID(pulse) *1 ±80A PD 40 (Tc=25°C) EAS *2 160 IAS 20 Tch 150

^{*1} PW≤100µs, duty cycle≤1%

^{*2} VDD=20V, L=740 μ H, ILp=20A, unclamped, RG=50 Ω , See Fig.1



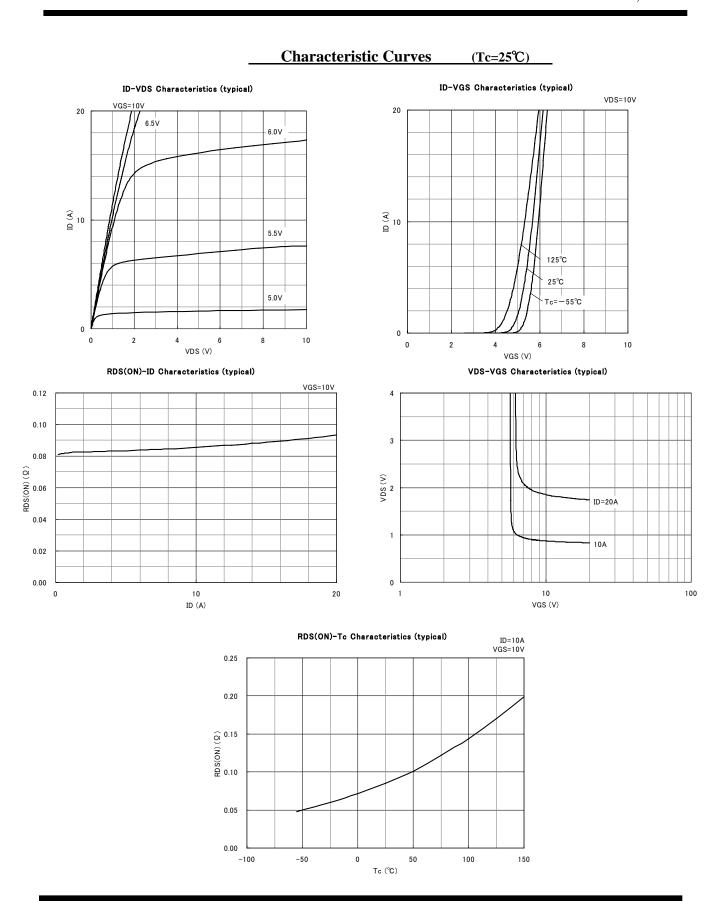
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Electrical characteristics

(Ta=25°C)

Domester	Cb - 1	Test Conditions	Limits			
Parameter	Symbol		MIN.	TYP.	MAX.	Unit
Drain to Source breakdown Voltage	V(BR)DSS	ID=100μA,VGS=0V	250			V
Gate to Source Leakage Current	IGSS	VGS=±30V			±100	nA
Drain to Source Leakage Current	IDSS	VDS=250V, VGS=0V			100	μΑ
Gate Threshold Voltage	VTH	VDS=10V, ID=1mA	3.0		4.5	V
Forward Transconductance	Re(Yfs)	VDS=10V, ID=10A	8	17		S
Static Drain to Source On-Resistance	RDS(on)	ID=10A, VGS=10V		86	95	mΩ
Input Capacitance	Ciss	VDS=25V VGS=0V f=1MHz		1600		
Output Capacitance	Coss			280		pF
Reverse Transfer Capacitance	Crss			50		
Turn-On Delay Time	td(on)	ID=10A, VDD \approx 120V RL=12 Ω , VGS=10V RG=5 Ω See Fig.2		30		
Rise Time	tr			60		
Turn-Off Delay Time	td(off)			80		ns
Fall Time	tf			45		
Source-Drain Diode Forward Voltage	VSD	ISD=20A,VGS=0V		1.0	1.5	V
Source-Drain Diode Reverse Recovery Time	trr	ISD=20A,VGS=0V di/dt=100A/ μ s		140		ns

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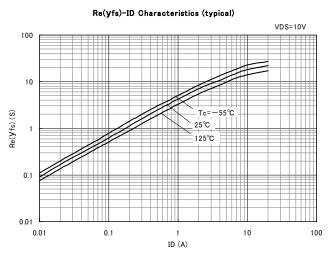
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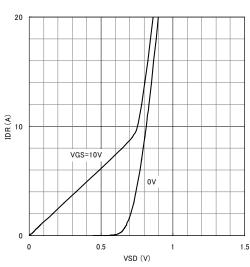
40

50

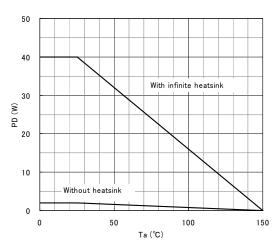


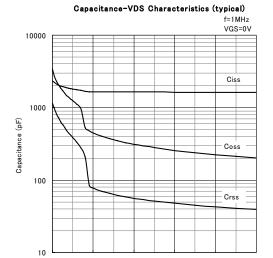


IDR-VSD Characteristics (typical)



PD-Ta Characteristics



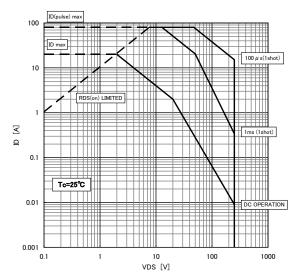


SAFE OPERATING AREA

VDS (V)

0

10



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Fig.1 Unclamped Inductive Test Method

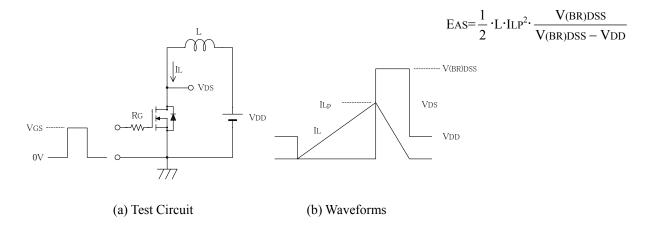
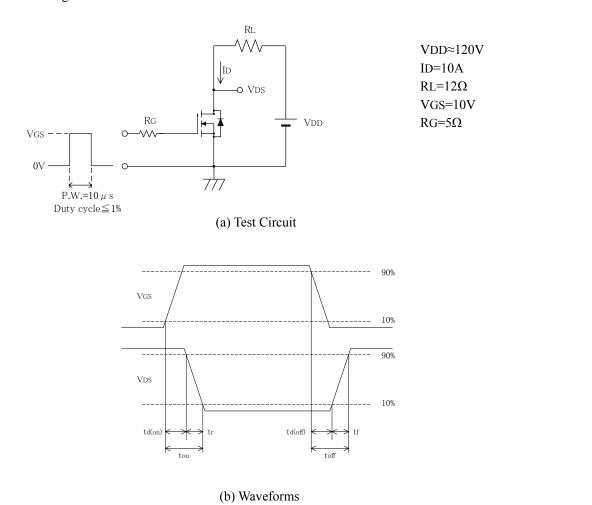


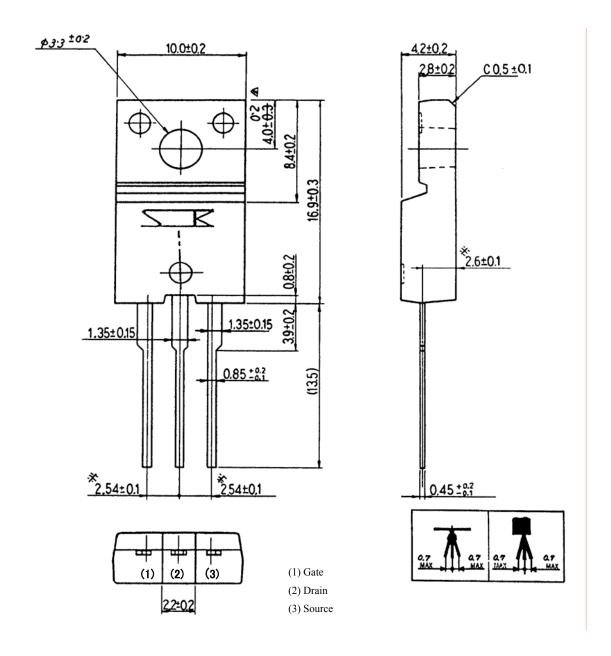
Fig.2 Switching Time Test Method



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External dimensions

FM20 (TO220 Full Mold)



Weight Approx. 2g

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