

Features and Benefits

- TO-220F-7L package
- Lead (Pb) free compliance
- The built-in startup circuit reduces the number of external components and lowers standby power consumption
- Multi-mode control allows high efficiency operation across the full range of loads
- Auto burst oscillation mode for standby mode, for improving low standby power at no load: input power
 30 mW at 100 VAC and < 50 mW at 230 VAC
- Bottom-skip mode minimizes switching loss at medium to low loads
- Internal MOSFET V_{DSS}(min) is 650 V
- Internal MOSFET $R_{DS(on)}(max)$ 1.9 Ω (STR-Y6753) or 1.4 Ω (STR-Y6754)
- Built-in soft start function reduces stress applied to the incorporated power MOSFET and peripheral components
- Step-on burst oscillation minimizes transformer audible noise
- Built-in leading edge blanking (LEB) function eliminates

Continued on the next page...

Package: 7-Pin TO-220F



Not to scale

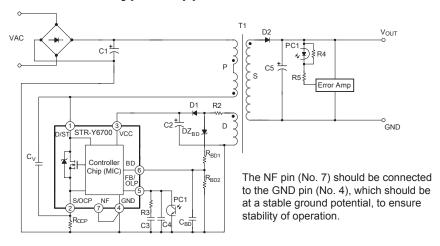
Description

The STR-Y6753 and STR-Y6754 each comprise a power MOSFET and a multi-functional monolithic integrated circuit (MIC) controller designed for controlling switch mode power supplies. The quasi-resonant mode of operation, coupled with the bottom-skip function, allows high efficiency and low noise at low to high operational levels, while burst oscillation mode ensures minimum power consumption at standby.

In order to sustain low power consumption under low load and in standby mode, the controller has built-in startup and standby circuits. This enables output power for the STR-Y6753 up to 60 W with universal input or 100 W with a 380 VDC input, and for the STR-Y6754 up to 67 W with universal input or 120 W with a 380 VDC input.

The compact 7-pin full mold package (TO220F-7L) reduces board space by requiring a minimum of external components, thus simplifying circuit design. This IC, including various protection functions, is an excellent choice for standardized, compact power supplies.

Typical Application



Off-Line Quasi-Resonant Switching Regulators

Features and Benefits (continued)

external filter components

- Built-in Bias Assist function enables stable startup operation
- V_{CC} operational range expanded
- Internal power MOSFET is avalanche energy guaranteed; twochip structure
- Protection functions
 - Overcurrent protection (OCP): pulse by pulse basis, low dependence on input voltage
 - Overload protection (OLP): latched shutoff*
 - Overvoltage protection (OVP): latched shutoff*
- Maximum on-time limitation
- Thermal shutdown protection (TSD): latched shutoff*

Selection Guide

Part Number	V _{DSS} (min) (V)	$R_{DS(on)}(max)$ (Ω)	Package	Packing
STR-Y6753	650	1.9 TO-220F		50 pieces per tube
STR-Y6754] 030	1.4	10-2201	30 pieces per tube

Absolute Maximum Ratings Unless specifically noted, T_A = 25°C and V_{CC} = 20 V

Characteristic	Symbol		Notes	Pins	Rating	Unit
Drain Current ¹		STR-Y6753	Cingle pulse	1 – 2	9.2	Α
Drain Current	I _{DPEAK}	STR-Y6754	Single pulse	1-2	11.0	Α
Mariana Cuitabia a Cumant		STR-Y6753	T = 20°C to 405°C	4 0	9.2	Α
Maximum Switching Current	I _{DMAX}	STR-Y6754	$T_A = -20^{\circ}\text{C to } 125^{\circ}\text{C}$	1 – 2	11.0	Α
Single Pulse Avalanche Energy ²	_	STR-Y6753	Single pulse, V _{DD} = 99 V, L= 20 mH, I _{LPEAK} = 2.9 A	1 – 2	99	mJ
Single Pulse Avaianche Energy ²	E _{AS}	STR-Y6754	Single pulse, V _{DD} = 99 V, L= 20 mH, I _{LPEAK} = 4.1 A	1-2	198	mJ
Input Voltage in Control Part (MIC)	V _{CC}			3 – 4	35	V
Startup (D/ST) Pin Voltage	V _{STARTUP}			1 – 4	-1.0 to V _{DSS}	V
OCP Pin Voltage	V _{OCP}			2 – 4	-2.0 to 6.0	V
FB Pin Voltage	V_{FB}			5 – 4	-0.3 to 7.0	V
FB Pin Sink Current ³	I _{FB}			5 – 4	10.0	mA
BD Pin Voltage	V _{BD}			6 – 4	-6.0 to 6.0	V
		STR-Y6753	With an infinite bacteinly	1 – 2	20.2	W
Power Dissipation in MOSFET4	P _{D1}	STR-Y6754	With an infinite heatsink	1-2	21.5	W
		Without heats	ink	1 – 2	1.8	W
Power Dissipation in Control Part (MIC)	P _{D2}			_	0.8	W
Internal Frame Temperature in Operation	T _F	Recommended internal frame temperature is $T_F = 105$ °C (max).		_	-20 to 115	°C
Operating Ambient Temperature	T _{OP}			_	-20 to 115	°C
Storage Temperature	T _{stg}			_	-40 to 125	°C
Channel Temperature	T _{ch}			_	150	°C

¹Refer to MOSFET Safe Operating Area Curve.

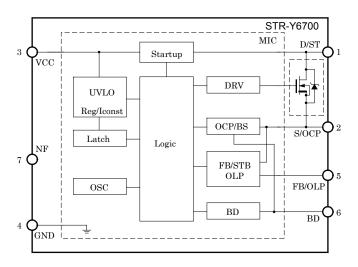
^{*}Latched shutoff means the output is kept in a shutoff mode for protection, until reset.

²Refer to MOSFET Avalanche Energy Derating Coefficient Curve.

³The polarity value for current specifies a sink as "+ ," and a source as "-," referencing the IC.

⁴Refer to MOSFET Temperature versus Power Dissipation Curve.

Functional Block Diagram



Pin List Table

Name	Number	Number Function			
1	D/ST	MOSFET drain and Startup circuit input			
2	S/OCP	MOSFET source and overcurrent detection signal input			
3	VCC	Control circuit power supply input			
4	GND	Ground			
5	FB/OLP	Constant Voltage Control signal input, Standby control, and overload detection signal input			
6	BD	Bottom Detection signal input, Input Compensation detection signal input			
7	NF	For stable operation, connect to GND pin, using the shortest possible path			

Off-Line Quasi-Resonant Switching Regulators

Electrical Characteristics of Control Part (MIC) Unless specifically noted, T_A = 25°C and V_{CC} = 20 V

Characteristic	Symbol	Test Conditions	Pins	Min.	Тур.	Max.	Unit
Power Supply Startup Operation	,		1	1			
Operation Start Voltage	V _{CC(ON)}		3 – 4	13.8	15.1	17.3	V
Operation Stop Voltage ¹	V _{CC(OFF)}		3 – 4	8.4	9.4	10.7	V
Circuit Current in Operation	I _{CC(ON)}		3 – 4	_	1.3	3.7	mA
Circuit Current in Non-Operation	I _{CC(OFF)}	V _{CC} = 13 V	3 – 4	_	4.5	50	μA
Startup Circuit Operation Voltage	V _{START(ON)}		1 – 4	42	57	72	V
Startup Current	I _{CC(STARTUP)}	V _{CC} = 13 V	3 – 4	-4.5	-3.1	-1.0	mA
Startup Current Supply Threshold Voltage ¹	V _{CC(BIAS)}		3 – 4	9.5	11.0	12.5	V
Operation Frequency	f _{OSC}		1 – 4	18.4	21.0	24.4	kHz
Soft Start Operation Duration	t _{SS}		1 – 4	_	6.05	_	ms
Normal Operation							
Bottom-Skip Operation Threshold Voltage 1	V _{OCP(BS1)}		2 – 4	0.487	0.572	0.665	V
Bottom-Skip Operation Threshold Voltage 2	V _{OCP(BS2)}		2 – 4	0.200	0.289	0.380	V
Quasi-Resonant Operation Threshold Voltage 1 ²	V _{BD(TH1)}		6 – 4	0.14	0.24	0.34	V
Quasi-Resonant Operation Threshold Voltage 2 ²	V _{BD(TH2)}		6 – 4	_	0.17	_	V
Maximum Feedback Current	I _{FB(MAX)}		5 – 4	-320	-205	-120	μΑ
Stand-by Operation							
Standby Operation Threshold Voltage	V _{FB(STBOP)}		5 – 4	0.45	0.80	1.15	V
Protected Operation							
Maximum On-Time	t _{ON(MAX)}		1 – 4	30.0	40.0	50.0	μs
Leading Edge Blanking Time	4	STR-Y6753	1-4	_	470	_	ns
Leading Edge Blanking Time	t _{ON(LEB)}	STR-Y6754	1 – 4	_	455	_	ns
Overcurrent Detection Threshold Voltage (Normal Operation)	V _{OCP(H)}	V _{BD} = 0 V	2 – 4	0.820	0.910	1.000	V
Overcurrent Detection Threshold Voltage (Input Compensation in Operation)	V _{OCP(L)}	V _{BD} = -3 V	2 – 4	0.560	0.660	0.760	V
Overcurrent Detection Threshold Voltage (Latched shutoff) ³	V _{OCP(La.OFF)}		2 – 4	1.65	1.83	2.01	V

Continued on the next page...

Off-Line Quasi-Resonant Switching Regulators

Electrical Characteristics of Control Part (MIC) (Continued) Unless specifically noted, $T_A = 25$ °C and $V_{CC} = 20$ V

Characteristic	Symbol	Test Conditions	Pins	Min.	Тур.	Max.	Unit
BD Pin Source Current	I _{BD(O)}		6 – 4	-250	-83	-30	μA
OLP Bias Current	I _{FB(OLP)}		5 – 4	-15	-10	-5	μA
OLP Threshold Voltage	V _{FB(OLP)}		5 – 4	5.50	5.96	6.40	V
OVP Threshold Voltage	V _{CC(OVP)}		3 – 4	28.5	31.5	34.0	V
FB Pin Maximum Voltage in Feedback Operation	V _{FB(MAX)}		5 – 4	3.70	4.05	4.40	V
Thermal Shut Down Temperature	T _{J(TSD)}		_	135	_	_	°C

Note: The polarity value for current specifies a sink as "+ ," and a source as "-," referencing the IC.

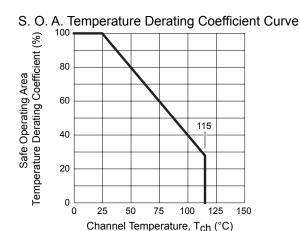
Electrical Characteristics of MOSFET Unless specifically noted, $T_A = 25$ °C and $V_{CC} = 20$ V

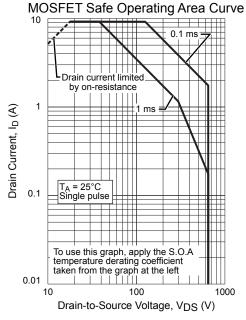
Characteristic	Symbol	Test Conditions		Pins	Min.	Тур.	Max.	Unit
Voltage Between Drain and Source	V _{DSS}			1 – 2	650	-	_	V
Drain Leakage Current	I _{DSS}			1 – 2	_	_	300	μA
On-Resistance	R _{DS(on)}	STR-Y6753		1 – 2	_	_	1.9	Ω
		STR-Y6754		1 – 2	_	_	1.4	Ω
Switching Time	t _f	STR-Y6753		1 – 2	_	_	250	ns
		STR-Y6754		1 – 2	-	_	300	ns
Thermal Resistance	R _{θch-F}	STR-Y6753	Between a channel of the MOSFET	_	_	2.7	3.1	°C/W
		STR-Y6754	and the internal leadframe	_	_	2.4	2.7	°C/W

 $^{^{1}} The \ relation \ of \ V_{CC(BIAS)} > V_{CC(OFF)} \ is \ maintained.$ $^{2} The \ relation \ of \ V_{BD(TH1)} > V_{BD(TH2)} \ is \ maintained \ in \ each \ product.$

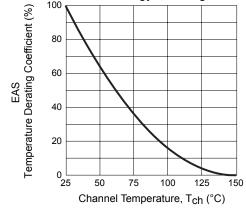
²The latch circuit means a circuit operated OVP, OLP, OCP (latch-off), or TSD.

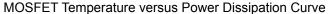
Characteristic Performance (STR-Y6753)

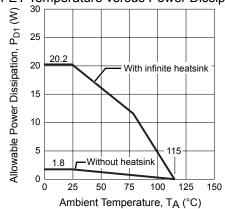


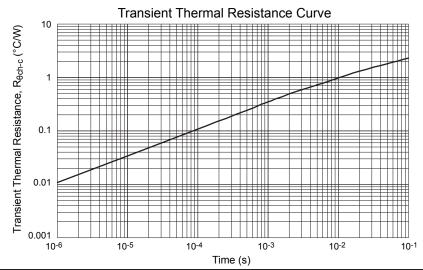


MOSFET Avalanche Energy Derating Coefficient Curve

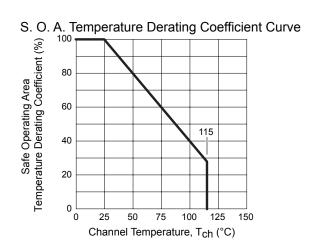


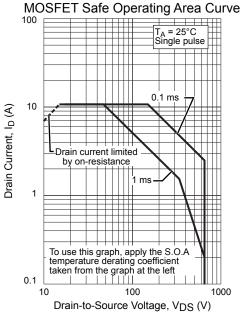




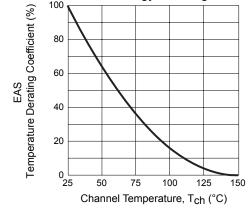


Characteristic Performance (STR-Y6754)

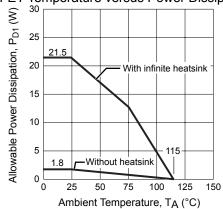


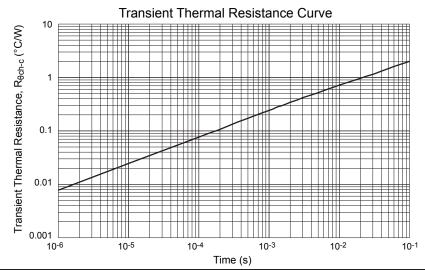


MOSFET Avalanche Energy Derating Coefficient Curve

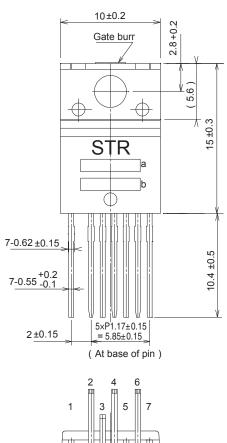


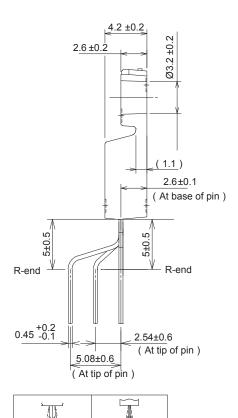
MOSFET Temperature versus Power Dissipation Curve

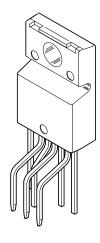




Package Outline Drawing









Unit: mm Package: TO-220F

(Sanken leadform #3051)
Leadframe material: Cu
Pin treatment: Solder dip
Weight: Approximately 1.45 g
"Gate Burr" shows area
where 0.3 mm (max) gate
burr may be present

a: Part # Y6753 [or Y6754]
b: Lot number

1 st letter: Last digit of year
2 nd letter: Month

Jan to September: Numeric
October: O
November: N
December: D
3 rd and 4 th letter: Date
01 to 31: Numeric
5 th letter: Internal use control number

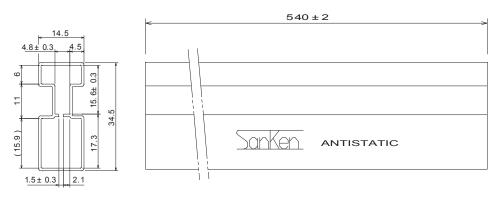
Side view

0.5

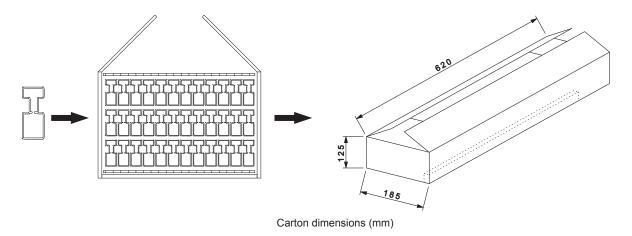
Front view

Pin treatment Pb-free. Device composition compliant with the RoHS directive.

Packing Specifications



Tube dimensions (mm) 50 pieces per tube



36 tubes per carton (maximum) 1800 pieces maximum per carton

Off-Line Quasi-Resonant Switching Regulators

Because reliability can be affected adversely by improper storage environments and handling methods, please observe the following cautions.

Cautions for Storage

- Ensure that storage conditions comply with the standard temperature (5°C to 35°C) and the standard relative humidity (around 40% to 75%); avoid storage locations that experience extreme changes in temperature or humidity.
- Avoid locations where dust or harmful gases are present and avoid direct sunlight.
- Reinspect for rust on leads and solderability of products that have been stored for a long time.

Cautions for Testing and Handling

When tests are carried out during inspection testing and other standard test periods, protect the products from power surges from the testing device, shorts between the product pins, and wrong connections.

Remarks About Using Silicone Grease with a Heatsink

- When silicone grease is used in mounting this product on a heatsink, it shall be applied evenly and thinly. If more silicone grease than required is applied, it may produce excess stress.
- Volatile-type silicone greases may crack after long periods of time, resulting in reduced heat radiation effect. Silicone grease with low consistency (hard grease) may cause cracks in the mold resin when screwing the product to a heatsink.
- Our recommended silicone greases for heat radiation purposes, which will not cause any adverse effect on the product life, are indicated below:

Type	Suppliers
G746	Shin-Etsu Chemical Co., Ltd.
YG6260	MOMENTIVE Performance Materials, Inc.
SC102	Dow Corning Toray Co., Ltd.

Heatsink Assembly

 Attachment torque should be in the range 0.588 to 0.785 Nom (6 to 8 kgfocm).

Soldering

- The leadframe temperature should never exceed T_F=105°C(max).
- When soldering the products, please be sure to minimize the working time, within the following limits:

260±5°C 10 s

350±5°C 3 s (solder iron)

 Soldering iron should be at a distance of at least 2.0 mm from the body of the products.

Electrostatic Discharge

- When handling the products, the operator must be grounded. Grounded wrist straps worn should have at least 1 M Ω of resistance from the operator to ground to prevent shock hazard, and it should be placed near the operator.
- Workbenches where the products are handled should be grounded and be provided with conductive table and floor mats.
- When using measuring equipment such as a curve tracer, the equipment should be grounded.
- When soldering the products, the head of soldering irons or the solder bath must be grounded in other to prevent leak voltages generated by them from being applied to the products.
- The products should always be stored and transported in Sanken shipping containers or conductive containers, or be wrapped in aluminum foil.

Off-Line Quasi-Resonant Switching Regulators

- The contents in this document are subject to changes, for improvement and other purposes, without notice. Make sure that this is the latest revision of the document before use.
- Application and operation examples described in this document are quoted for the sole purpose of reference for the use of the products herein and Sanken can assume
 no responsibility for any infringement of industrial property rights, intellectual property rights or any other rights of Sanken or any third party which may result from
 its use
- Although Sanken undertakes to enhance the quality and reliability of its products, the occurrence of failure and defect of semiconductor products at a certain rate is
 inevitable. Users of Sanken products are requested to take, at their own risk, preventative measures including safety design of the equipment or systems against any
 possible injury, death, fires or damages to the society due to device failure or malfunction.
- Sanken products listed in this document are designed and intended for the use as components in general purpose electronic equipment or apparatus (home appliances, office equipment, telecommunication equipment, measuring equipment, etc.).

 When considering the use of Sanken products in the applications where higher reliability is required (transportation equipment and its control systems, traffic signal control systems or equipment, fire/crime alarm systems, various safety devices, etc.), and whenever long life expectancy is required even in general purpose electronic equipment or apparatus, please contact your nearest Sanken sales representative to discuss, prior to the use of the products herein.

 The use of Sanken products without the written consent of Sanken in the applications where extremely high reliability is required (aerospace equipment, nuclear power control systems, life support systems, etc.) is strictly prohibited.
- In the case that you use our semiconductor devices or design your products by using our semiconductor devices, the reliability largely depends on the degree of derating to be made to the rated values. Derating may be interpreted as a case that an operation range is set by derating the load from each rated value or surge voltage or noise is considered for derating in order to assure or improve the reliability. In general, derating factors include electric stresses such as electric voltage, electric current, electric power etc., environmental stresses such as ambient temperature, humidity etc. and thermal stress caused due to self-heating of semiconductor devices. For these stresses, instantaneous values, maximum values and minimum values must be taken into consideration.

 In addition, it should be noted that since power devices or IC's including power devices have large self-heating value, the degree of derating of junction temperature (T_J) affects the reliability significantly.
- When using the products specified herein by either (i) combining other products or materials therewith or (ii) physically, chemically or otherwise processing or treating the products, please duly consider all possible risks that may result from all such uses in advance and proceed therewith at your own responsibility.
- Anti radioactive ray design is not considered for the products listed herein.
- · Sanken assumes no responsibility for any troubles, such as dropping products caused during transportation out of Sanken's distribution network.
- The contents in this document must not be transcribed or copied without Sanken's written consent.