

# Off-Line Quasi-Resonant Switching Regulator

#### **Features and Benefits**

- TO-220F-7L package
- Lead (Pb) free compliance
- The built-in startup circuit reduces the number of external components and lowers standby power consumption
- Multi-mode control allows high efficiency operation across the full range of loads
- Auto burst oscillation mode for standby mode, for improving low standby power at no load: input power
   30 mW at 100 VAC and < 50 mW at 230 VAC</li>
- Bottom-skip mode minimizes switching loss at medium to low loads
- Internal MOSFET V<sub>DSS</sub>(min) is 500 V
- Internal MOSFET R<sub>DS(on)</sub>(max) 0.8 Ω
- Built-in soft start function reduces stress applied to the incorporated power MOSFET and peripheral components
- Step-on burst oscillation minimizes transformer audible noise
- Built-in leading edge blanking (LEB) function eliminates external filter components

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Package: 7-Pin TO-220F



Not to scale

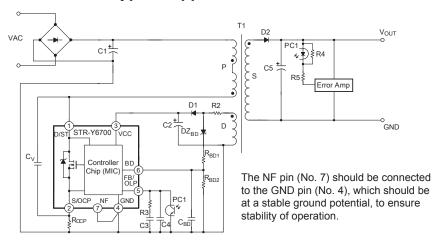
### **Description**

The STR-Y6735 comprises a power MOSFET and a multifunctional monolithic integrated circuit (MIC) controller designed for controlling switch mode power supplies. The quasi-resonant mode of operation, coupled with the bottomskip function, allows high efficiency and low noise at low to high operational levels, while burst oscillation mode ensures minimum power consumption at standby.

In order to sustain low power consumption under low load and in standby mode, the controller has built-in startup and standby circuits. This enables output power up to 120 W with a 100 VAC input.

The compact 7-pin full mold package (TO220F-7L) reduces board space by requiring a minimum of external components, thus simplifying circuit design. This IC, including various protection functions, is an excellent choice for standardized, compact power supplies.

### **Typical Application**



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### Features and Benefits (continued)

- Built-in Bias Assist function enables stable startup operation
- V<sub>CC</sub> operational range expanded
- Internal power MOSFET is avalanche energy guaranteed; twochip structure
- Protection functions
- Overcurrent protection (OCP): pulse by pulse basis, low dependence on input voltage
- Overload protection (OLP): latched shutoff\*
- Overvoltage protection (OVP): latched shutoff\*
- Maximum on-time limitation
- Thermal shutdown protection (TSD): latched shutoff\*

#### **Selection Guide**

Part Number	Package	Packing
STR-Y6735	TO-220F	50 pieces per tube

### **Absolute Maximum Ratings** Unless specifically noted, $T_A = 25$ °C and $V_{CC} = 20$ V

Characteristic	Symbol	Notes	Pins	Rating	Unit
Drain Current <sup>1</sup>	I <sub>DPEAK</sub>	Single pulse	1 – 2	14.6	Α
Maximum Switching Current	I <sub>DMAX</sub>	T <sub>A</sub> = −20°C to 125°C	1 – 2	14.6	Α
Single Pulse Avalanche Energy <sup>2</sup>	E <sub>AS</sub>	Single pulse, $V_{DD}$ = 99 V, L= 20 mH, $I_{LPEAK}$ = 3.5 A	1 – 2	152	mJ
Input Voltage in Control Part (MIC)	V <sub>CC</sub>		3 – 4	35	V
Startup (D/ST) Pin Voltage	V <sub>STARTUP</sub>		1 – 4	-1.0 to V <sub>DSS</sub>	V
OCP Pin Voltage	V <sub>OCP</sub>		2 – 4	-2.0 to 6.0	V
FB Pin Voltage	V <sub>FB</sub>		5 – 4	-0.3 to 7.0	V
FB Pin Sink Current <sup>3</sup>	I <sub>FB</sub>		5 – 4	10.0	mA
BD Pin Voltage	V <sub>BD</sub>		6 – 4	-6.0 to 6.0	V
Device Discinsting in MOCFETA		With an infinite heatsink	1 – 2	21.5	W
Power Dissipation in MOSFET <sup>4</sup>	P <sub>D1</sub>	Without heatsink	1 – 2	1.8	W
Power Dissipation in Control Part (MIC)	P <sub>D2</sub>		_	0.8	W
Internal Frame Temperature in Operation	T <sub>F</sub>	Recommended internal frame temperature is $T_F = 105^{\circ}C$ (max).	-	-20 to 115	°C
Operating Ambient Temperature	T <sub>OP</sub>		_	-20 to 115	°C
Storage Temperature	T <sub>stg</sub>		-	-40 to 125	°C
Channel Temperature	T <sub>ch</sub>		_	150	°C

<sup>&</sup>lt;sup>1</sup>Refer to MOSFET Safe Operating Area Curve.

All performance characteristics given are typical values for circuit or system baseline design only and are at the nominal operating voltage and an ambient temperature, T<sub>A</sub>, of 25°C, unless otherwise stated.

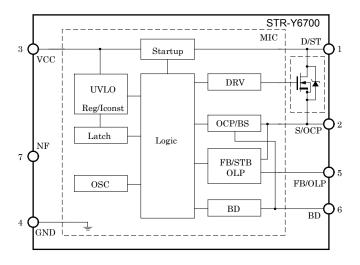
<sup>\*</sup>Latched shutoff means the output is kept in a shutoff mode for protection, until reset.

<sup>&</sup>lt;sup>2</sup>Refer to MOSFET Avalanche Energy Derating Coefficient Curve.

<sup>&</sup>lt;sup>3</sup>The polarity value for current specifies a sink as "+ ," and a source as "-," referencing the IC.

<sup>&</sup>lt;sup>4</sup>Refer to MOSFET Temperature versus Power Dissipation Curve.

### **Functional Block Diagram**



### Pin List Table

Name	Number	Function		
1	D/ST	MOSFET drain and Startup circuit input		
2	S/OCP	MOSFET source and overcurrent detection signal input		
3	VCC	Control circuit power supply input		
4	GND	Ground		
5	FB/OLP	Constant Voltage Control signal input, Standby control, and overload detection signal input		
6	BD	Bottom Detection signal input, Input Compensation detection signal input		
7	NF	For stable operation, connect to GND pin, using the shortest possible path		

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### Electrical Characteristics of Control Part (MIC) Unless specifically noted, T<sub>A</sub> is 25°C and V<sub>CC</sub> = 20 V

Characteristic	Symbol	Test Conditions	Pins	Min.	Тур.	Max.	Unit
Power Supply Startup Operation							
Operation Start Voltage	V <sub>CC(ON)</sub>		3 – 4	13.8	15.1	17.3	V
Operation Stop Voltage <sup>1</sup>	V <sub>CC(OFF)</sub>		3 – 4	8.4	9.4	10.7	V
Circuit Current in Operation	I <sub>CC(ON)</sub>		3 – 4	_	1.3	3.7	mA
Circuit Current in Non-Operation	I <sub>CC(OFF)</sub>	V <sub>CC</sub> = 13 V	3 – 4	_	4.5	50	μΑ
Startup Circuit Operation Voltage	V <sub>START(ON)</sub>		1 – 4	42	57	72	V
Startup Current	I <sub>CC(STARTUP)</sub>	V <sub>CC</sub> = 13 V	3 – 4	-4.5	-3.1	-1.0	mA
Startup Current Supply Threshold Voltage <sup>1</sup>	V <sub>CC(BIAS)</sub>		3 – 4	9.5	11.0	12.5	V
Operation Frequency	f <sub>OSC</sub>		1 – 4	18.4	21.0	24.4	kHz
Soft Start Operation Duration	t <sub>SS</sub>		1 – 4	_	6.05	_	ms
Normal Operation							
Bottom-Skip Operation Threshold Voltage 1	V <sub>OCP(BS1)</sub>		2 – 4	0.487	0.572	0.665	V
Bottom-Skip Operation Threshold Voltage 2	V <sub>OCP(BS2)</sub>		2 – 4	0.200	0.289	0.380	V
Quasi-Resonant Operation Threshold Voltage 1 <sup>2</sup>	V <sub>BD(TH1)</sub>		6 – 4	0.14	0.24	0.34	V
Quasi-Resonant Operation Threshold Voltage 2 <sup>2</sup>	V <sub>BD(TH2)</sub>		6 – 4	_	0.17	-	V
Maximum Feedback Current	I <sub>FB(MAX)</sub>		5 – 4	-320	-205	-120	μΑ
Stand-by Operation							
Standby Operation Threshold Voltage	V <sub>FB(STBOP)</sub>		5 – 4	0.45	0.80	1.15	V
Protected Operation							
Maximum On-Time	t <sub>ON(MAX)</sub>		1 – 4	30.0	40.0	50.0	μs
Leading Edge Blanking Time	t <sub>ON(LEB)</sub>		1 – 4	_	455	_	ns
Overcurrent Detection Threshold Voltage (Normal Operation)	V <sub>OCP(H)</sub>	V <sub>BD</sub> = 0 V	2 – 4	0.820	0.910	1.000	V
Overcurrent Detection Threshold Voltage (Input Compensation in Operation)	V <sub>OCP(L)</sub>	V <sub>BD</sub> = -3 V	2 – 4	0.560	0.660	0.760	V
Overcurrent Detection Threshold Voltage (Latched shutoff) <sup>3</sup>	V <sub>OCP(La.OFF)</sub>		2 – 4	1.65	1.83	2.01	V
BD Pin Source Current	I <sub>BD(O)</sub>		6 – 4	-250	-83	-30	μΑ
OLP Bias Current	I <sub>FB(OLP)</sub>		5 – 4	-15	-10	-5	μΑ
OLP Threshold Voltage	V <sub>FB(OLP)</sub>		5 – 4	5.50	5.96	6.40	V
OVP Threshold Voltage	V <sub>CC(OVP)</sub>		3 – 4	28.5	31.5	34.0	V
FB Pin Maximum Voltage in Feedback Operation	V <sub>FB(MAX)</sub>		5 – 4	3.70	4.05	4.40	V
Thermal Shut Down Temperature	T <sub>J(TSD)</sub>		_	135	_	_	°C

Note: The polarity value for current specifies a sink as "+ ," and a source as "-," referencing the IC.

 $<sup>^{1}\</sup>text{The relation of V}_{\text{CC(BIAS)}} > \text{V}_{\text{CC(OFF)}}$  is maintained.  $^{2}\text{The relation of V}_{\text{BD(TH1)}} > \text{V}_{\text{BD(TH2)}}$  is maintained in each product.

<sup>&</sup>lt;sup>2</sup>The latch circuit means a circuit operated OVP, OLP, OCP (latch-off), or TSD.

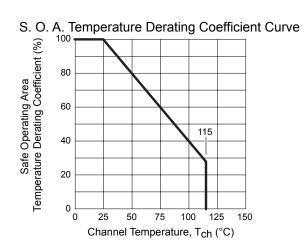
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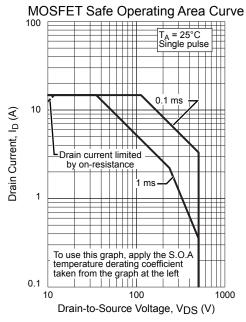
## Electrical Characteristics of MOSFET Unless specifically noted, $T_A$ = 25°C and $V_{CC}$ = 20 V

Characteristic	Symbol	Test Conditions	Pins	Min.	Тур.	Max.	Unit
Voltage Between Drain and Source	V <sub>DSS</sub>		1 – 2	500	-	-	V
Drain Leakage Current	I <sub>DSS</sub>		1 – 2	-	-	300	μA
On-Resistance	R <sub>DS(on)</sub>		1 – 2	-	-	0.8	Ω
Switching Time	t <sub>f</sub>		1 – 2	-	-	300	ns
Thermal Resistance	R <sub>θch-F</sub>	Between a channel of the MOSFET and the internal leadframe	_	_	2.4	2.7	°C/W

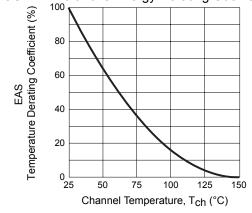
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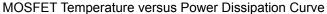
#### **Characteristic Performance**

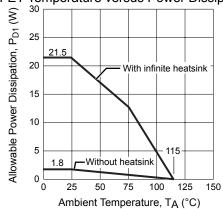


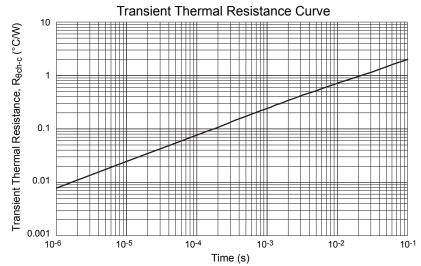


MOSFET Avalanche Energy Derating Coefficient Curve



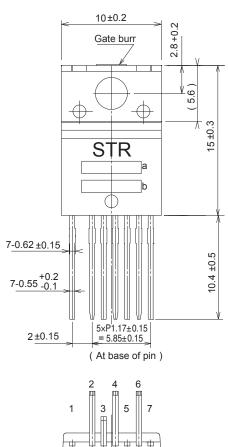


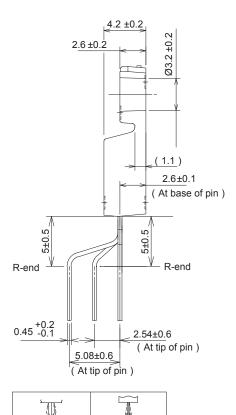


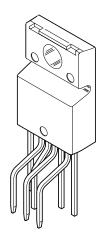


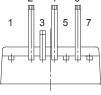
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### **Package Outline Drawing**









Unit: mm Package: TO-220F

(Sanken leadform #3051) Leadframe material: Cu Pin treatment: Solder dip Weight: Approximately 1.45 g "Gate Burr" shows area where 0.3 mm (max) gate burr may be present

a: Part # Y6735 b: Lot number 1 st letter: Last digit of year 2<sup>nd</sup>letter: Month Jan to September: Numeric October: O November: N December: D 3<sup>rd</sup> and 4<sup>th</sup> letter: Date 01 to 31: Numeric 5<sup>th</sup>letter: Internal use control number

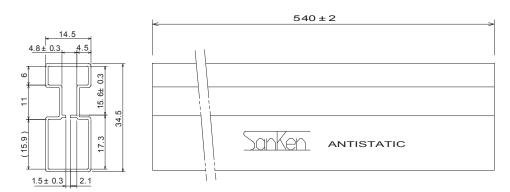
Side view

0.5

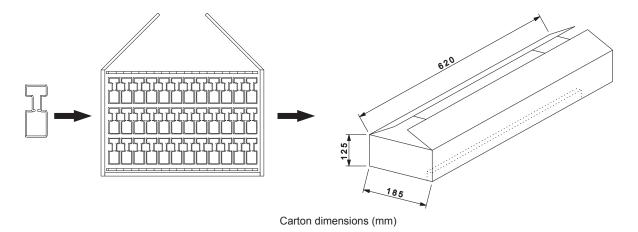
Front view

Pin treatment Pb-free. Device composition compliant with the RoHS directive.

### **Packing Specifications**



Tube dimensions (mm) 50 pieces per tube



36 tubes per carton (maximum) 1800 pieces maximum per carton

# Off-Line Quasi-Resonant Switching Regulator

Because reliability can be affected adversely by improper storage environments and handling methods, please observe the following cautions.

#### **Cautions for Storage**

- Ensure that storage conditions comply with the standard temperature (5°C to 35°C) and the standard relative humidity (around 40% to 75%); avoid storage locations that experience extreme changes in temperature or humidity.
- Avoid locations where dust or harmful gases are present and avoid direct sunlight.
- Reinspect for rust on leads and solderability of products that have been stored for a long time.

#### **Cautions for Testing and Handling**

When tests are carried out during inspection testing and other standard test periods, protect the products from power surges from the testing device, shorts between the product pins, and wrong connections.

#### Remarks About Using Silicone Grease with a Heatsink

- When silicone grease is used in mounting this product on a heatsink, it shall be applied evenly and thinly. If more silicone grease than required is applied, it may produce excess stress.
- Volatile-type silicone greases may crack after long periods of time, resulting in reduced heat radiation effect. Silicone grease with low consistency (hard grease) may cause cracks in the mold resin when screwing the product to a heatsink.
- Our recommended silicone greases for heat radiation purposes, which will not cause any adverse effect on the product life, are indicated below:

Туре	Suppliers
G746	Shin-Etsu Chemical Co., Ltd.
YG6260	MOMENTIVE Performance Materials, Inc.
SC102	Dow Corning Toray Co., Ltd.

#### **Heatsink Assembly**

 Attachment torque should be in the range 0.588 to 0.785 Nom (6 to 8 kgfocm).

#### Soldering

- The leadframe temperature should never exceed T<sub>F</sub>=105°C(max).
- When soldering the products, please be sure to minimize the working time, within the following limits:

260±5°C 10 s

350±5°C 3 s (solder iron)

 Soldering iron should be at a distance of at least 2.0 mm from the body of the products.

#### **Electrostatic Discharge**

- When handling the products, the operator must be grounded. Grounded wrist straps worn should have at least 1 M $\Omega$  of resistance from the operator to ground to prevent shock hazard, and it should be placed near the operator.
- Workbenches where the products are handled should be grounded and be provided with conductive table and floor mats.
- When using measuring equipment such as a curve tracer, the equipment should be grounded.
- When soldering the products, the head of soldering irons or the solder bath must be grounded in other to prevent leak voltages generated by them from being applied to the products.
- The products should always be stored and transported in Sanken shipping containers or conductive containers, or be wrapped in aluminum foil.

# Off-Line Quasi-Resonant Switching Regulator

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