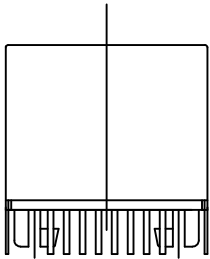
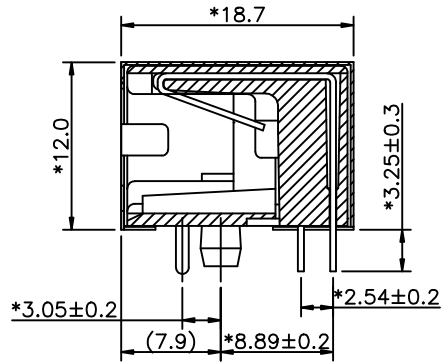
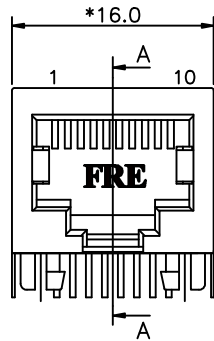
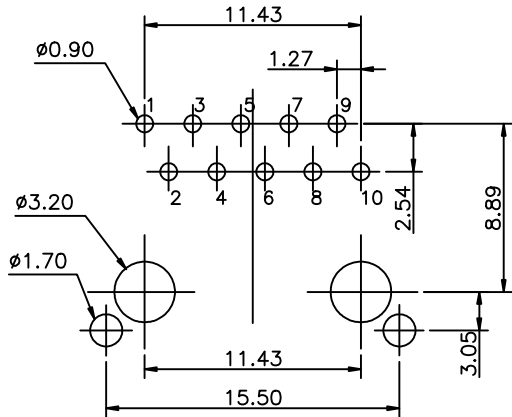
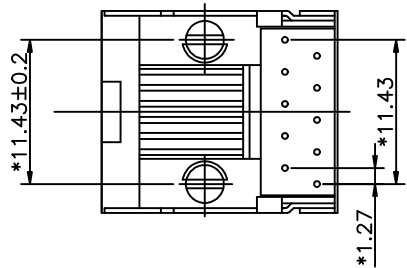


REVISION RECORD				
REV	ECO	DESCRIPTION	DRFT	CHKD



SECTION A-A



PC Board Layout Component Side Shown

NOTES:

ELECTRICAL:

1. VOLTAGE RATING : 125 VAC RMS.
2. CURRENT RATING : 1.5 AMP.
3. CONTACT RESISTANCE : 40 MILLIOHMS MAX.
4. INSULATION RESISTANCE: 1000 MEGOHMS MIN @ 500 VDC.
5. DIELECTRIC STRENGTH : 1000 VAC RMS 60Hz, 1MIN.

MECHANICAL:

1. HOUSING & INSERT MATERIAL : PBT+30%G.F UL94V-0.
2. CONTACT MATERIAL : PHOSPHOR BRONZE Ø0.46mm.
- CONTACT PLATING : GOLD PLATING OVER NICKEL.
2. SHIELD MATERIAL : 0.25mm THICKNESS BRASS C2680 H.
- SHIELD PLATING : SELECTIVE PLATED TIN-LEAD OVER NICKEL OR PLATED NICKEL
4. OPERATING LIFE : 750 CYCLES MIN.
5. PCB RETENTION PRE-SOLDER : 1 LB MIN.
6. PCB RETENTION POST-SOLDER: 10 LBS MIN.

ENVIRONMENTAL:

1. STORAGE : -40°C TO +85°C.
2. OPERATION: -40°C TO +85°C.
3. WAVE SOLDERING TEMPERATURE: 230~250°C (3~5 SECONDS)

MATES WITH MODULAR PLUG CONFORMING TO

FCC PART 68, SUBPART F.

CUL FILE NO. E163191

LEAD FREE PART NUMBER: E560X-4E006X-L

NO. OF CONTACT	1- 3u"	2- 6u"	F-GOLD FLASH
8- 8 CONTACTS	3-15u"	4-30u"	
1-10 CONTACTS	5-50u"	6-Pd 20u"	

← MM (INCH) →		DFTO QSJ	DATE 7/1/05	FULL RISE ELECTRONIC CO., LTD	
TOLERANCES EXCEPT AS NOTED		CHKD	DATE		
DETACHED LISTS	MM	MFO	DATE	TITLE	
	.0 ±0.20	APPVL XLF	DATE 07/04/05	11.5 SIDE ENTRY PCB JACK 10P	
	.00 ±0.15	MATERIAL :		DRAWING NO. GE563004-LC	
	.000 ±0.075	QT'Y :		/PART NO. SEE NOTE	
ANGLES ±		FINISH :		SIZE A3	REV 0
THIRD ANGLE PROJECTION		SCALE : 2.5:1		DO NOT SCALE DRAWING	
				SHEET X	OF Y