



2MHz, 3A Synchronous Step Down Converter

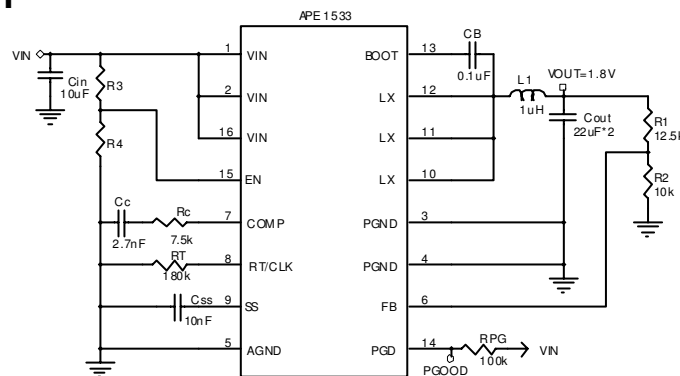
Features

- Two internal MOSFETs with typical $R_{ds(on)}$ of $50m\Omega$ for high efficiency at 3A loads
- Switching frequency from 200kHz to 2MHz
- Voltage reference of $0.803V \pm 1\%$
- Synchronizes to external clock from 300kHz to 2MHz
- Adjustable Slow Start/Sequencing
- UV and OV Power-Good Output
- Low Operating and Shutdown Quiescent Current
- Cycle-by-Cycle Current Limit, Thermal and Frequency Fold Back Protection
- RoHS-compliant and halogen-free (HF)

APPLICATIONS

- DSPs, FPGAs, ASIC, and Microprocessors
- I/O Supplies
- System Power Supplies

Typical Application



Ordering information

APE1533X-HF-3TR ← Packing Type:
 Package Type: ↑ TR: Tape and reel
 VN3: VQN3x3-16

Example:

APE1533VN3-HF-3TR : in RoHS-compliant, halogen-free VQN3x3, shipped on tape and reel (3000 pcs/reel).



Absolute Maximum Ratings (at $T_A = 25^\circ\text{C}$)

At the pin:

VIN	-0.3V to 6V
EN	-0.3V to 6V
LX	-0.3V to 6V
BOOT	LX+6V
RT/CLK	-0.3V to 6V
FB, SS	-0.3V to 6V
PGD	-0.3V to 6V
PGD Sink Current	10mA
GND, PGND	-0.3V to 0.3V
Storage Temperature Range (T_{ST})	-65 to +150 °C
Junction Temperature (T_J)	150 °C
Lead Temperature (Soldering, 10sec.)	260 °C
Thermal Resistance from Junction to Ambient (R_{thJA})	
QFN-16 (3mmX3mm)	60 °C/W

RECOMMENDED OPERATING CONDITIONS

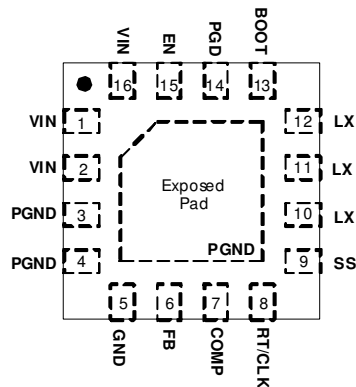
VIN	2.95V to 6V
EN	-0.3V to 6V
LX	-0.3V to 6V
Operating Temperature Range	-40 °C to 85 °C

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.
 USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.
 APEC DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED
 HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.
 APEC RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE
 RELIABILITY, FUNCTION OR DESIGN.



Pin Assignment

Top View
QFN 3x3-16L



Pin Functional Descriptions

PIN No.	PIN SYMBOL	PIN DESCRIPTION
1, 2, 16	VIN	Input supply voltage from 2.95V to 6V.
3, 4	PGND	Power ground.
5	GND	Analog ground.
6	FB	Output feedback pin.
7	COMP	Compensation pin. Connect frequency compensation components at this pin.
8	RT/CLK	Resistor timing or external clock input pin.
9	SS	Soft-start pin. Connect an external capacitor to adjust the output rise time.
10, 11, 12	LX	Switching node.
13	BOOT	Supply input for internal high-side N-MOSFET gate drive (boot terminal). Connect a bootstrap capacitor from this pin to LX node.
14	PGD	Power good output pin. PGD is an open-drain output. Pull up to VIN rail with a pull-up resistor.
15	EN	Enable pin, internal pull-up current source.
17	Exposed pad	Connect to power ground directly.



Electrical Characteristics

($V_{IN}=2.95$ to $6V$, $T_A = 25^\circ C$, unless otherwise specified)

PARAMETER	SYM	TEST CONDITION	MIN	TYP	MAX	UNIT
Input						
Operation Voltage Range	V_N		2.95		6	V
Under Voltage Lockout Threshold	UVLO	Rising		2.6	2.8	V
		hysteresis		200		mV
Quiescent Current	I_Q	$V_{IN}=5V$, $V_{FB}=0.9V$, $RT=400k\Omega$		400		μA
Shutdown Current	I_{SD}	$EN=0V$, $0.95V \leq V_{IN} \leq 6V$		3		μA
EN Threshold	V_{EN}	Rising		1.25		V
		Falling		1.18		V
EN Input Current	I_{EN}	$V_{EN} + 50mV$		-3.2		μA
		$V_{EN} - 50mV$		-0.65		μA
Reference Voltage	V_{FB}	$V_{IN}=2.95$ to $6V$	0.795	0.803	0.811	V
Controller						
High Side Switch Resistance ^(Note1)	R_{DRVH}	BOOT-LX=5V		50		m Ω
		BOOT-LX=2.95V		64		m Ω
Low Side Switch Resistance ^(Note1)	R_{DRVL}	$V_{IN}=5V$		50		m Ω
		$V_{IN}=2.95V$		64		m Ω
Switching Current Limit	I_{LM}		5			A
LX Rise/Fall Time ^(Note1)		$V_{IN}=5V$		1.5		V/ns
BOOT Charge Resistance		$V_{IN}=5V$		16		Ω
Error Amplifier						
COMP Leakage Current				7		nA
EA Transconductance ^(Note1)	gm	$I_{COMP} = \pm 2\mu A$, $V_{COMP}=1V$		225		$\mu A/V$
COMP Sink/Source Current		$V_{COMP}=1V$, 0.1V overdrive		± 20		μA
Current Sense to COMP Transconductance ^(Note1)	gm_{CS}			13		A/V



Electrical Characteristics

(T_A =25 °C, unless otherwise specified)

PARAMETER	SYM	TEST CONDITION	MIN	TYP	MAX	UNIT
Resistor Timing and External Clock (RT/CLK)						
Switching Frequency Range		RT mode	200		2000	kHz
		CLK mode	300		2000	kHz
Switching Frequency	fsw	RT=400kΩ	400	500	600	kHz
Minimum CLK ON Time	T _{CLK_MIN}			75		ns
RT/CLK Voltage	V _{RT/CLK}	RT=400kΩ		0.5		V
RT/CLK Threshold		High		1.6	2.2	V
		Low	0.4	0.6		V
Delay Time ^(Note1)	t _D	RT/CLK falling to LX rising edge, fsw=500kHz, with RT resistor		150		ns

Soft Start (SS)

SS Charge Current	I _{SS}	V _{SS} =0.4V		1.8		uA
SS Discharge Current	I _{SS-D}	UVLO, EN, T _{thermal} fault, V _{IN} =5V, V _{SS} =0.5V		1.25		mA
		Over-current, V _{FB} =0V		20		uA

Power Good (PGD)

FB Threshold (Good)	V _{FB-R1}	Rising		93		%V _{FB}
	V _{FB-F1}	Falling		105		%V _{FB}
FB Threshold (Fault)	V _{FB-R2}	Rising		91		%V _{FB}
	V _{FB-F2}	Falling		107		%V _{FB}
PGD Leakage Current	I _{PGD}	V _{FB} =V _{REF} , V _{PGD} =5.5V		0.1		uA
PGD On Resistance ^(Note1)	R _{PGD}			10		Ω
PGD Output Low		I _{PGD} =3.5mA		0.3		V
Minimum VIN for Valid PGD Output		V _{PGD} <0.5V at 100uA		1.2		V

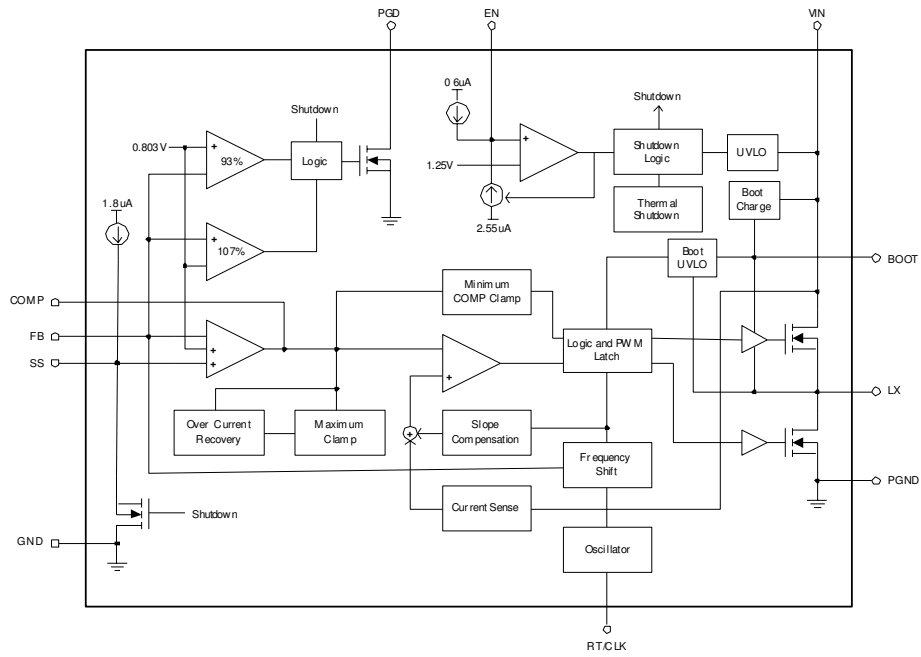
Thermal Shutdown

Thermal Shutdown Threshold ^(Note1)	T _{SD}			175		°C
		Hysteresis		15		°C

Note1: Guarantee by design, not production tested.



Block Diagram





Typical Characteristics

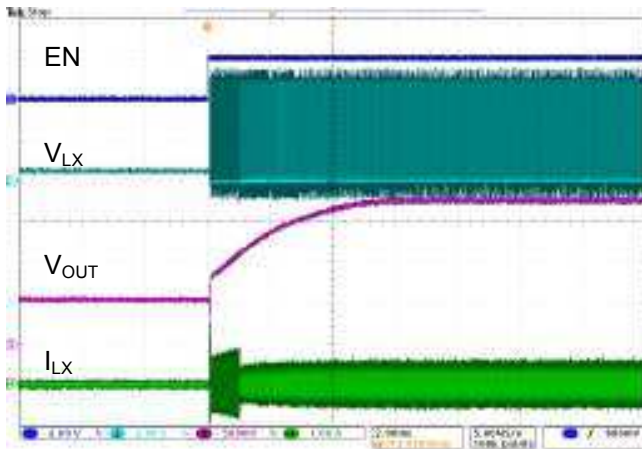


Fig.1 Enable, 1 Vo/0A

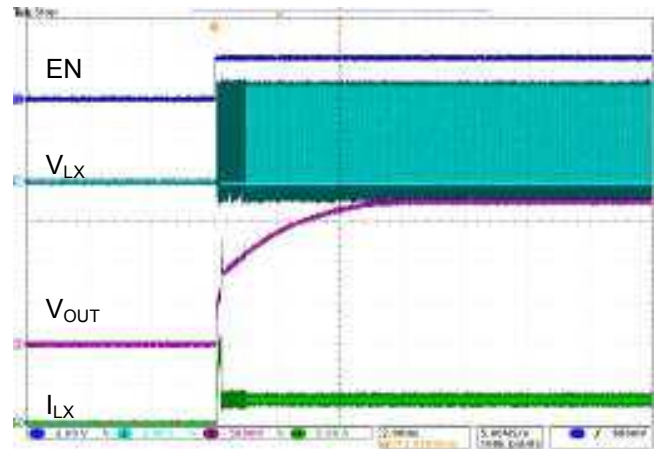


Fig.2 Enable, i_i to 1.8Vo/3A

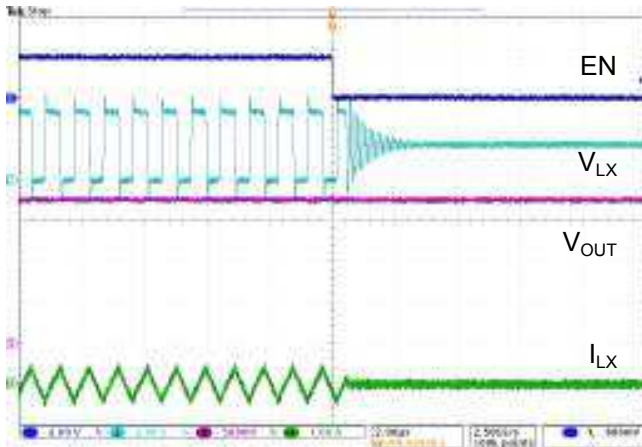


Fig.3 Disable, i_i Vo/0A

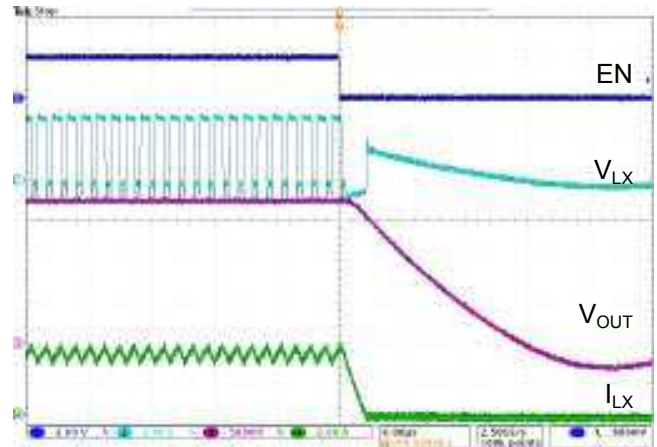


Fig.4 Disable, i_i to 1.8Vo/3A

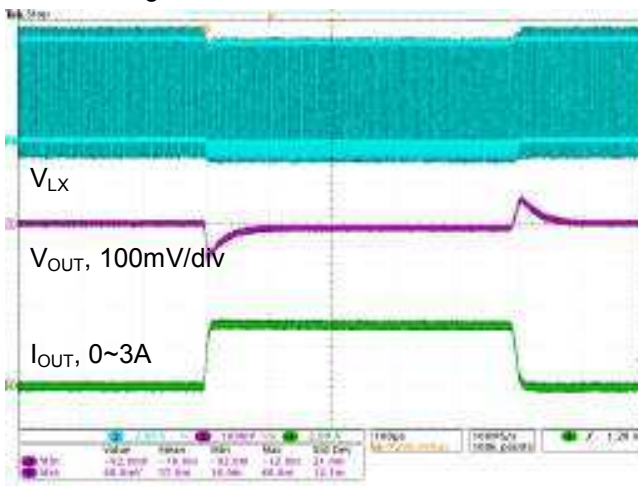


Fig.5 Load Transient, V, Vo=1.8V

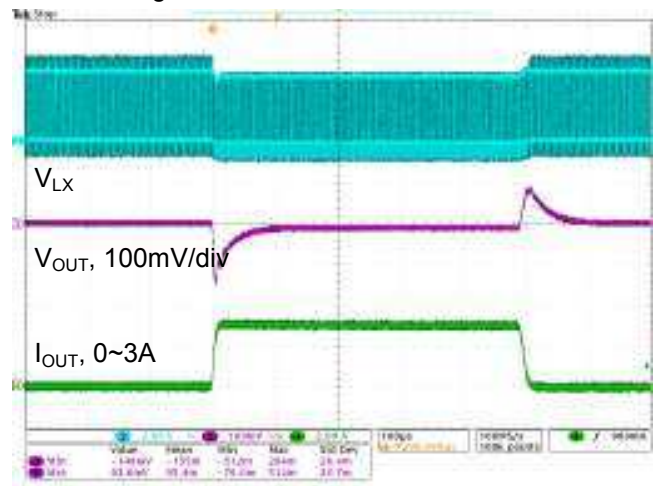


Fig.6 Load T, Vin=3.3V, Vo=1.8V



Typical Characteristics

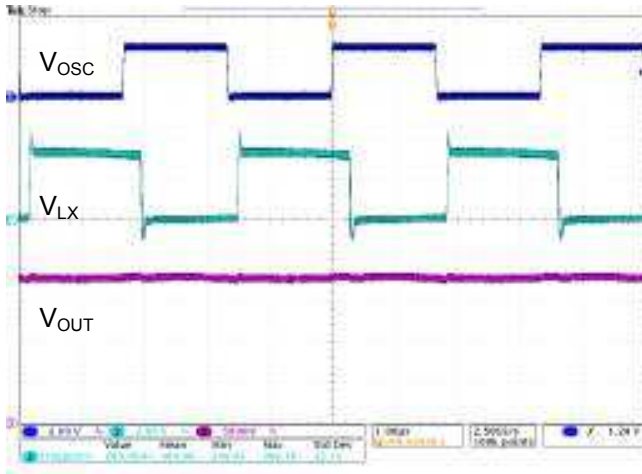


Fig.7 External PWM at RT pin, fosc=300kHz

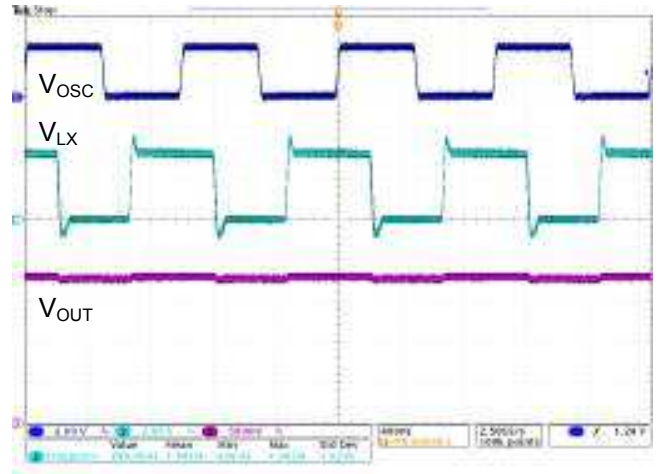


Fig.8 M a : RT pin, fosc=1MHz

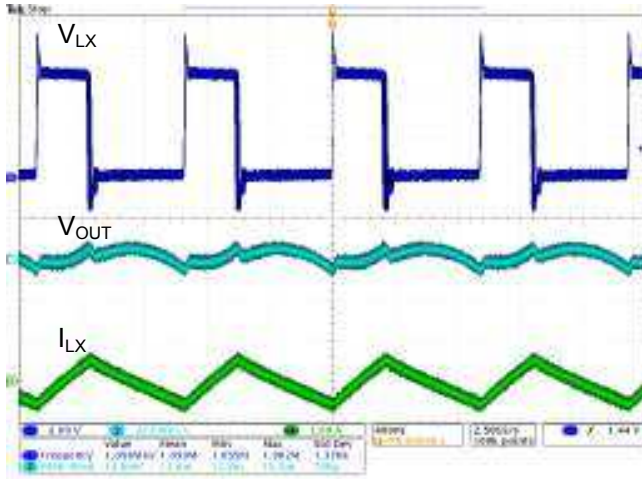


Fig.9 Output R $i = 1.8V, I_o = 0A$

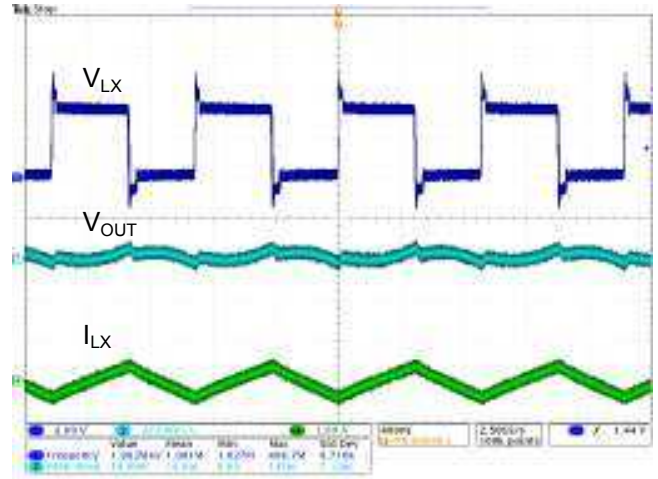


Fig.10 Output Ripple, $= 3.3V, V_o = 1.8V, I_o = 0A$

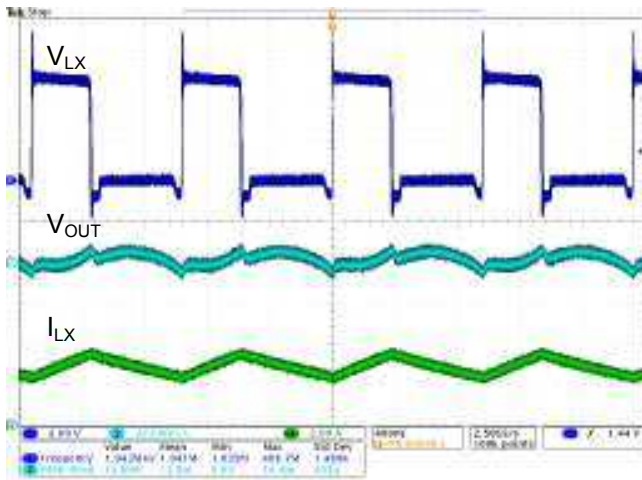


Fig.11 Output Ripple, $i = 1.8V, I_o = 3A$

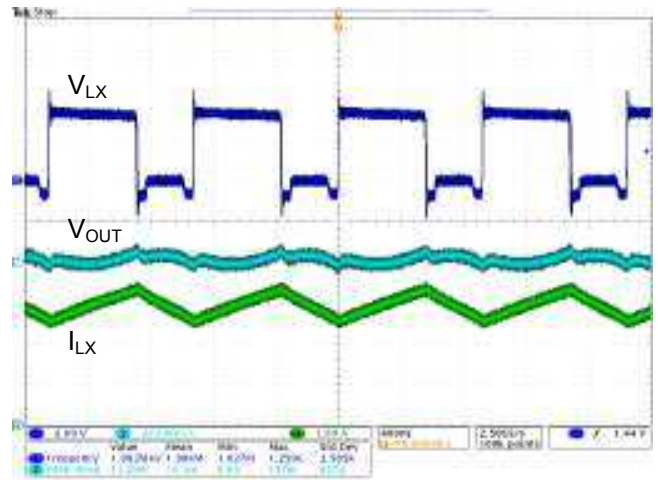


Fig.12 Output Ripple, $= 3.3V, V_o = 1.8V, I_o = 3A$



Typical Characteristics

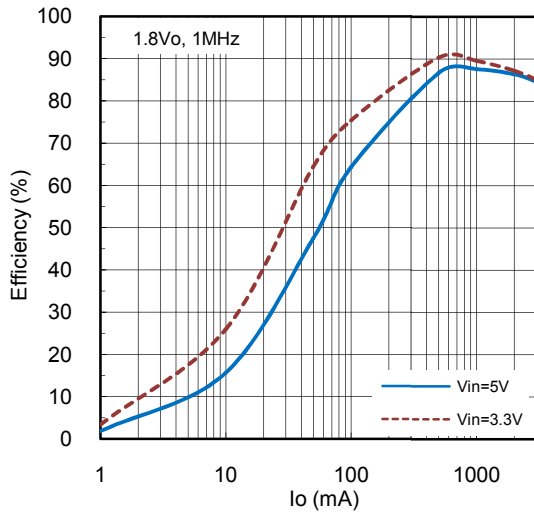


Fig.13 Efficiency vs. I_o for $V_o=1.8V$

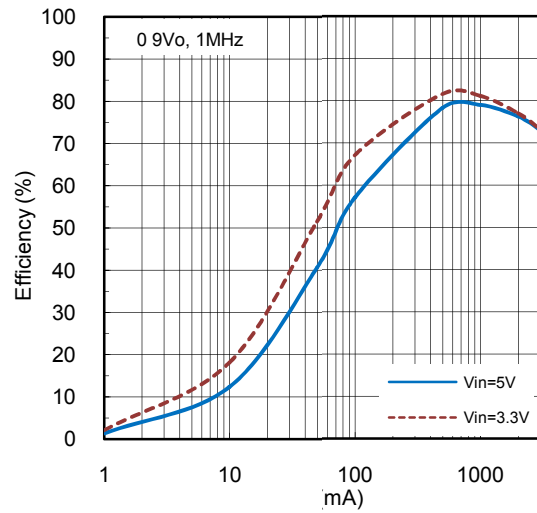


Fig.14 Efficiency vs. I_o for $V_o=0.9V$

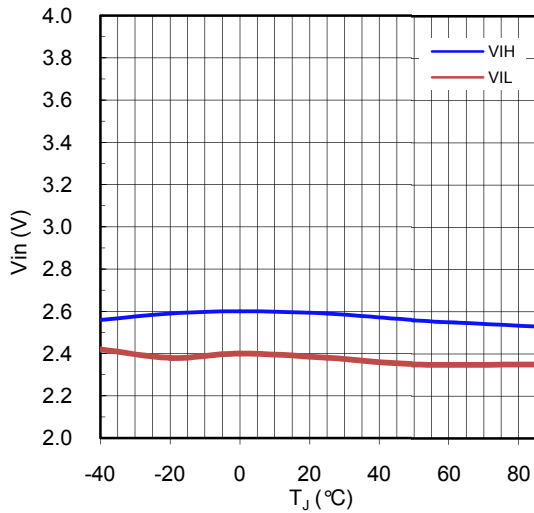


Fig.15 UVLO Threshold vs. Temperature

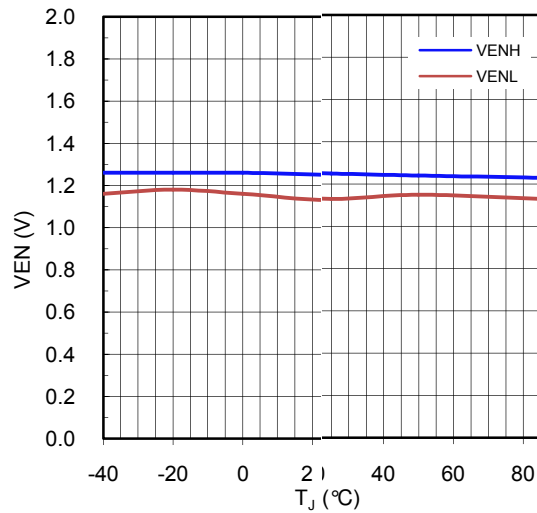


Fig.16 EN Threshold vs. Temperature

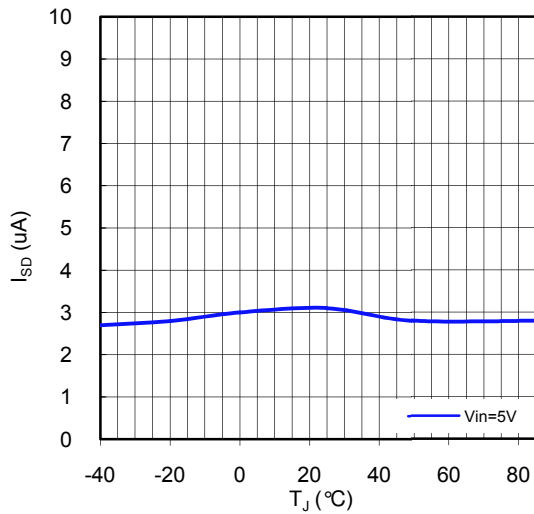


Fig.17 Shutdown Current vs. Temperature

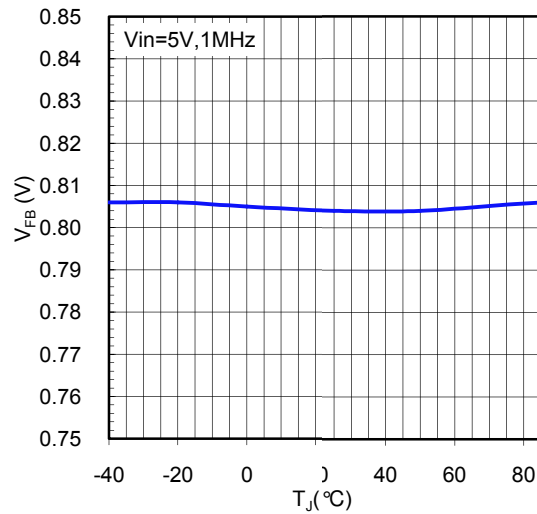
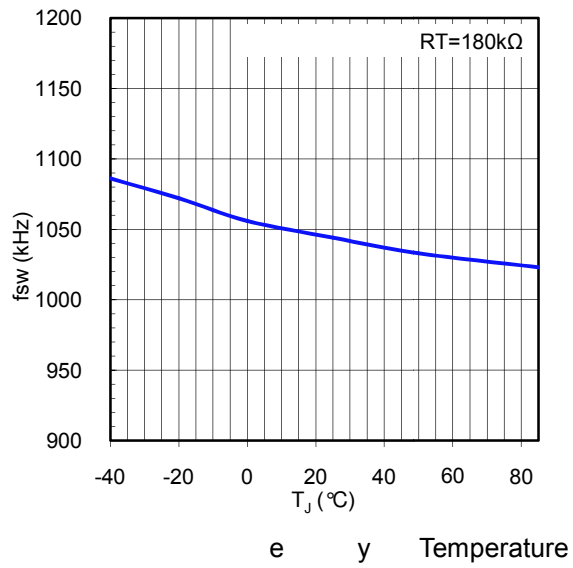


Fig.18 Feedback Voltage vs. Temperature

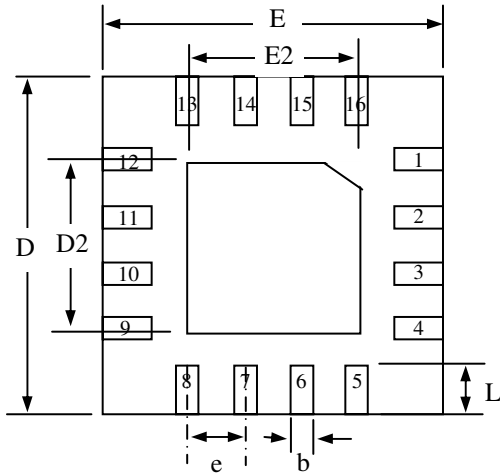


Typical Characteristics

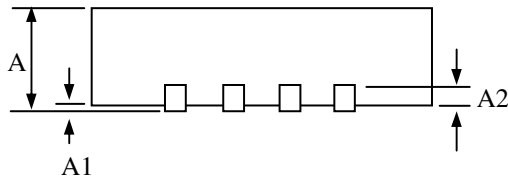




Package Dimensions: VQN3x3-16



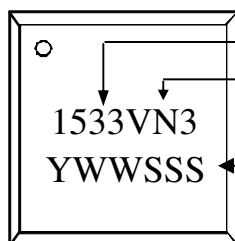
BOTTOM VIEW



SYMBOLS	Millimeters		
	MIN	NOM	MAX
A	0.75	0.85	1.00
A1	0.00	0.02	0.05
A2	0.175	0.200	0.250
b	0.18	0.23	0.30
D	2.95	3.00	3.05
D2	1.50	1.55	1.60
E	2.95	3.00	3.05
E2	1.50	1.55	1.60
e	0.50 (ref.)		
L	0.35	0.40	0.45

1. All dimensions are in millimeters.
2. Dimensions do not include mold protrusions.

Marking Information:



Product : APE1533

Package:

VN3 = RoHS-compliant halogen-free VQN3x3

Date/lot code (YWWSSS)

Y: Last digit of the year

WW: Work week

SSS: Lot code sequence