



# Solid State Devices, Inc.

14701 Firestone Blvd \* La Mirada, Ca 90638  
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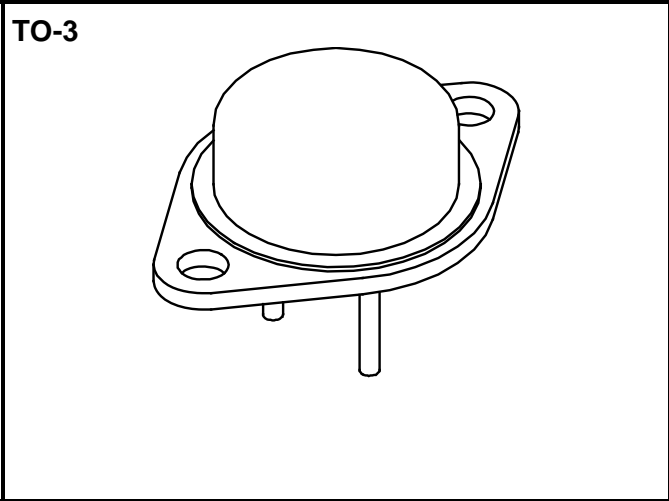
**SSR4045CT/3**

## Designer's Data Sheet

### FEATURES:

- PIV: 45 Volts
- Low Forward Voltage Drop
- Low Reverse Leakage
- Hermetically Sealed Package
- Guard Ring for Overvoltage Protection
- Gold Eutectic Die Attach
- 175°C Operating Junction Temperature
- Also Available in the following Configurations:  
Common Anode- SSR4045CA/3  
Doubler- SSR4045D/3
- TX, TXV, and Space Level Screening Available

**40 AMPS  
45 VOLTS  
POSITIVE CENTERTAP  
SCHOTTKY  
RECTIFIER**



MAXIMUM RATINGS		Symbol	Value	Units
Peak Repetitive Reverse Voltage and DC Blocking Voltage	SSR4045CT/3	$V_{RRM}$ $V_{RWM}$ $V_R$	45	Volts
Average Rectified Forward Current <sup>1/</sup> (Resistive Load, 60 Hz, Sine Wave, $T_A=25^\circ\text{C}$ )		$I_O$	40	Amps
Peak Surge Current <sup>1/</sup> (8.3 ms Pulse, Half Sine Wave Superimposed on $I_O$ , allow junction to reach equilibrium between pulses, $T_A=25^\circ\text{C}$ )		$I_{FSM}$	600	Amps
Operating and Storage Temperature		$T_{OP}$ & $T_{stg}$	-65 to +175	°C
Maximum Thermal Resistance <sup>1/</sup> Junction to Case		$R_{\theta JC}$	0.9	°C/W

Notes:

<sup>1/</sup> Both Legs Tied Together. (Doubler Per Leg:  $I_O = 20\text{A}$ ,  $I_{FSM} = 300\text{A}$ ,  $R_{\theta JC} = 1.8^\circ\text{C/W}$ )

<b>NOTE:</b> All specifications are subject to change without notification. SCD's for these devices should be reviewed by SSDI prior to release.	<b>DATA SHEET #: RS0025B</b>	<b>DOC</b>
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**SSR4045CT/3**

ELECTRICAL CHARACTERISTICS (Per Leg)	Symbol	Max	Unit
<b>Instantaneous Forward Voltage Drop</b> ( $T_A = 25^\circ\text{C}$ , Pulse)	$I_F = 5\text{Amps}$	$V_{F1}$	0.50
	$I_F = 10\text{Amps}$	$V_{F2}$	0.56
	$I_F = 20\text{Amps}$	$V_{F3}$	0.69
<b>Instantaneous Forward Voltage Drop</b> ( $I_F = 10\text{Amps}$ , $T_A = -55^\circ\text{C}$ , Pulse)		$V_{F4}$	0.63
<b>Reverse Leakage Current</b> (Rated $V_R$ , $T_A = 25^\circ\text{C}$ , Pulse)		$I_{R1}$	200 $\mu\text{A}$
<b>Reverse Leakage Current</b> (Rated $V_R$ , $T_A = 100^\circ\text{C}$ , Pulse)		$I_{R2}$	15 $\text{mA}$
<b>Junction Capacitance</b> ( $V_R = 10\text{V}_{\text{DC}}$ , $T_A = 25^\circ\text{C}$ , $f = 1\text{MHz}$ )		$C_J$	800 $\text{pF}$

**CASE OUTLINE:  
TO-3**

**PIN OUT: SSR4045CT/3**  
(Common Cathode)  
CASE: CATHODE  
PIN 1: ANODE 1  
PIN 2: ANODE 2

**PIN OUT: SSR4045CA/3**  
(Common Anode)  
CASE: ANODE  
PIN 1: CATHODE 1  
PIN 2: CATHODE 2

**PIN OUT: SSR4045D/3**  
(Doubler)  
CASE: AC  
PIN 1: ANODE  
PIN 2: CATHODE

The drawing shows a TO-3 case with the following dimensions:  
 - Top view: 2x  $\varnothing .165$  holes, .525 MAX width, .675 / .655 length, 2x R.188 MAX fillets, 1.197 / 1.177 width.  
 - Side view: .135 MAX height, SEATING PLANE, 2x  $\varnothing .043$  / .038 holes,  $\varnothing .875$  MAX diameter, .450 / .250 base dimensions, 2x .312 MIN spacing.