



PRELIMINARY

SOLID STATE DEVICES, INC

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SSR8045N

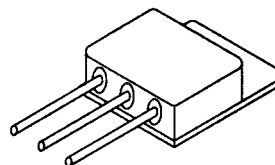
Designer's Data Sheet

FEATURES:

- Low Reverse Leakage
- Low Forward Voltage Drop
- Guard Ring for Overvoltage protection
- Isolated Hermetically Sealed Power Package
- Available with Ceramic Seals
- Custom Lead Forming Available
- Eutectic Die Attach
- 175°C Operating Temperature
- TX, TXV and Space Level Screening Available

80 AMP
45 VOLTS
SCHOTTKY
RECTIFIER

TO-258



MAXIMUM RATINGS

RATING	SYMBOL	VALUE	UNIT
Peak Repetitive Reverse and DC Blocking Voltage	VRRM VRWM VR	45	Volts
Average Rectified Forward Current (Resistive Load, 60Hz, Sine Wave, TA=25°C)	IO	80	Amps
Peak Surge Current (8.3 ms Pulse, Half Sine Wave Superimposed on IO, allow junction to reach equilibrium between pulses, TA=25°C)	IFSM	1200	Amps
Operating and storage temperature	Top & Tstg	-65 to +175	°C
Maximum Thermal Resistance Junction to Case	RθJC	0.5	°C/W

NOTE: All specifications are subject to change without notification.
SCD's for these devices should be reviewed by SSDI prior to release.

DATA SHEET#: RS0053 A

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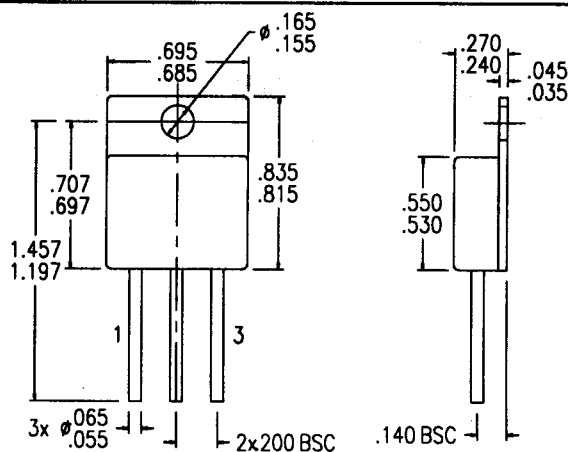
ELECTRICAL CHARACTERISTICS

CHARACTERISTICS	SYMBOL	MAXIMUM	UNIT
Instantaneous Forward Voltage Drop (IF = 20 Adc, TA=25°C, 300μs Pulse) (IF = 40 Adc, TA=25°C, 300μs Pulse) (IF = 80 Adc, TA=25°C, 300μs Pulse)	VF1	0.55 0.70 0.80	Vdc
Instantaneous Forward Voltage Drop (IF = 40 Adc, TA= - 55°C, 300μs Pulse)	VF2	0.77	Vdc
Reverse Leakage Current (Rated VR, TA=25°C, 300μs pulse minimum)	IR1	800	μA
Reverse Leakage Current (Rated VR, TA=100°C, 300μs pulse minimum)	IR2	60	mA
Junction Capacitance (VR = 10 Vdc, TA=25°C, f= 1 MHz)	CJ	3200	pf

CASE OUTLINE: TO-258

PIN 1: CATHODE
PIN 2: ANODE
PIN 3: ANODE

NOTE:
Pin 2 and 3 should be
connected in application.



TYPICAL OPERATING CURVES

(TA=25°C unless otherwise specified)

