

14701 Firestone Blvd * La Mirada, Ca 90638 Phone: (562) 404-4474 * Fax: (562) 404-1773 ssdi@ssdi-power.com * www.ssdi-power.com

Designer's Data Sheet Part Number/Ordering Information $^{1/}$ SSR4045 CT M Screening 2/ = Not Screened TX = TX LevelTXV = TXV Level = S Level Lead Bend: = Straight $\overline{\text{UB}} = \text{Up Bend}$ DB = Down Bend = TO-254L Package: = TO-254Z**Configuration:** CT = Common Cathode CA = Common Anode D = Doubler

SSR4045CTM SSR4045CTZ

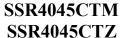
40 AMP, 45 VOLTS POSITIVE CENTERTAP SCHOTTKY RECTIFIER

FEATURES:

- Low Forward Voltage Drop
- Low Reverse Leakage
- Guard Ring for Overvoltage Protection
- Isolated Hermetically Sealed Power Package
- Available in Glass or Ceramic Seal Packages
- Eutectic Die Attach
- 175°C Operating Temperature
- TX, TXV, or Space Level Screening Available

MAXIMUM RATINGS			-
RATING	SYMBOL	VALUE	UNIT
Peak Repetitive Reverse and DC Blocking Voltage	$egin{array}{c} V_{RRM} \ V_{RWM} \ V_{R} \end{array}$	45	Volts
Average Rectified Forward Current			
(Resistive Load, 60Hz, Sine Wave, TC=55°C) <u>3</u> /	I_0	40	Amps
Peak Surge Current (8.3 ms Pulse, Half Sine Wave, TA=25°C) 3/	I _{FSM}	600	Amps
Operating and Storage Temperature	T _{OP} & T _{STG}	-65 to +175	°C
Maximum Thermal Resistance Junction to Case 4/	$R_{ heta JC}$	1.0	°C/W
Junction to Case ⁵ /		2.0	

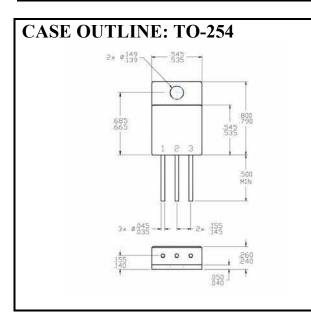
NOTES: 1/ For ordering information, price, and availability, contact factory.	TO-254	TO-254Z
2/ Screening per MIL-PRF-19500 3/ Both legs tied together. For doubler configuration, value shall be half of the value shown, tested per leg		
4/ Both legs tied together. For doubler configuration, both dies tested in series 5/ Per leg		

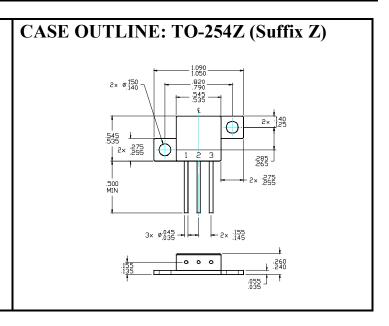




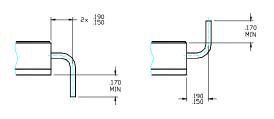
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ELECTRICAL CHARACTERISTICS 5/				
CHARACTERISTICS		SYMBOL	MAXIMUM	UNIT
I	$I_{F} = 5A$ $I_{F} = 10A$ $I_{F} = 20A$	$\begin{array}{c} V_{F1} \\ V_{F2} \\ V_{F3} \end{array}$	0.52 0.60 0.77	Vdc
Instantaneous Forward Voltage Drop $(I_F = 10A, T_A = -55^{\circ}C, 300-500 \mu sec Pulse)$ Reverse Leakage Current		$ m V_{F4}$ $ m I_{R1}$	0.67 200	Vdc μA
Reverse Leakage Current (Rated V _R , T _A = 25°C, 300 µsec Pulse Minimulation (Rated V _R , T _A = 100°C, 300 µsec Pulse Minimulation)		I _{R1}	15	mΑ
Junction Capacitance (V _R = 10 V, f = 1MHz, T _A = 25°C)		C _J	800	pF





OPTIONAL LEAD BEND CONFIGURATION



Pin Assignment:

Code	Function	Pin 1	Pin 2	Pin 3
CT	Common Cathode	Anode	Cathode	Anode
CA	Common Anode	Cathode	Anode	Cathode
D	Doubler	Cathode	AC	Anode

NOTE:	All specifications are subject to change without notification.
	or these devices should be reviewed by SSDI prior to release.

SUFFIX MDB & ZDB SUFFIX MUB & ZUB